

Ultra Low Noise, Fast Response, High PSRR 300/500mA Low Dropout Linear Regulator

General Description

The uP8806P/Q is an ultra low noise, fast response, high power supply rejection ratio (PSRR) low dropout regulator specifically designed to continuously deliver up to 300mA output current. Designed with a P-channel MOSFET series pass transistor, the uP8806P/Q yields extremely low dropout voltage (e.g. 300mV at 300mA).

The uP8806P/Q does not require a bypass capacitor, hence achieving the smallest PCB area. This device reduces output voltage noise under $70\mu V_{(RMS)}$ and improves PSRR to 40dB at 100kHz.

The uP8806P/Q is designed and optimized to work with low-value, low-cost ceramic capacitors. Only a 1uF ceramic output capacitor is required for stable operation for any load conditions.

Other features include foldback overcurrent protection, quick soft start, and overtemperature protection. The uP8806P/Q is available in fixed output voltage from 0.8V to 4.75V with 0.1V per step. The device comes in (T)SOT23-3L/5L, SOT223-3L, SOT89-3L and TO252-3L packages.

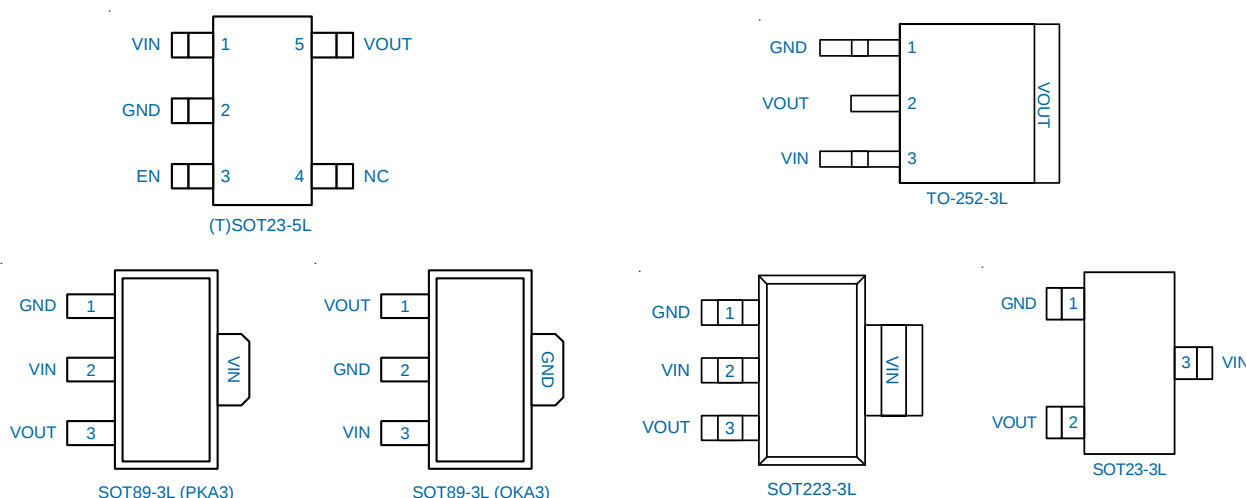
Features

- Wide Input Voltage Range from 2.4 to 6V
- Ultra Low Dropout Voltage: 300mV @ 300mA
- High Power Supply Rejection Ratio Up to 74dB @1kHz
- Ultra Fast Response in Line/Load Transient
- Stable with 1uF Ceramic Output Capacitor
- Low Shutdown Current: < 1uA
- Foldback Output Current Limit
- High Output Accuracy
 - 1.5% Initial Accuracy
 - Fixed Output Voltages: 0.8V to 4.75V with 0.1V Per Step
- Over-Temperature Protection
- RoHS Compliant and Halogen Free

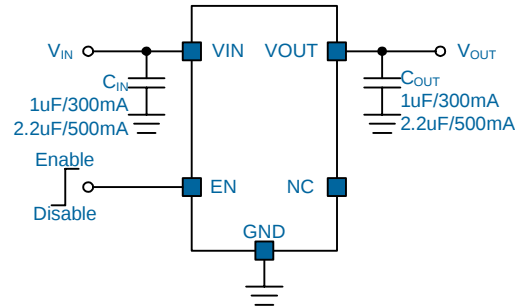
Applications

- Cellular and Cordless Phones
- Bluetooth Portable Radios and Accessories
- Battery-Powered Equipment
- Laptop, Palmtops, Notebook Computers
- Hand-Held Instruments
- PCMCIA Cards
- Portable Information Appliances

Pin Configuration



Typical Application Circuit



Ordering Information

Order Number	Package	Top Marking	Remark
uP8806PDD6-XX	WDFN1.6x1.6-6L	TBD	OCP: 450mA / Output Voltage: +/- 1.5%
uP8806PMA3-XX	SOT23-3L	S98PXX	OCP: 450mA / Output Voltage: +/- 1.5%
uP8806PMA5-XX	SOT23-5L	S99PXX	OCP: 450mA / Output Voltage: +/- 1.5%
uP8806QMA5-XX		S99QXX	OCP: 700mA / Output Voltage: +/- 1.5%
uP8806PMT5-XX	TSOT23-5L	N65PXX	OCP: 450mA / Output Voltage: +/- 1.5%
uP8806QJA3-XX	SOT223-3L	SA1QXX	OCP: 700mA / Output Voltage: +/- 1.5%
uP8806PKA3-XX	SOT89-3L	SA2PXX	OCP: 450mA / Output Voltage: +/- 1.5%
uP8806QKA3-XX		SA2QXX	OCP: 700mA / Output Voltage: +/- 1.5%
uP8806QHA3-XX	TO252-3L	SA3QXX	OCP: 700mA / Output Voltage: +/- 1.5%

XX: Output Voltage
10: 1.0V; 12: 1.2V; 15: 1.5V; 18: 1.8V; 25: 2.5V; 27: 2.7V; 28: 2.8V; 30: 3.0V; 31: 3.1V; 33: 3.3V; 46: 4.6V
C5 = 1.25V, 13 = 1.3V, 17 = 1.7V, 20 = 2.0V, 32 = 3.2V

Note:

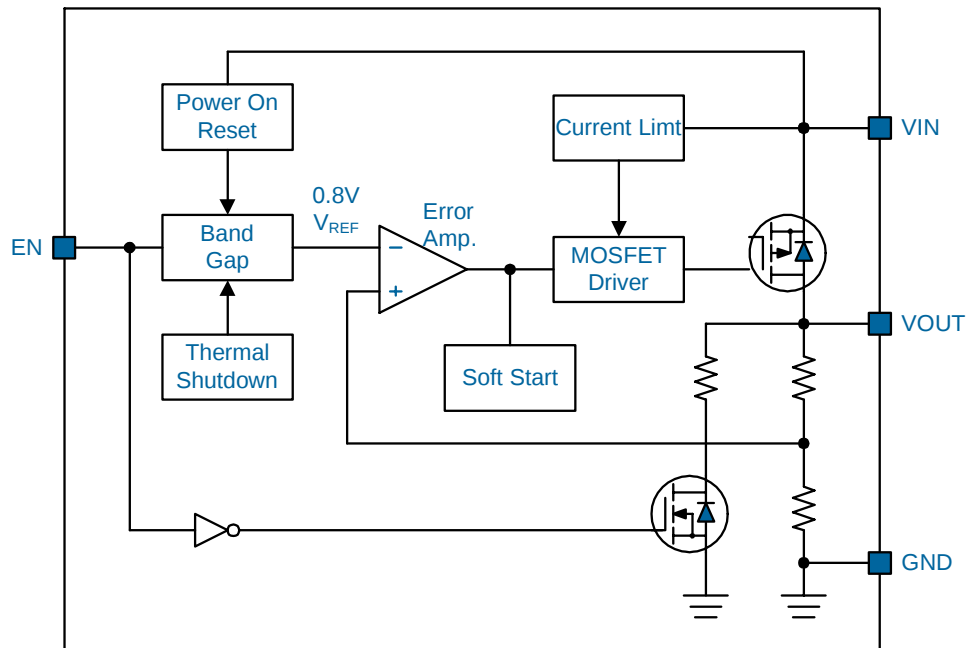
(1) Please check the sample/production availability with uPI representatives.

(2) uPI products are compatible with the current IPC/JEDEC J-STD-020 requirement. They are halogen-free, RoHS compliant and 100% matte tin (Sn) plating that are suitable for use in SnPb or Pb-free soldering processes.

Functional Pin Description

Name	Pin Function
VIN	Input Voltage. This pin connects to the source of the internal pass transistor that supplies current to the output pin. Bypass VIN to GND with a minimum 1uF ceramic capacitor. Place the decoupling capacitor physically as close as possible to the device.
GND	Ground.
EN	Enable Input. Pulling this pin below 0.4V turns the regulator off, reducing the quiescent current to a fraction of its operating value. Pull this pin higher than 1.4V for normal operation.
NC	Not Internally Connected.
VOUT	Output Voltage. This pin is power output of the device. Bypass this pin with a minimum 1uF ceramic capacitor.

Functional Block Diagram



Functional Description

Definitions

Some important terminologies for LDO are specified below.

Dropout Voltage

The input/output voltage differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output drops 2% below its nominal value, dropout voltage is affected by junction temperature, load current and minimum input supply requirements.

Line Regulation

The change in output voltage for a change in input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that average chip temperature is not significantly affected.

Load Regulation

The change in output voltage for a change in load current at constant chip temperature. The measurement is made under conditions of low dissipation or by using pulse techniques such that average chip temperature is not significantly affected.

Maximum Power Dissipation

The maximum total device dissipation for which the regulator will operate within specifications.

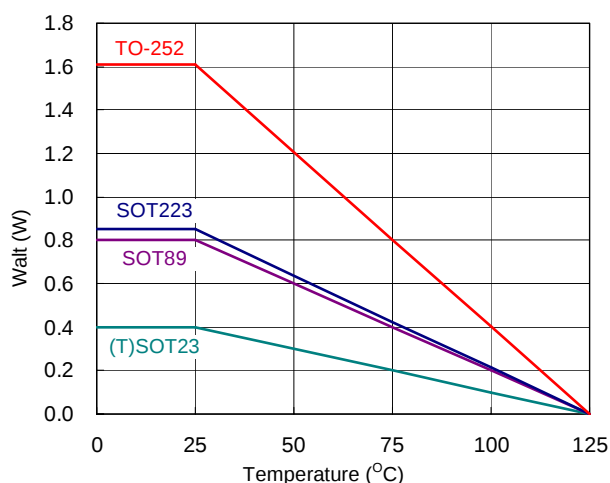


Figure 1. Power Dissipation vs. Temperature

Quiescent Bias Current

Current which is used to operate the regulator chip and is not delivered to the load. The quiescent current I_Q is defined as the supply current used by the regulator itself that does not pass into the load. It typically includes all bias currents required by the LDO and any drive current for the pass transistor.

The uP8806P/Q is an ultra low noise, fast response, high power supply rejection ratio (PSRR) low dropout regulator specifically designed to continuously deliver up to 300mA output current for space-limited applications. Designed with a P-channel MOSFET series pass transistor, the uP8806P/Q yields extremely low dropout voltage (e.g. 150mV at 150mA and 300mV at 300mA). The uP8806P/Q does not require a bypass capacitor, hence achieving the smallest PCB area. This device reduces output voltage noise under 70uV_(RMS) and improves PSRR to 40dB at 100kHz. The uP8806P/Q is designed and optimized to work with low-value, low-cost ceramic capacitors. Only a 1uF ceramic output capacitor is required for stable operation for any load conditions. Other features include foldback overcurrent protection, quick soft start, and overtemperature protection. The uP8806P/Q is available in adjustable and fixed output voltages from 0.8V to 4.75V with 0.1V increments.

As shown in the *Functional Block Diagram*, the uP8806P/Q consists of a bandgap for reference voltage, error amplifier, P-channel MOSFET pass transistor and internal feedback voltage divider. The 0.8V bandgap reference voltage is connected to the inverting input of error amplifier. The error amplifier compares this reference voltage with the feedback voltage and amplifies the difference. If the feedback voltage is lower than the reference voltage, the pass-transistor gate is pulled low. This allows more current to pass to the output and increases the output voltage. If the feedback voltage is too high, the pass transistor gate is pulled high, allowing less current to pass to the output. The output voltage is fed back through an internal resistor voltage-divider connected to the VOUT pin. Additional blocks include a current limiter, thermal sensor, and shutdown logic.

Supply Input Power On Reset

The input voltage supplies current to the output voltage and supplies current for control circuit. The input voltage is monitored for power on reset (POR) to ensure the regulator is not enabled until the input voltage is high enough for normal operation. The POR threshold level is typical 1.9V at V_{IN} rising.

Enable/Shutdown

The uP8806P/Q features an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin lower than 0.4V shuts down the regulator and reduces its quiescent current less than 1uA. The voltage reference, error amplifier, gate-driver circuit and pass transistor are disabled in the shutdown state.

Forcing the enable pin higher than 1.4V enables the output voltage (once the input voltage is higher than its POR threshold level). If the enable function is not needed in a specific application, it may be tied to VIN to keep the regulator in an always on state. The enable pin uses CMOS technology and cannot be left floating, as this may cause an indeterminate state on the output.

Current Limit and Short-Circuit Protection

The uP8806P/Q includes a current limiter that monitors and controls the gate voltage of pass transistor to limit the output current to 450mA(uP8806P) or 700mA(uP8806Q) typically. A short circuit protector monitors the output voltage and asserts output short circuit if V_{OUT} is lower than 40% of V_{NOM} . The current limiting level is reduced to around 110mA and overtemperature threshold level is lowered to 100°C when output short circuit occurs. This limits the junction temperature to a safe level and allows the output to be shorted to ground for an indefinite duration without damaging the device. The output voltage is rebuilt after short circuit is removed.

Over Temperature Protection

The over temperature protection limits total power dissipation in the uP8806P/Q when the junction temperature exceeds $T_j = 150\text{ }^{\circ}\text{C}$, the thermal sensor signal the shutdowns logic, turning off the pass transistor and allows the device to cool down. The thermal sensor turns on the pass transistor again after the device's junction temperature drops by 30 °C, resulting in a pulsed output during continuous thermal-overload conditions. The overtemperature protection is designed to protect the device in the event of a fault condition. For continual operation, do not exceed the recommended temperature of $T_j = 125\text{ }^{\circ}\text{C}$ for maximum reliability.

Absolute Maximum Rating

(Note 1)

Supply Input Voltage V_{IN}	-0.3V to +6.5V
< 200ns	-0.3V to +7.0V
Other Pins	-0.3V to ($V_{IN} + 0.3V$)
Storage Temperature Range	-65°C to +150°C
Junction Temperature	150°C
Lead Temperature (Soldering, 10 sec)	260°C
ESD Rating (Note 2)	
HBM (Human Body Mode)	2kV
MM (Machine Mode)	200V

Thermal Information

Package Thermal Resistance (Note 3)

SOT23-3L/5L θ_{JA}	250°C/W
SOT23-3L/5L θ_{JC}	140°C/W
TSOT23-5L θ_{JA}	250°C/W
TSOT23-5L θ_{JC}	100°C/W
SOT223-3L θ_{JA}	117°C/W
SOT223-3L θ_{JC}	15°C/W
SOT89-3L θ_{JA}	125°C/W
SOT89-3L θ_{JC}	15°C/W
TO252-3L θ_{JA}	62°C/W
TO252-3L θ_{JC}	2°C/W

Power Dissipation, P_D @ $T_A = 25^\circ\text{C}$

SOT23-3L/5L	0.4W
TSOT23-5L	0.4W
SOT223-3L	0.85W
SOT89-3L	0.8W
TO252-3L	1.61W

Recommended Operation Conditions

(Note 4)

Operating Junction Temperature Range	-40°C to +125°C
Operating Ambient Temperature Range	-40°C to +85°C
Supply Input Voltage, V_{IN}	+2.4V to +5.5V

Note 1. Stresses listed as the above *Absolute Maximum Ratings* may cause permanent damage to the device.

These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2. Devices are ESD sensitive. Handling precaution recommended.

Note 3. θ_{JA} is measured in the natural convection at $T_A = 25^\circ\text{C}$ on a low effective thermal conductivity test board of JEDEC 51-3 thermal measurement standard.

Note 4. The device is not guaranteed to function outside its operating conditions.

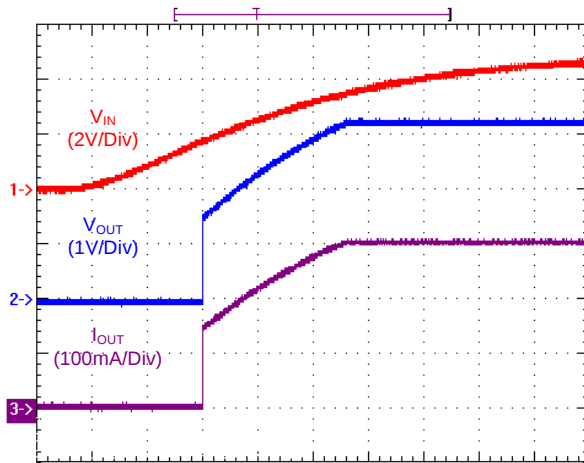
Electrical Characteristics

($V_{IN} = 5V$, $T_A = 25^\circ C$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Supply Input Voltage						
Supply Input Voltage	V _{IN}		2.4	--	6.0	V
POR Threshold	V _{PORTH}		--	1.9	2.4	V
POR Hysteresis	V _{PORHYS}		--	0.35	--	V
Quiescent Current	I _Q	V _{EN} = 5V, I _{OUT} = 0mA	--	35	50	uA
Shutdown Current	I _{SHDN}	V _{EN} = 0V	--	0.1	1	uA
Output Voltage						
Output Voltage Accuracy	V _{OUT}	V _{IN} = V _{NOM} + 1.0V; I _{OUT} = 1mA	-1.5	--	1.5	%V _{NOM}
Output Line Regulation	ΔV _{OUT(LINE)}	2.5V < V _{IN} < 5.5V, I _{OUT} = 1mA	--	0.01	0.1	%/V
Output Load Regulation	ΔV _{OUT(LOAD)}	1mA < I _{OUT} < 300mA, V _{IN} = V _{NOM} + 1.0V	--	0.5	1	%/A
Output Voltage Noise		10Hz to 1MHz; C _{OUT} = 1uF	--	70	--	uV _(RMS)
Power Supply Rejection Ratio	PSRR	I _{OUT} = 30mA; 1kHz, V _{OUT} = 1.2V	--	74	--	dB
		I _{OUT} = 30mA; 1kHz, V _{OUT} = 3.0V	--	63	--	
		I _{OUT} = 30mA; 100kHz	--	40	--	
		I _{OUT} = 30mA; 1MHz	--	35	--	
Dropout Voltage	V _{DROP}	I _{OUT} = 150mA, 2.5V < V _{NOM} < 3.3V	--	150	--	mV
		I _{OUT} = 300mA, 2.5V < V _{NOM} < 3.3V	--	300	--	mV
Enable						
Enable High Level	V _{EN}		1.4	--	--	V
Disable Low Level	V _{SD}		--	--	0.4	V
EN Input Current	I _{EN}	V _{IN} = 5.5V, V _{EN} = 5.5V or 0V	-1	0	1	uA
Enable Delay Time		form V _{EN} > 2V to V _{OUT} > 10%V _{NOM}	10	25	50	us
Output Pull Low Resistance When Turn Off		V _{IN} = 5V, V _{OUT} = 0.5V	300	500	700	Ω
Protection						
Current Limit Threshold	I _{LIM_TH}	uP8806P	330	450	--	mA
		uP8806Q	530	700	--	
Output Current	I _{OUT}	uP8806P	300	--	--	mA
		uP8806Q	500	--	--	
Short Circuit Current	I _{LIM_SC}		--	105	--	mA
Thermal Shutdown Temperature	T _{SD}	I _{OUT} = 0mA, V _{IN} = V _{EN} = 5.5V ,	--	150	--	°C
Thermal Shutdown Hysteresis	T _{SDHYS}	I _{OUT} = 0mA, V _{IN} = V _{EN} = 5.5V	--	30	--	°C

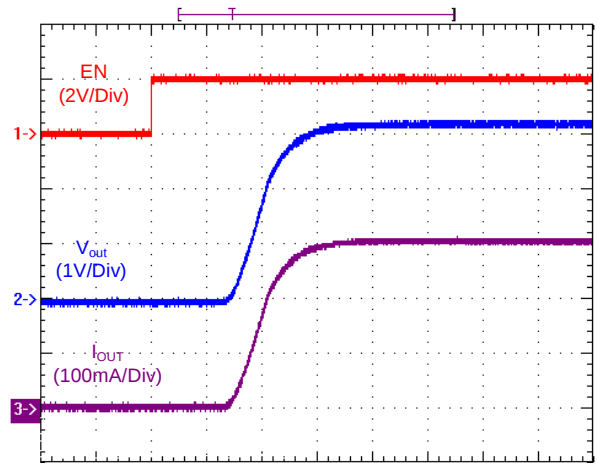
Typical Operation Characteristics

Power On Waveforms



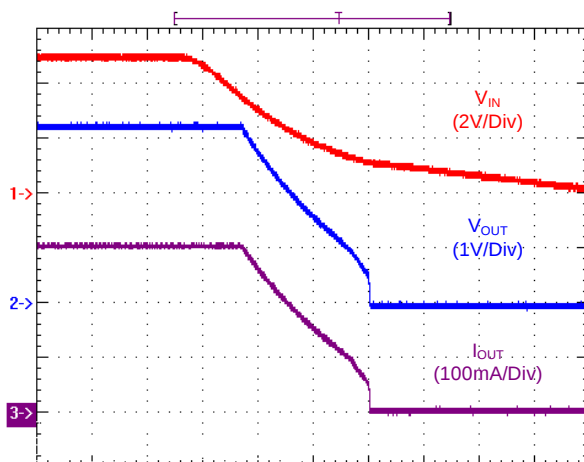
Time: 1ms/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

Turn On Waveforms



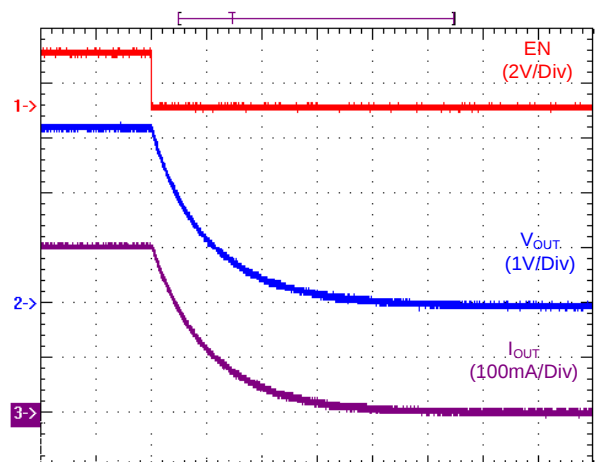
Time: 20μs/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

Power Off Waveforms



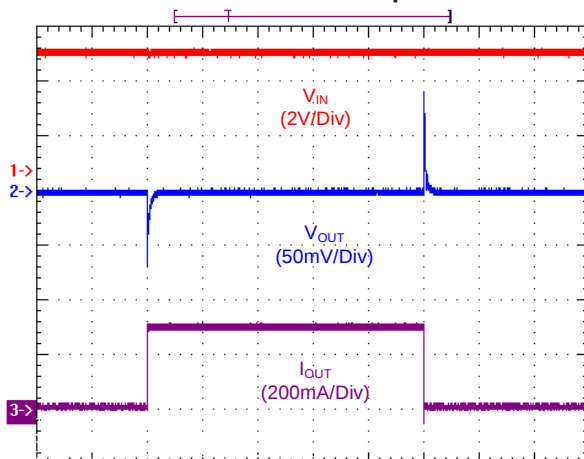
Time: 2ms/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

Turn Off Waveforms



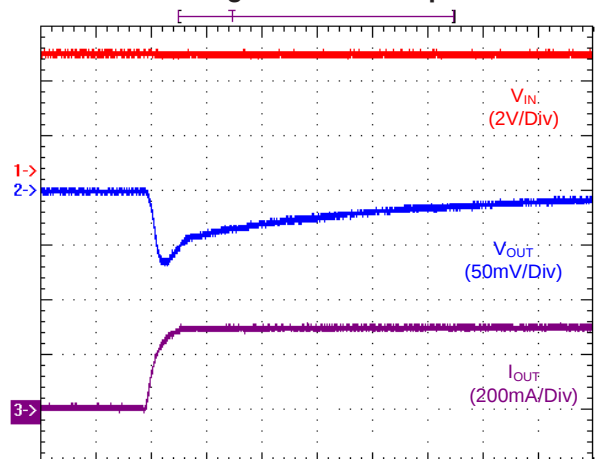
Time: 10μs/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

Load Transient Response



Time: 200μs/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

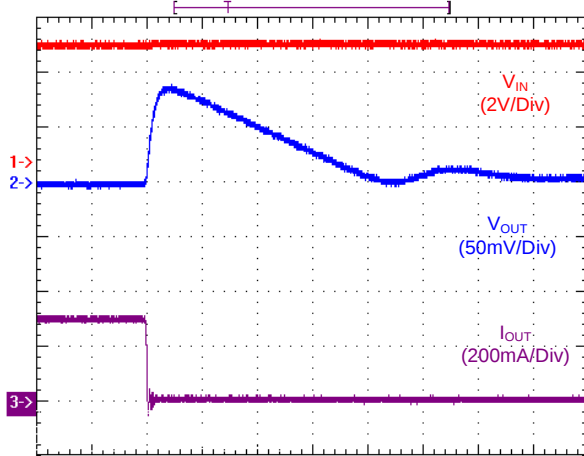
Load Regulation On Response



Time: 2μs/Div
 $C_{IN} = C_{OUT} = 1\mu F$, $V_{OUT} = 3.3V$, $R_{OUT} = 11\Omega$

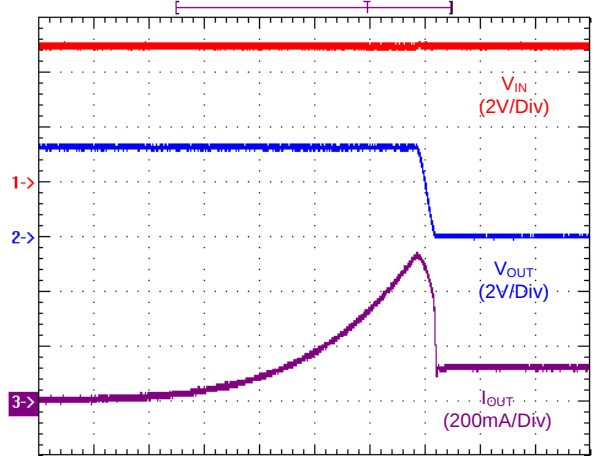
Typical Operation Characteristics

Load Transient Off Response



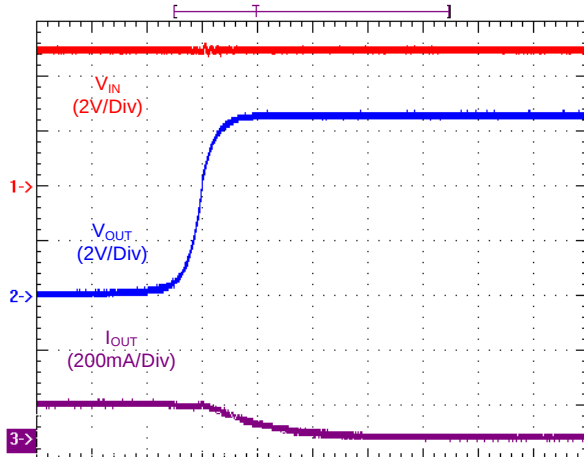
Time: 1ms/Div
 $C_{IN} = C_{out} = 1\mu F$, $V_{out} = 4.3V$, $R_{OUT} = 11\Omega$

Short Circuit Protection



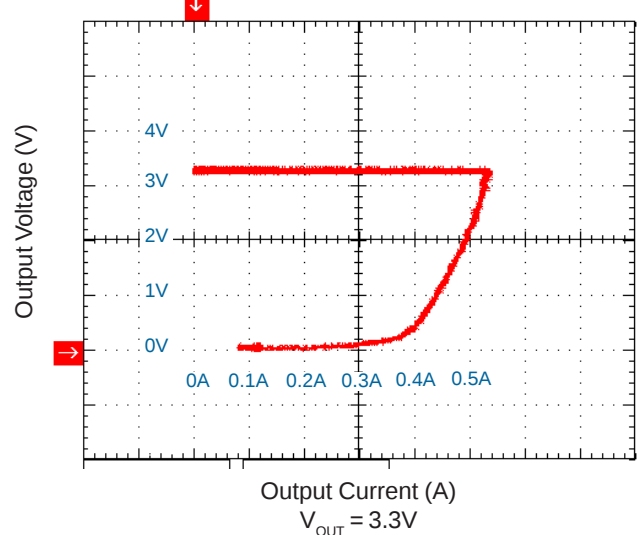
Time: 40us/Div
 $C_{IN} = C_{out} = 1\mu F$, $V_{out} = 3.3V$, $V_{IN} = 4.3V$

Short Circuit Protection Recover

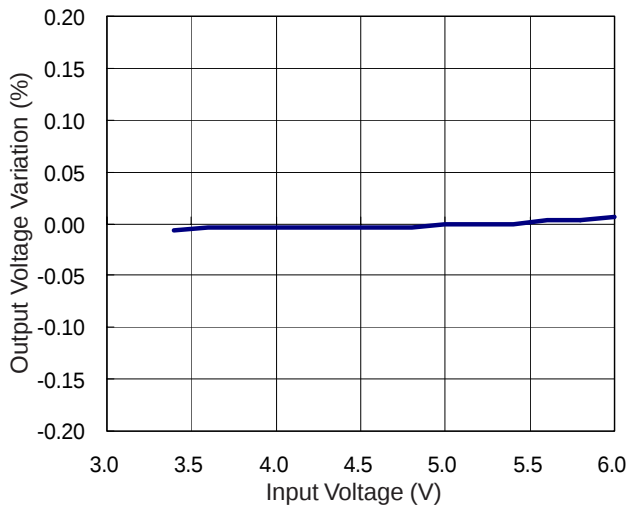


Time: 40us/Div
 $C_{IN} = C_{out} = 1\mu F$, $V_{out} = 3.3V$, $V_{IN} = 4.3V$

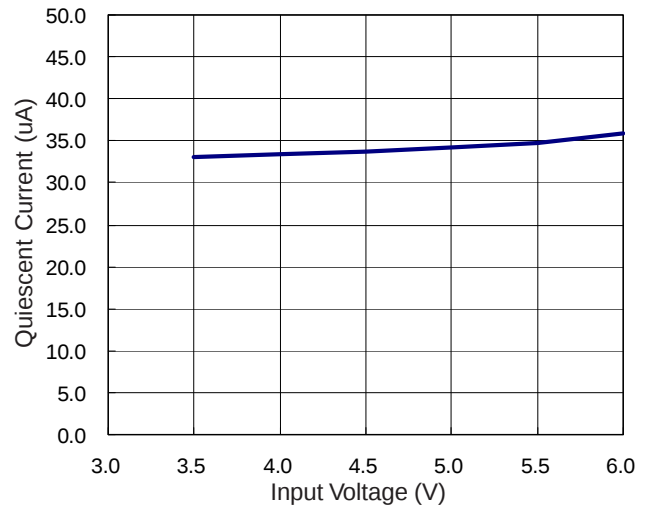
Output Short Circuit



Output Voltage Line Regulation

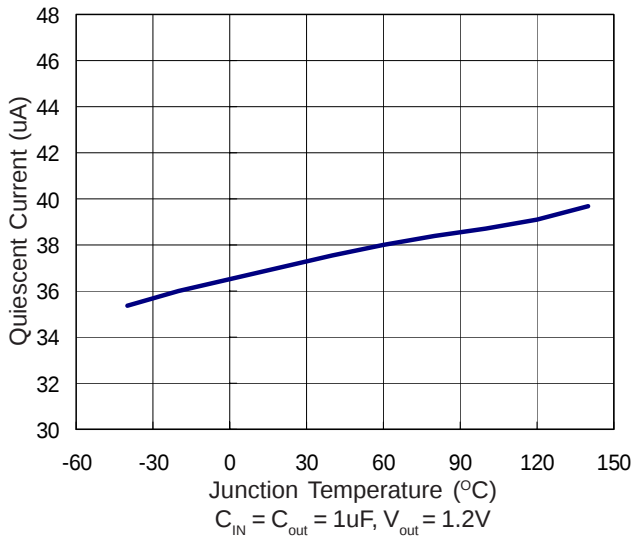


Quiescent Current vs. Input Voltage

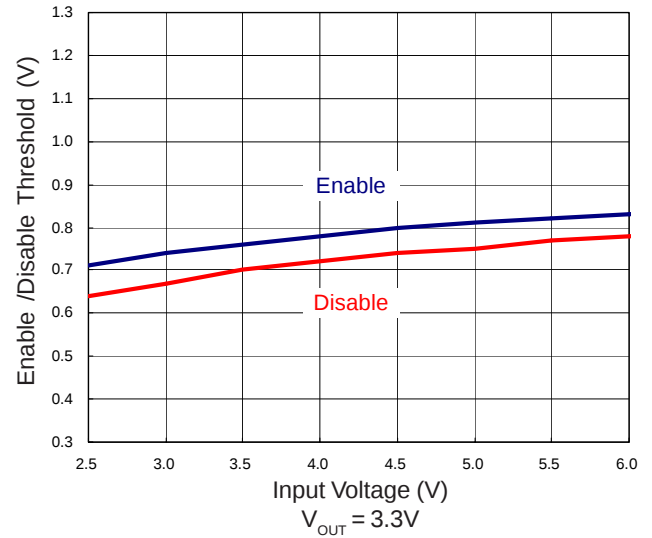


Typical Operation Characteristics

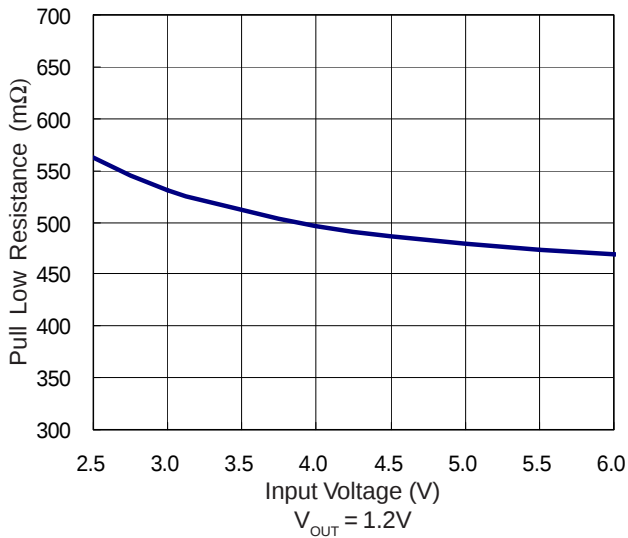
Quiescent Current vs. Temperature



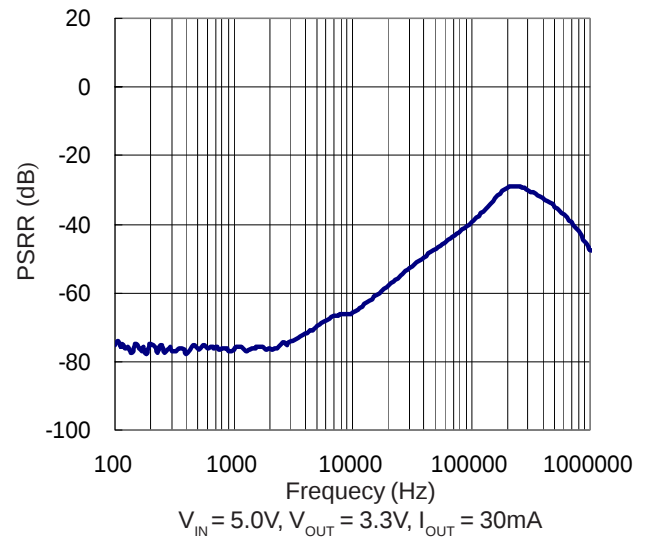
Enable /Disable vs. Input Voltage



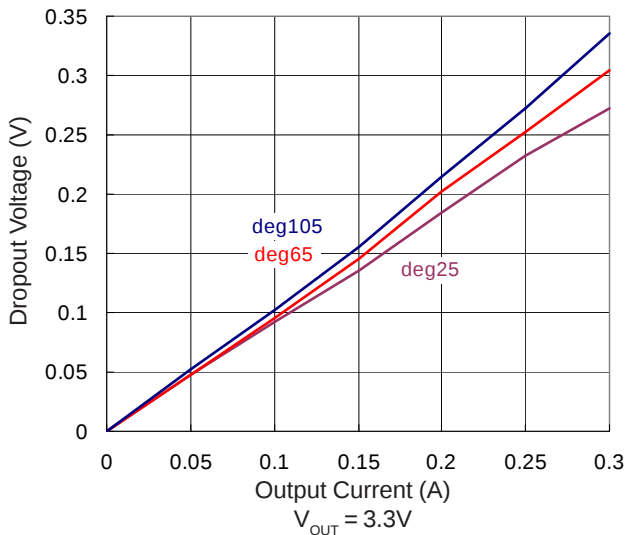
Output Pull Low Resistance vs. Input Voltage



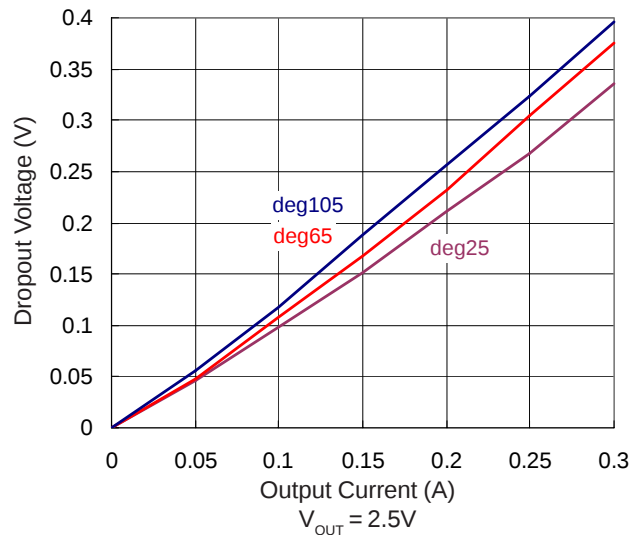
PSRR



Output Current vs. Dropout Voltage



Dropout Voltage vs. Output Current



Application Information

The uP8806P/Q is specially designed to provide low-noise, high PSRR output voltage without a bypassing capacitor on its reference voltage. However, input and output capacitor should be well considered for optimal performance.

Input Capacitors

The uP8806P/Q requires well-decoupled supply input for optimal performance. A minimum 1µF capacitor is required from-input-to-ground to provide stability. Input capacitors greater than 1µF offer superior input line transient response and will assist in maximizing the highest possible power supply ripple rejection ratio (PSRR). Ceramic, tantalum, or aluminum electrolytic capacitors may be selected for C_{IN}. There is no specific capacitor ESR requirement for C_{IN}. However, low-ESR ceramic capacitors provide optimal performance at a minimum of space and are highly recommended due to their inherent capability over tantalum capacitors to withstand input current surges from low impedance sources such as batteries in portable devices. Additional high frequency capacitors, such as small-valued NPO dielectric type capacitors, help filter out high-frequency noise and are good design practice in any RF-based circuit. Place the capacitors physically as close as possible to the device with wide and direct PCB traces.

Output Capacitors and Stability

For proper load voltage regulation and operational stability, a capacitor is required between V_{OUT} and GND pins. The uP8806P/Q is designed and optimized to work with low-value, low-cost ceramic capacitors in space saving and performance consideration. Typical output capacitor values for maximum output current conditions range from 1µF to 10µF. Larger capacitors are recommended for applications expecting low output noise and optimum power supply ripple rejection characteristics. Place the capacitors physically as close as possible to the device with wide and direct PCB traces.

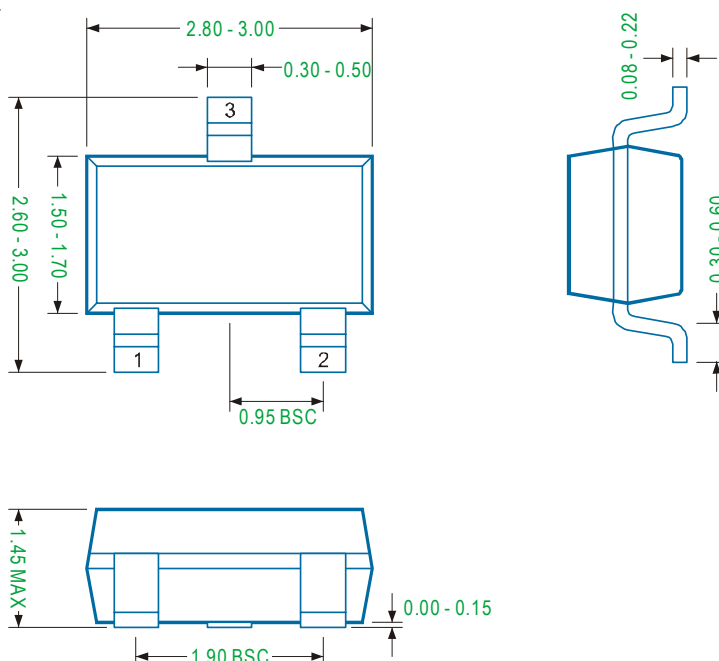
X7R/X5R dielectric-type ceramic capacitors are recommended because of their temperature performance. X7R type capacitors loss capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U or Y5V dielectric capacitors loss their capacitance by 50% and 60% respectively over their operating temperature ranges. If Y5V or Z5U capacitors are used as output capacitors, the capacitance must be much higher than that of X7R capacitors to ensure the same minimum capacitance over the operating temperature range.

ESR of output capacitors should be well considered to ensure stable operation of the device. High ESR capacitors may cause high frequency oscillation.

No Load Stability

The uP8806P/Q is designed to maintain output voltage regulation and stability under operational no load conditions. This is important characteristic for CMOS RAM keep-alive applications where the output current may drop to zero.

SOT23-3L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

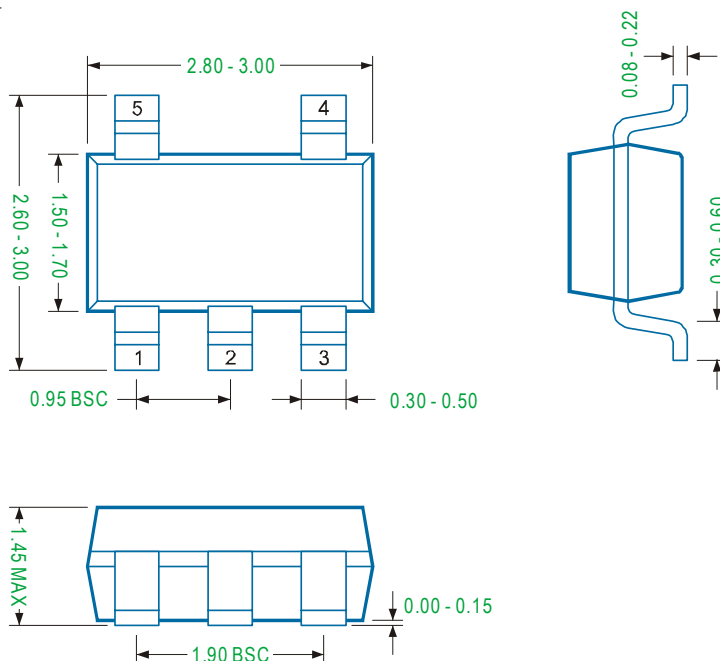
TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

SOT23-5L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

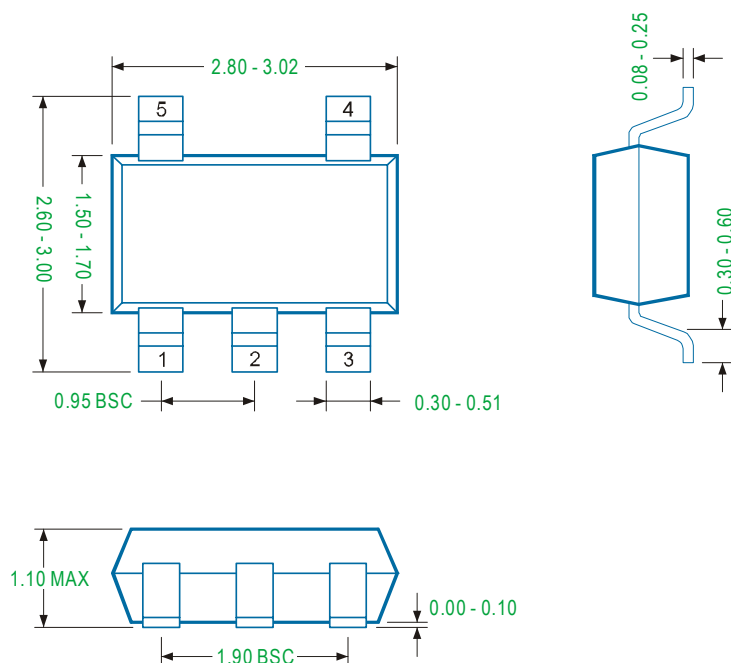
TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

TSOT23-5L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

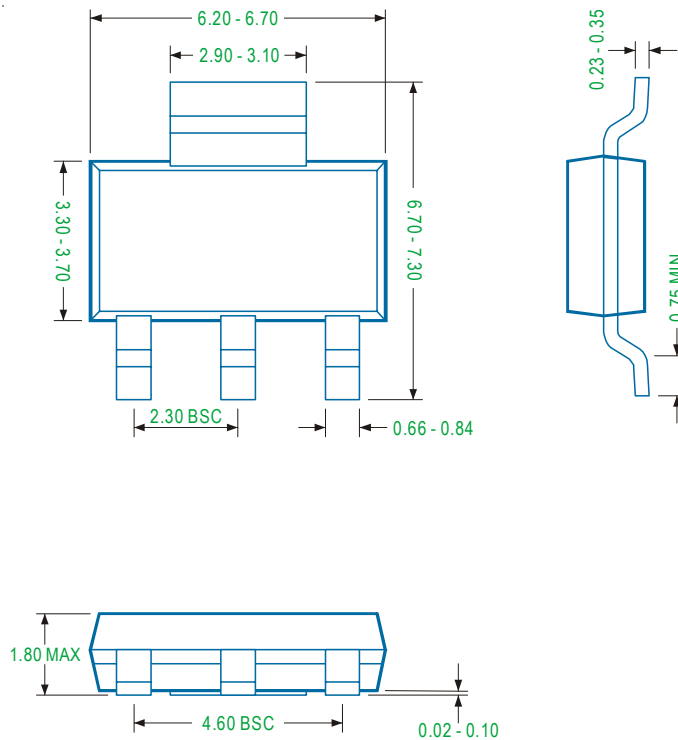
TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

SOT223-3L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

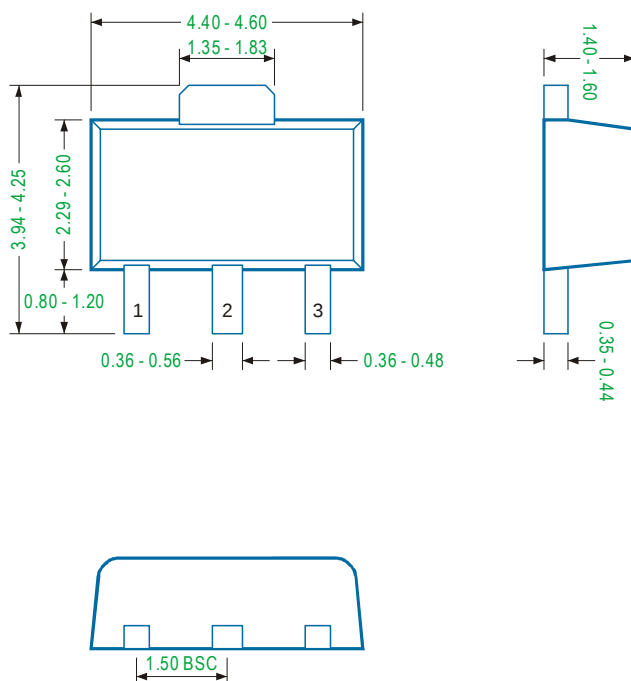
TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

SOT89-3L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

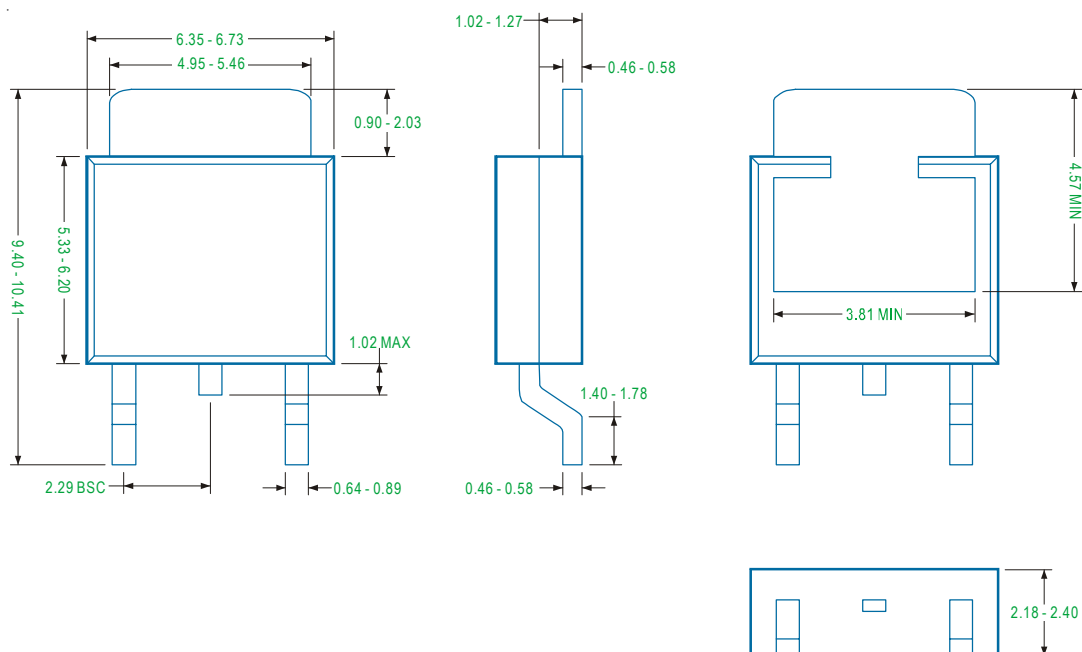
TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

TO252-3L



Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

TYP: Typical. Provided as a general value. This value is not a device specification.

2. Dimensions in Millimeters.

3. Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.

Important Notice

uPI and its subsidiaries reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

uPI products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment. However, no responsibility is assumed by uPI or its subsidiaries for its use or application of any product or circuit; nor for any infringements of patents or other rights of third parties which may result from its use or application, including but not limited to any consequential or incidental damages. No uPI components are designed, intended or authorized for use in military, aerospace, automotive applications nor in systems for surgical implantation or life-sustaining. No license is granted by implication or otherwise under any patent or patent rights of uPI or its subsidiaries.

COPYRIGHT (c) 2015, UPI SEMICONDUCTOR CORP.

uPI Semiconductor Corp.

Headquarter

9F., No. 5, Taiyuan 1st St. Zhubei City,
Hsinchu Taiwan, R.O.C.

TEL : 886.3.560.1666 FAX : 886.3.560.1888

Sales Branch Office

12F-5, No. 408, Ruiguang Rd. Neihu District,
Taipei Taiwan, R.O.C.

TEL : 886.2.8751.2062 FAX : 886.2.8751.5064