

# SPECIFICATION FOR LCD MODULE

Model No. TM404BBAW6

|               |       |
|---------------|-------|
| Prepared by:  | Date: |
| Checked by :  | Date: |
| Verified by : | Date: |
| Approved by:  | Date: |

**TIANMA MICROELECTRONICS CO., LTD**

## REVISION RECORD

| Date | Ref. Page | Revision No. | Revision Items | Check & Approval |
|------|-----------|--------------|----------------|------------------|
|      |           |              |                |                  |

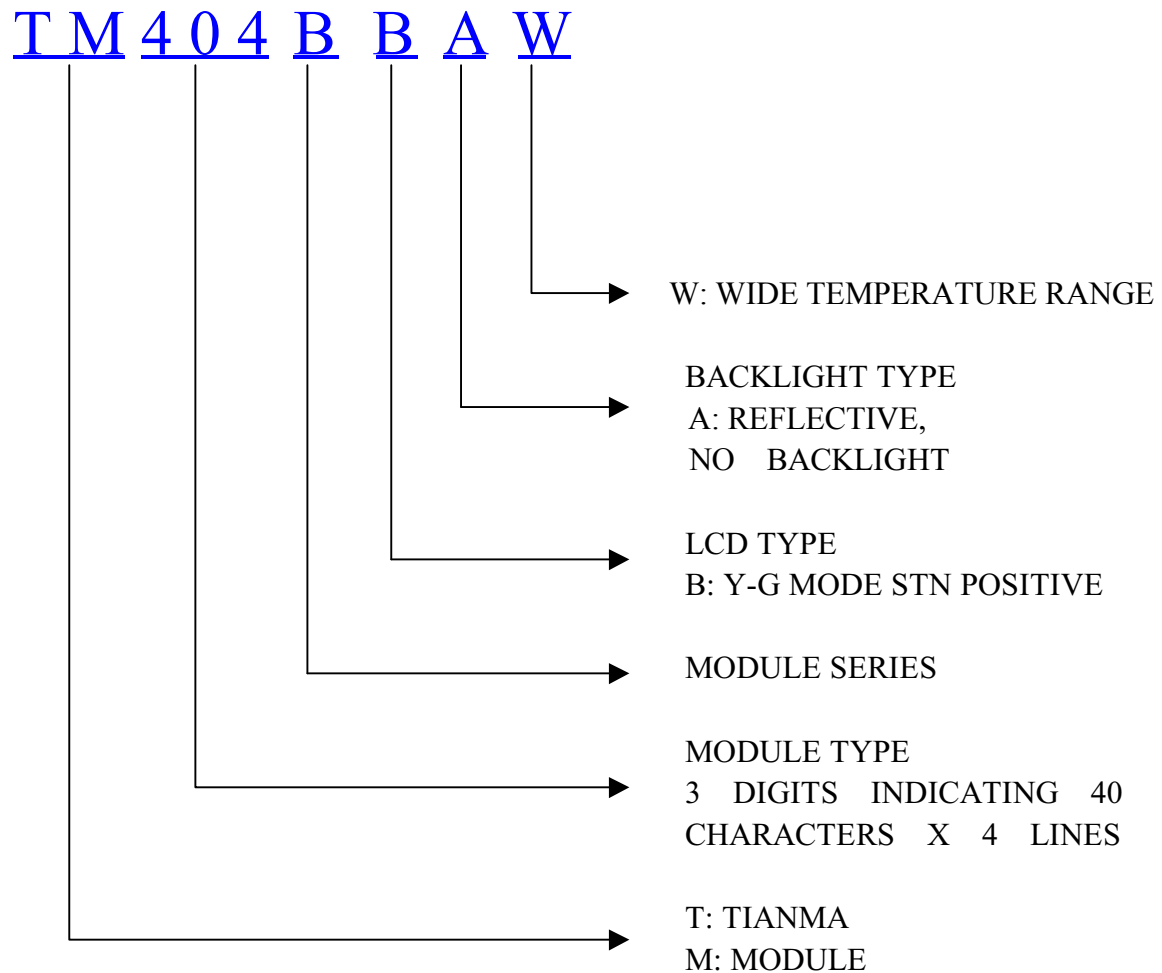
## 1. General Specifications

|                            |                                       |
|----------------------------|---------------------------------------|
| 1.1 Display type:          | STN                                   |
| 1.2 Display color*:        |                                       |
| Display color:             | Blue-Black                            |
| Background:                | Yellow-Green                          |
| 1.3 Polarizer mode:        | Reflective/Positive                   |
| 1.4 Viewing Angle:         | 6:00                                  |
| 1.5 Driving Method:        | 1/16 Duty 1/5 Bias                    |
| 1.6 Backlight:             | none                                  |
| 1.7 Display Fonts:         | 5 x 7 dots+Cursor (1 Character)       |
| 1.8 Controller:            | S6A0069X01-Q0RJ(KS0066UQ-00)          |
| 1.8 Data Transfer:         | 8 Bit Parallel                        |
| 1.9 Operating Temperature: | -20----+70°C                          |
| Storage Temperature:       | -30----+80 °C                         |
| 1.10 Outline Dimensions:   | Refer to outline drawing on next page |
| 1.11 Dot Matrix:           | 40 Characters X 4 Lines               |
| 1.12 Dot Size:             | 0.50X0.55(mm)                         |
| 1.13 Dot Pitch:            | 0.57X0.62 (mm)                        |
| 1.14 Weight:               | Approx. 120g                          |

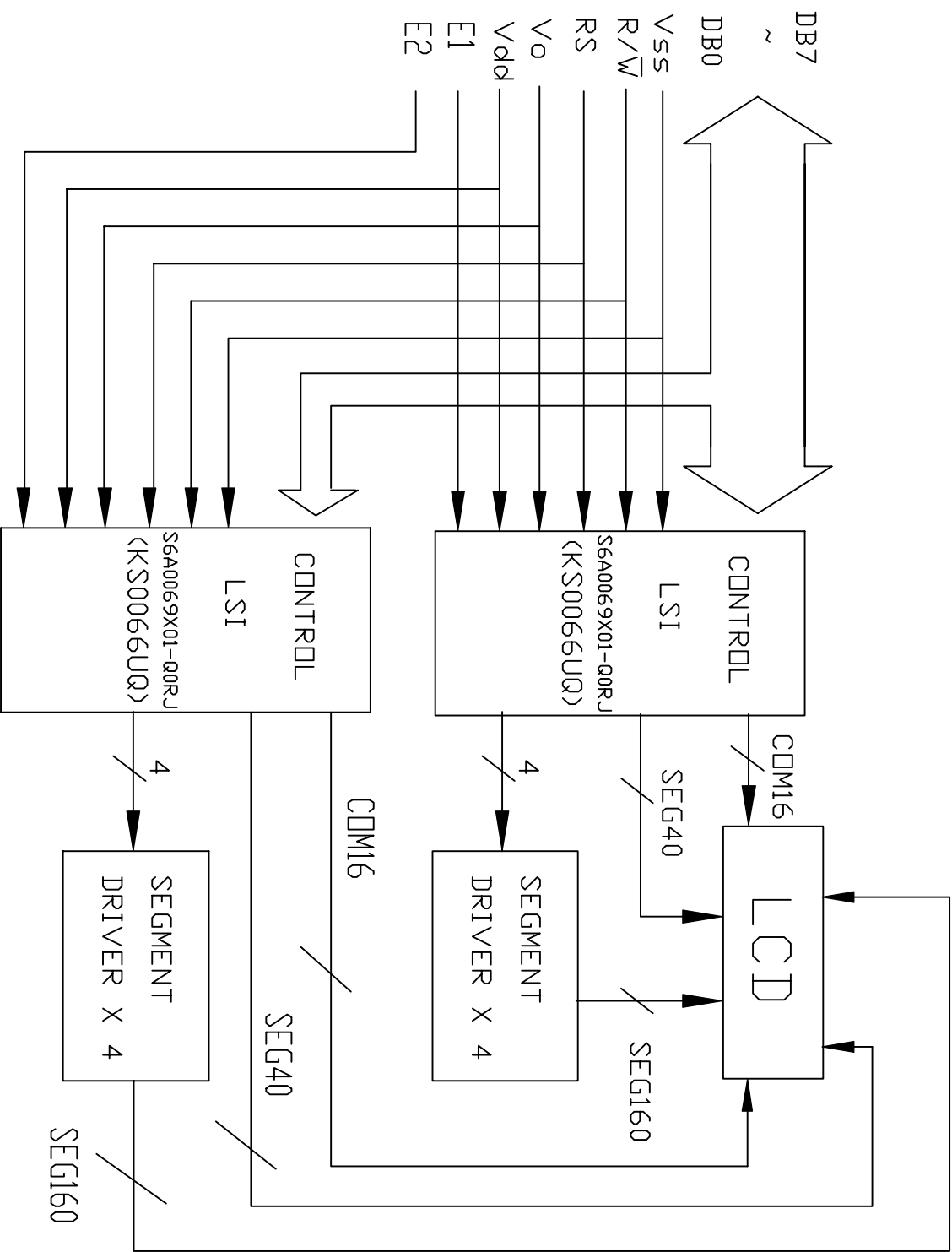
\* Color tone is slightly changed by temperature and driving voltage.



### 3 LCD Module Part Numbering System



4 Circuit Block Diagram



## 5 Absolute Maximum Ratings

| Item                        | Symbol          | Min. | Max. | Unit | Remark          |
|-----------------------------|-----------------|------|------|------|-----------------|
| Power Supply Voltage        | $V_{DD}-V_{SS}$ | -0.3 | 7.0  | V    |                 |
| LCD Driving Voltage         | $V_{LCD}$       | -0.3 | 13.0 |      |                 |
| Operating Temperature Range | $T_{OP}$        | -20  | +70  | °C   | No Condensation |
| Storage Temperature Range   | $T_{ST}$        | -30  | +80  |      |                 |

## 6 Electrical Specifications and Instruction Code

### 6.1 Electrical characteristics

| Item                          |      | Symbol                               | Min.        | Typ. | Max.         | Unit |
|-------------------------------|------|--------------------------------------|-------------|------|--------------|------|
| Supply Voltage<br>(Logic)     |      | $V_{DD}-V_{SS}$                      | 4.5         | 5.0  | 5.5          | V    |
| Supply Voltage<br>(LCD Drive) |      | $V_{LCD}$                            | -           | 4.7  | -            | V    |
| Input<br>Signal<br>Voltage    | High | $V_{IH}$<br>( $V_{DD}=5.0$ )         | $0.7V_{DD}$ | -    | $V_{DD}+0.3$ | V    |
|                               | Low  | $V_{IL}$<br>( $V_{DD}=5.0$ )         | -0.3        | -    | $0.2 V_{DD}$ | V    |
| Supply current<br>(Logic)     |      | $I_{DD}$<br>( $V_{DD}-V_{SS}=5.0V$ ) | -           | -    | 3.0          | mA   |
| Supply current<br>(LCD Drive) |      | $I_{EE}$                             | -           | -    | 1.0          | mA   |
|                               |      |                                      |             |      |              |      |

## 6.2 Interface Signals

| Pin No. | Symbol | Level | Description                               |
|---------|--------|-------|-------------------------------------------|
| 1       | Vss    | 0V    | Ground                                    |
| 2       | Vdd    | 5V    | Power supply voltage for logic and LCD(+) |
| 3       | Vo     | 0.3V  | Power supply voltage for LCD(-)           |
| 4       | RS     | H/L   | Selects registers                         |
| 5       | R/W    | H/L   | Selects read or write                     |
| 6       | E1     | H/L   | Enable Input 1                            |
| 7       | E2     | H/L   | Enable Input 2                            |
| 8       | DB0    | H/L   | Data bit0                                 |
| 9       | DB1    | H/L   | Data bit1                                 |
| 10      | DB2    | H/L   | Data bit2                                 |
| 11      | DB3    | H/L   | Data bit3                                 |
| 12      | DB4    | H/L   | Data bit4                                 |
| 13      | DB5    | H/L   | Data bit5                                 |
| 14      | DB6    | H/L   | Data bit6                                 |
| 15      | DB7    | H/L   | Data bit7                                 |
| 16      | NC     | -     | Not connected                             |
|         |        |       |                                           |
|         |        |       |                                           |

### 6.3 Interface Timing Chart

AC Characteristics( $V_{DD}=4.5V\sim 5.5V$ ,  $T_a=-30\sim +85^{\circ}C$ )

| Mode                           | Characteristic            | Symbol     | Min. | Typ. | Max. | Unit |
|--------------------------------|---------------------------|------------|------|------|------|------|
| Write Mode<br>(Refer to Fig-6) | E Cycle Time              | $t_c$      | 500  | -    | -    | ns   |
|                                | E Rise / Fall Time        | $t_R, t_F$ | -    | -    | 20   |      |
|                                | E Pulse Width (High, Low) | $t_w$      | 230  | -    | -    |      |
|                                | R/W and RS Setup Time     | $t_{su1}$  | 40   | -    | -    |      |
|                                | R/W and RS Hold Time      | $t_{H1}$   | 10   | -    | -    |      |
|                                | Data Setup Time           | $t_{su2}$  | 80   | -    | -    |      |
|                                | Data Hold Time            | $t_{H2}$   | 10   | -    | -    |      |
| Read Mode<br>(Refer to Fig-7)  | E Cycle Time              | $t_c$      | 500  | -    | -    | ns   |
|                                | E Rise / Fall Time        | $t_R, t_F$ | -    | -    | 20   |      |
|                                | E Pulse Width (High, Low) | $t_w$      | 230  | -    | -    |      |
|                                | R/W and RS Setup Time     | $t_{su}$   | 40   | -    | -    |      |
|                                | R/W and RS Hold Time      | $t_H$      | 10   | -    | -    |      |
|                                | Data Output Delay Time    | $t_D$      | -    | -    | 120  |      |
|                                | Data Hold Time            | $t_{DH}$   | 5    | -    | -    |      |

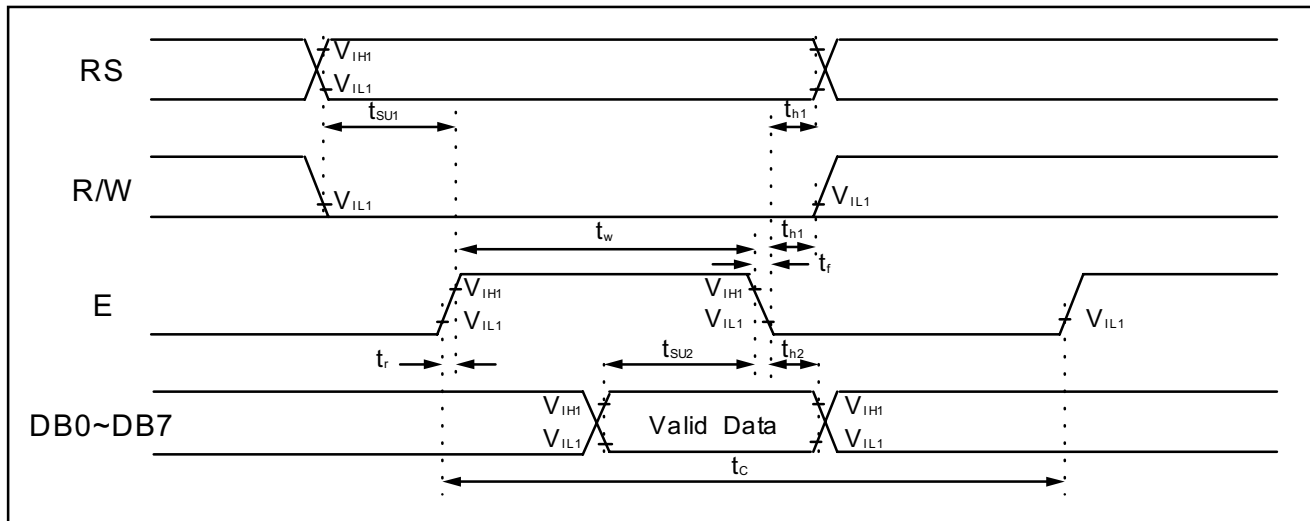


Figure 6 . Write Mode Timing Diagram

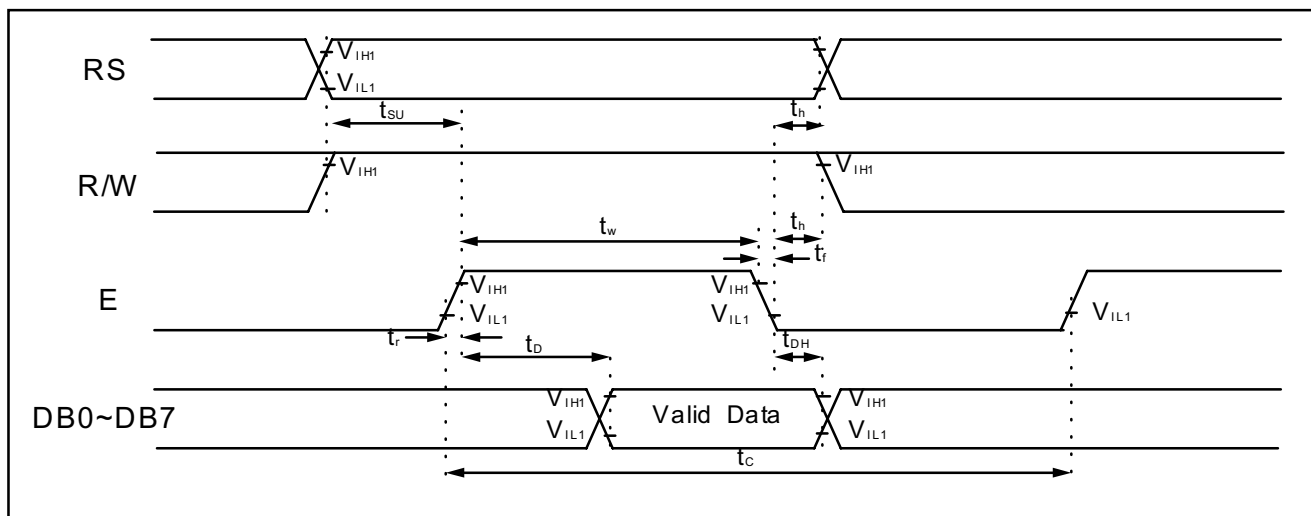


Figure 7 . Read Mode Timing Diagram

## 6.4 Instruction Code

| Instruction                | Instruction Code |     |     |     |     |     |     |     |     |     | Description                                                                                                                                            | Execution time (fosc= 270 kHz) |
|----------------------------|------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|--------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------|
|                            | RS               | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |                                                                                                                                                        |                                |
| Clear Display              | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 1   | Write "20H" to DDRAM and set DDRAM address to "00H" from AC                                                                                            | 1.53 ms                        |
| Return Home                | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 1   | -   | Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.                       | 1.53 ms                        |
| Entry Mode Set             | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 1   | I/D | SH  | Assign cursor moving direction and enable the shift of entire display.                                                                                 | 39 $\mu$ s                     |
| Display ON/OFF Control     | 0                | 0   | 0   | 0   | 0   | 0   | 1   | D   | C   | B   | Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.                                                                               | 39 $\mu$ s                     |
| Cursor or Display Shift    | 0                | 0   | 0   | 0   | 0   | 1   | S/C | R/L | -   | -   | Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.                                                    | 39 $\mu$ s                     |
| Function Set               | 0                | 0   | 0   | 0   | 1   | DL  | N   | F   | -   | -   | Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5 $\times$ 11dots/5 $\times$ 8 dots) | 39 $\mu$ s                     |
| Set CGRAM Address          | 0                | 0   | 0   | 1   | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set CGRAM address in address counter.                                                                                                                  | 39 $\mu$ s                     |
| Set DDRAM Address          | 0                | 0   | 1   | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set DDRAM address in address counter.                                                                                                                  | 39 $\mu$ s                     |
| Read Busy Flag and Address | 0                | 1   | BF  | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.                                 | 0 $\mu$ s                      |
| Write Data to RAM          | 1                | 0   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Write data into internal RAM (DDRAM/CGRAM).                                                                                                            | 43 $\mu$ s                     |
| Read Data from RAM         | 1                | 1   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Read data from internal RAM (DDRAM/CGRAM).                                                                                                             | 43 $\mu$ s                     |

\* "-": don't care

## 6.5 Character generator ROM(KS0066U-00)

| Upper<br>4bit<br>Lower<br>4bit | LLLL             | LL LH | LL HL | LL HH | LH LL | LH LH | LH HL | LH HH | HLLL | HLLH | HL HL | HL HH | HH LL | HH LH | HH HL | HH HH |
|--------------------------------|------------------|-------|-------|-------|-------|-------|-------|-------|------|------|-------|-------|-------|-------|-------|-------|
| LLLL                           | CG<br>RAM<br>(1) |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LL LH                          | (2)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LL HL                          | (3)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LL HH                          | (4)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LH LL                          | (5)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LH LH                          | (6)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LH HL                          | (7)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| LH HH                          | (8)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HLLL                           | (1)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HLLH                           | (2)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HL HL                          | (3)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HL HH                          | (4)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HH LL                          | (5)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HH LH                          | (6)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HH HL                          | (7)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |
| HH HH                          | (8)              |       |       |       |       |       |       |       |      |      |       |       |       |       |       |       |

## 7 Optical Characteristics

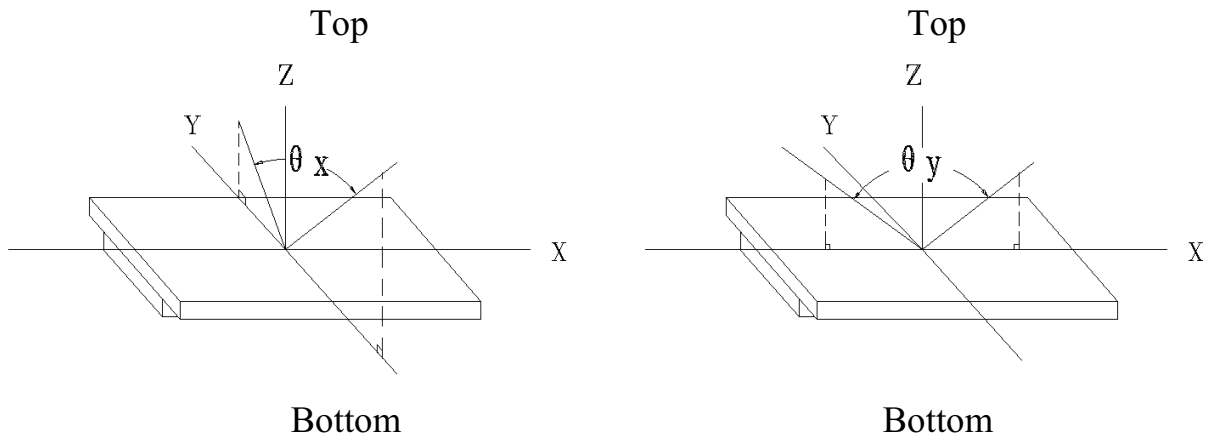
### 7.1 Optical Characteristics

Ta=25°C

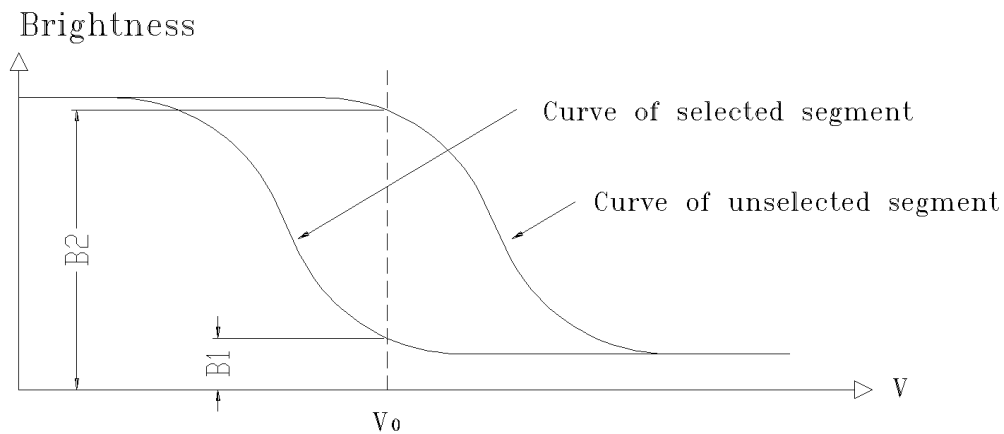
| Item           |          | Symbol     | Condition                                    |                      | Min. | Typ. | Max. | Unit |
|----------------|----------|------------|----------------------------------------------|----------------------|------|------|------|------|
| Viewing Angle  |          | $\theta_x$ | $Cr \geq 2$                                  | $\theta_y = 0^\circ$ | -35  | --   | 20   | Deg  |
|                |          | $\theta_y$ |                                              | $\theta_x = 0^\circ$ | -30  | --   | 30   |      |
| Contrast Ratio |          | Cr         | $\theta_x = 0^\circ$<br>$\theta_y = 0^\circ$ |                      | 4.0  | -    | -    |      |
| Response Time  | Turn on  | Ton        | $\theta_x = 0^\circ$<br>$\theta_y = 0^\circ$ |                      | -    | -    | 250  | ms   |
|                | Turn off | Toff       |                                              |                      | -    | -    | 250  |      |

## 7.2 Definition of Optical Characteristics

### 7.2.1 Definition of Viewing Angle



### 7.2.2 Definition of Contrast Ratio

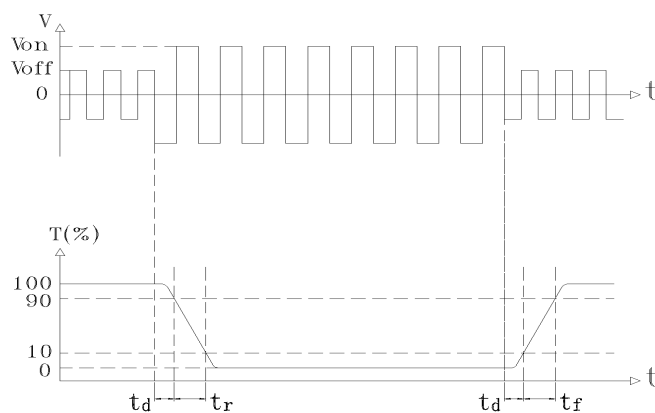


$$\text{Contrast Ratio} = B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

- 1) Ambient Temperature:  $25^{\circ}\text{C}$  ;
- 2) Frame frequency:  $84.3\text{Hz}$

### 7.2.3 Definition of Response time



Turn on time:  $t_{on} = t_d + t_r$       Turn off time:  $t_{off} = t_d + t_f$

Measuring Condition:

- 1) Operating Voltage:  $4.7\text{V}$
- 2) Frame frequency:  $84.3\text{Hz}$

## 8 Reliability

### 8.1 Content of Reliability Test

Ta=25°C

| No. | Test Item                          | Content of Test                                                                                                                                                                                                                                                                                                                                                                                       | Test condition                                    |
|-----|------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------|
| 1   | High Temperature Storage           | Endurance test applying the high storage temperature for a long time                                                                                                                                                                                                                                                                                                                                  | 80°C<br>96H                                       |
| 2   | Low Temperature Storage            | Endurance test applying the low storage temperature for a long time                                                                                                                                                                                                                                                                                                                                   | -30°C<br>96H                                      |
| 3   | High Temperature Operation         | Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time                                                                                                                                                                                                                                                                                 | 70°C<br>96H                                       |
| 4   | Low Temperature Operation          | Endurance test applying the electric stress under low temperature for a long time                                                                                                                                                                                                                                                                                                                     | -20°C<br>96H                                      |
| 5   | High Temperature /Humidity Storage | Endurance test applying the high temperature and high humidity storage for a long time                                                                                                                                                                                                                                                                                                                | 40°C<br>90%RH<br>96H                              |
| 6   | Temperature Cycle                  | Endurance test applying the low and high temperature cycle<br>$\begin{array}{ccccccc} -20^{\circ}\text{C} & \longleftrightarrow & 25^{\circ}\text{C} & \longleftrightarrow & 60^{\circ}\text{C} & \longleftrightarrow & 25^{\circ}\text{C} \\ 30\text{min} & & 5\text{min} & & 30\text{min} & & 5\text{min} \\ \longleftarrow & & & & & & \longrightarrow \\ & & & & & & 1 \text{ cycle} \end{array}$ | -20°C/60°C<br>10 cycles                           |
| 7   | Vibration Test (package state)     | Endurance test applying the vibration during transportation                                                                                                                                                                                                                                                                                                                                           | 10Hz~150Hz,<br>50m/s <sup>2</sup> ,<br>40min      |
| 8   | Shock Test (package state)         | Endurance test applying the shock during transportation                                                                                                                                                                                                                                                                                                                                               | Half- sine wave,<br>100m/s <sup>2</sup> ,<br>11ms |
| 9   | Atmospheric Pressure Test          | Endurance test applying the atmospheric pressure during transportation by air                                                                                                                                                                                                                                                                                                                         | 40kPa<br>16H                                      |

## 8.2 Failure Judgment Criterion

| Criterion Item           | Test Item No.                                                          |   |   |   |   |   |   |   |   | Failure Judgement Criterion         |
|--------------------------|------------------------------------------------------------------------|---|---|---|---|---|---|---|---|-------------------------------------|
|                          | 1                                                                      | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |                                     |
| Basic Specification      | √                                                                      | √ | √ | √ | √ | √ | √ | √ | √ | Out of the basic Specification      |
| Electrical specification | √                                                                      | √ | √ | √ | √ |   |   |   |   | Out of the electrical specification |
| Mechanical Specification |                                                                        |   |   |   |   |   | √ | √ |   | Out of the mechanical specification |
| Optical Characteristic   | √                                                                      | √ | √ | √ | √ | √ |   |   | √ | Out of the optical specification    |
| Note                     | For test item refer to 8.1                                             |   |   |   |   |   |   |   |   |                                     |
| Remark                   | Basic specification = Optical specification + Mechanical specification |   |   |   |   |   |   |   |   |                                     |

## 9 QUALITY LEVEL

| Examination<br>or Test                                                                                                                        | At T <sub>a</sub> =25℃<br>(unless otherwise stated)                                                         | Inspection     |      |      |    |                              |
|-----------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|----------------|------|------|----|------------------------------|
|                                                                                                                                               |                                                                                                             | Min.           | Max. | Unit | IL | AQL                          |
| External<br>Visual<br>Inspection                                                                                                              | Under normal illumi-<br>nation and eyesight<br>condition, the dis-tance<br>between eyes and LCD is<br>25cm. | See Appendix A |      |      | II | Major<br>1.0<br>Minor<br>2.5 |
| Display<br>Defects                                                                                                                            | Under normal illumi-<br>nation and eyesight<br>condition, display on<br>inspection.                         | See Appendix B |      |      | II | Major<br>1.0<br>Minor<br>2.5 |
| Note: Major defects: Open segment or common, Short, Serious damages, Leakage<br>Miner defects: Others<br>Sampling standard conforms to GB2828 |                                                                                                             |                |      |      |    |                              |

## **10 Precautions for Use of LCD Modules**

### **10.1 Handling Precautions**

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

## 10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :  $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

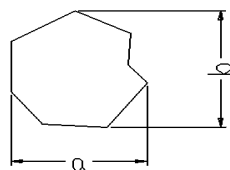
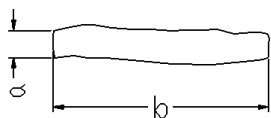
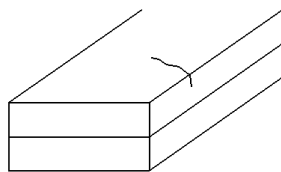
Relatively humidity:  $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

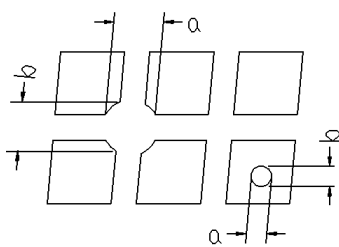
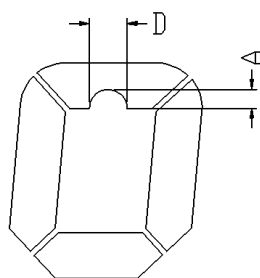
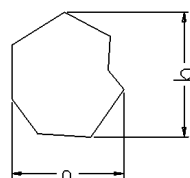
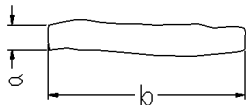
## Appendix A

### Inspection items and criteria for appearance defects

| Items                           | Contents                                                                            | Criteria                        |                                                               |                              |
|---------------------------------|-------------------------------------------------------------------------------------|---------------------------------|---------------------------------------------------------------|------------------------------|
| Leakage                         |                                                                                     | Not permitted                   |                                                               |                              |
| Rainbow                         |                                                                                     | According to the limit specimen |                                                               |                              |
| Polarizer                       | Wrong polarizer attachment                                                          | Not permitted                   |                                                               |                              |
|                                 | Bubble between polarizer and glass                                                  | Not counted                     | Max. 3 defects allowed                                        |                              |
|                                 |                                                                                     | $\phi<0.3\text{mm}$             | $0.3\text{mm}\leq\phi\leq0.5\text{mm}$                        |                              |
|                                 | Scratches of polarizer                                                              | According to the limit specimen |                                                               |                              |
| Black spot<br>(in viewing area) |  | Not counted                     | Max. 3 spots allowed                                          | Max. 3 spots (lines) allowed |
|                                 |                                                                                     | $X<0.2\text{mm}$                | $0.2\text{mm}\leq X\leq0.5\text{mm}$                          |                              |
|                                 |                                                                                     | $X=(a+b)/2$                     |                                                               |                              |
| Black line<br>(in viewing area) |  | Not counted                     | Max. 3 lines allowed                                          |                              |
|                                 |                                                                                     | $a<0.02\text{mm}$               | $0.02\text{mm}\leq a\leq0.05\text{mm}$<br>$b\leq2.0\text{mm}$ |                              |
| Progressive cracks              |  | Not permitted                   |                                                               |                              |

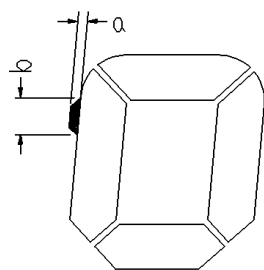
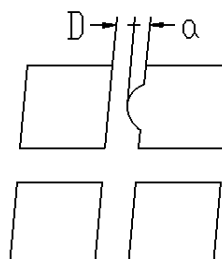
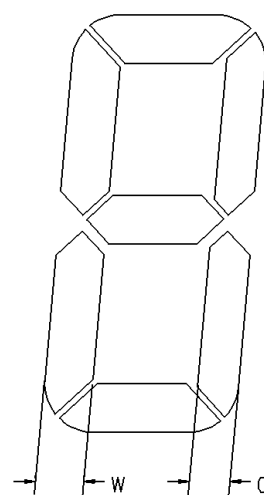
## Appendix B

### Inspection items and criteria for display defects

| Items                                 | Contents                                                                            | Criteria                        |                            |                             |
|---------------------------------------|-------------------------------------------------------------------------------------|---------------------------------|----------------------------|-----------------------------|
| Open segment or open common           |                                                                                     | Not permitted                   |                            |                             |
| Short                                 |                                                                                     | Not permitted                   |                            |                             |
| Wrong viewing angle                   |                                                                                     | Not permitted                   |                            |                             |
| Contrast radio uneven                 |                                                                                     | According to the limit specimen |                            |                             |
| Crosstalk                             |                                                                                     | According to the limit specimen |                            |                             |
| Pin holes and cracks in segment (DOT) |   | Not counted                     | Max.3 dots allowed         | Max.3 dots allowed          |
|                                       |                                                                                     | X<0.1mm                         | 0.1mm≤X≤0.2mm              |                             |
|                                       |                                                                                     | X=(a+b)/2                       |                            |                             |
|                                       |  | Not counted                     | Max.2 dots allowed         | Max.3 dots allowed          |
|                                       |                                                                                     | A<0.1mm                         | 0.1mm≤A≤0.2mm<br>D<0.25mm  |                             |
|                                       |                                                                                     |                                 |                            |                             |
| Black spot (in viewing area)          |  | Not counted                     | Max.3 spots allowed        | Max.3 spots (lines) allowed |
|                                       |                                                                                     | X<0.1mm                         | 0.1mm≤X≤0.2mm              |                             |
|                                       |                                                                                     | X=(a+b)/2                       |                            |                             |
| Black line (in viewing area)          |  | Not counted                     | Max.3 lines allowed        | Max.3 spots (lines) allowed |
|                                       |                                                                                     | a<0.02mm                        | 0.02mm≤a≤0.05mm<br>b≤0.5mm |                             |

## Appendix B

### Inspection items and criteria for display defects (continued)

| Items                             | Content                                                                             | Criteria                                                                                                          |                                                |                             |
|-----------------------------------|-------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------|------------------------------------------------|-----------------------------|
| Transfor-<br>mation<br>of segment |    | Not counted                                                                                                       | Max. 2 defects allowed                         | Max.3<br>defects<br>allowed |
|                                   |                                                                                     | $x<0.1\text{mm}$                                                                                                  | $0.1\text{mm}\leq x\leq 0.2\text{mm}$          |                             |
|                                   |                                                                                     | $x=(a+b)/2$                                                                                                       |                                                |                             |
|                                   |   | Not counted                                                                                                       | Max. 1 defects allowed                         |                             |
|                                   |                                                                                     | $a<0.1\text{mm}$                                                                                                  | $0.1\text{mm}\leq a\leq 0.2\text{mm}$<br>$D>0$ |                             |
|                                   |  | Max.2 defects allowed<br>$0.8W\leq a\leq 1.2W$<br><br>$a$ =measured value of width<br>$W$ =nominal value of width |                                                |                             |