SPECIFICATION FOR LCD MODULE

Model No. **TM12864DDCW**

Prepared by: Date: Checked by: Date: Verified by: Date: **Approved** Date:

TIANMA MICROELECTRONICS CO., LED

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1. General Specifications:

1.1 Display type: STN

1.2 Display color*:

Display color: Blue-Black

Background: Grey

1.3 Polarizer mode: Transflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/64 Duty 1/9 Bias

1.6 Backlight: LED

* Color tone is slightly changed by temperature and driving voltage.

1.7 Controller: HD61202UTE

1.8 Data Transfer: 8 Bit Parallel

1.9 Operating Temperature: -20----+70°C

Storage Temperature: -30----+80°C

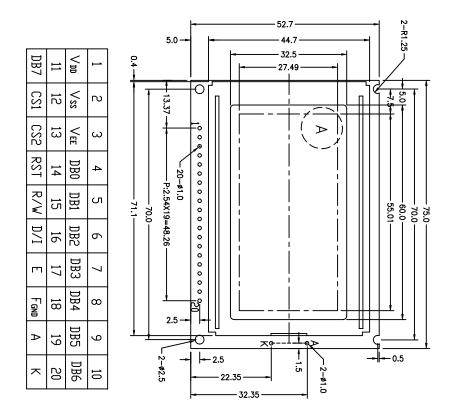
1.10 Outline Dimensions: Refer to outline drawing on next page

1.11 Dot Matrix: 128 X 64 Dots

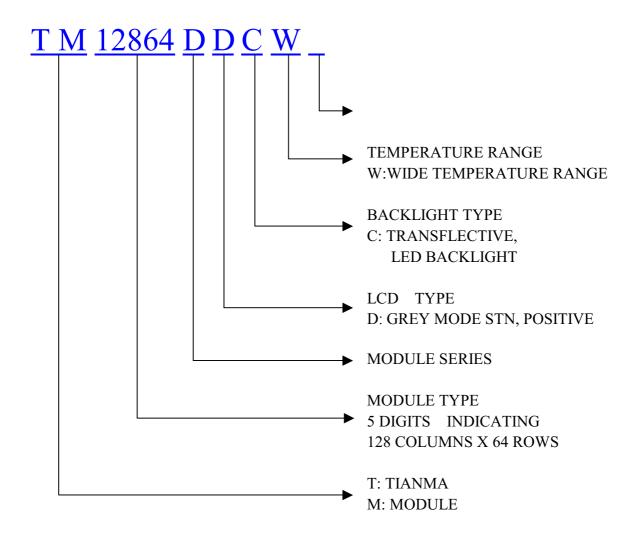
1.12 Dot Size: 0.40 X 0.40(mm)

1.13 Dot Pitch: 0.43 X 0.43(mm)

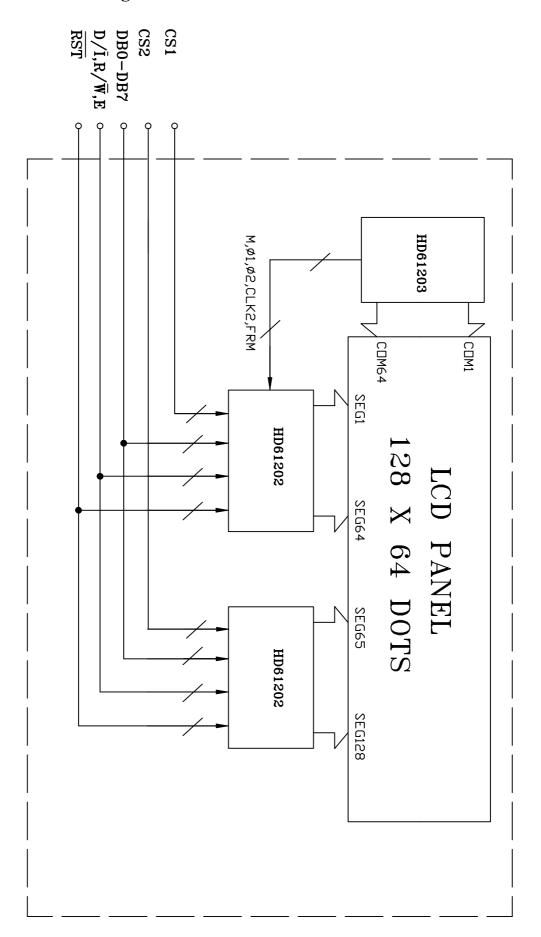
1.14 Weight: 45g



3. LCD Module Part Numbering System



4. Circuit Block Diagram



5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark	
Power Supply Voltage	V _{DD} -V _{SS}	-0.3	6.0	V		
LCD Driving Voltage	V _{LCD}	-	25.0	V		
Operating Temperature Range	Тор	-20	+70	°C	No	
Storage Temperature Range	Тѕт	-30	+80		Condensation	

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics

Iter	n	Symbol	Min.	Тур.	Max.	Unit
Supply V (Log	•	V _{DD} -V _{SS}	4.75	5.0	5.25	V
Supply V (LCD D	_	Vlcd	-	12.0	-	V
Input	High	V_{IH} $(V_{DD}=5.0)$	$0.8 \mathrm{V}_\mathrm{DD}$	-	V _{DD} +0.3	V
Signal Voltage	Low	V_{IL} $(V_{\text{DD}}=5.0)$	0	-	0.2 V _{DD}	V
Supply c (Log		$I_{ exttt{DD}}$	-	1.4	-	mA
Supply c (LCD D		${ m I}_{\scriptscriptstyle m EE}$	-	1.0	-	mA
Supply c (LEI		${ m I}_{\scriptscriptstyle m LED}$	_	-	200	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	V _{DD}	5.0V	Supply voltage for logic and LCD(+)
2	Vss	0V	Ground
3	VEE	-	Operating voltage for LCD(-)(variable)
4	DB0	H/L	Data bit0
5	DB1	H/L	Data bit1
6	DB2	H/L	Data bit2
7	DB3	H/L	Data bit3
8	DB4	H/L	Data bit4
9	DB5	H/L	Data bit5
10	DB6	H/L	Data bit6
11	DB7	H/L	Data bit7
12	CS1	L	Chip Select Signal 1
13	CS2	L	Chip Select Signal 2
14	RST	L	Reset Signal
15	R/W	H/L	Selects read or write
16	D/I	H/L	H:Data;L:Instruction code
17	E	H/L	Enable Input
18	FGND	0V	Frame Ground
17	A	4.2V	Power supply for LED(+) Backlight
18	K	0V	Power supply for LED(-) Backlight

6.3 Interface Timing Chart

Interface AC Characteristics

MPU Interface (GND = 0V, $V_{\rm CC}$ = 2.7 to 5.5V, Ta = -30 to +75°C) *

Item	Symbol	Min	Тур	Max	Unit	Note
E cycle time	t _{cyc}	1000	_	_	ns	Fig. 10, Fig. 11
E high level width	P _{WEH}	450	_	_	ns	
E low level width	P_{WEL}	450	_	_	ns	
E rise time	t _r	_	_	25	ns	
E fall time	t _f	_	_	25	ns	
Address setup time	t _{AS}	140	_	_	ns	
Address hold time	t _{AH}	10	_	_	ns	
Data setup time	t _{DSW}	200	_	_	ns	Fig. 10
Data delay time	t _{DDR}	_	_	320	ns	Fig. 11, Fig. 12
Data hold time (write)	t _{DHW}	10	_		ns	Fig. 10
Data hold time (read)	t _{DHR}	20	_	_	ns	Fig. 11

Note: Specified at +75°C for die products.

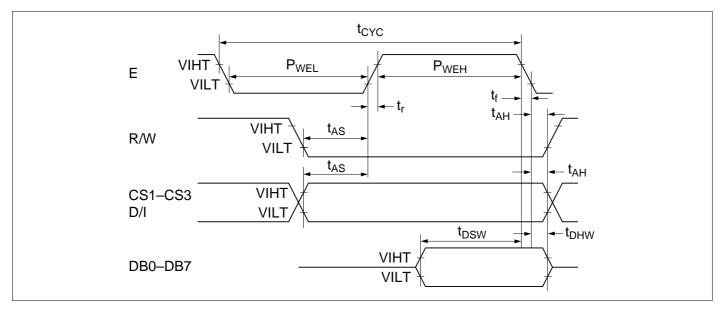


Figure 9 MPU Write Timing

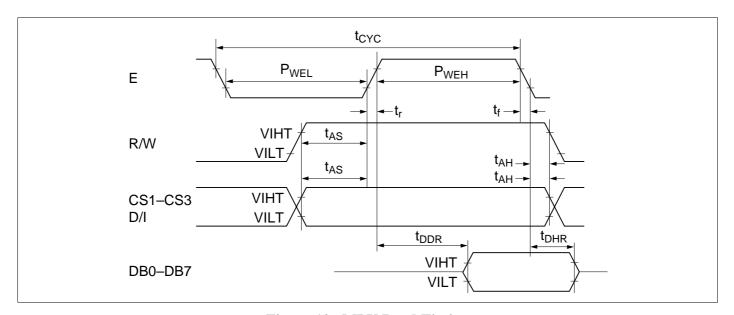


Figure 10 MPU Read Timing

6.4 Instruction Code

					O	Code						
Instructions	R/W	Δ	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Functions	
Display on/off	0	0	0	0	_	-	-	-	-	1/0	Controls display on/off. RAM data and internal status are not affected. 1: on, 0: off.	internal
Display start line	0	0	~	_	Displ	ay star	Display start line (0–63)	-63)			Specifies the RAM line displayed at the top of the screen.	top of the
Set page (X address)	0	0	~	0	_	~	_	Page (0–7)	(2-0)		Sets the page (X address) of RAM at the page (X address) register.	ne page
Set Y address	0	0	0	_	Yad	Y address (0–63))-63)				Sets the Y address in the Y address counter.	unter.
Status read	_	0	Busy	0	NO	Reset 0	t 0	0	0	0	Reads the status.	
					OFF						RESET 1: Reset 0: Normal	
											ON/OFF 1: Display off 0: Display on	
											Busy 1: Internal operation 0: Ready	
Write display data	0	-	Write data	data							Writes data DB0 (LSB) Has access to the to DB7 (MSB) on the data bus into display RAM specified in advance. After the	s to the the display fied in fter the
Read display data	~	_	Read data	data							Reads data DB0 (LSB) increased by 1. increased by 1. display RAM to the data bus.	nddress is by 1.
:	-	١,	,	 	 	·						

Busy time varies with the frequency (f_{CLK}) of Ø1, and Ø2. (1/f_{CLK} \leq TBUSY \leq 3/f_{CLK})

7. Optical Characteristics

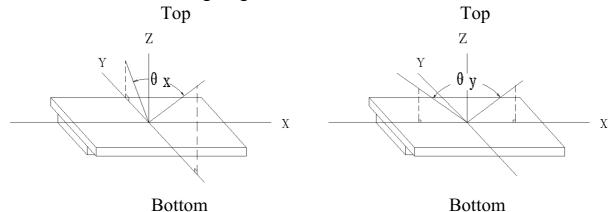
7.1 Optical Characteristics

 $Ta=25^{\circ}C$

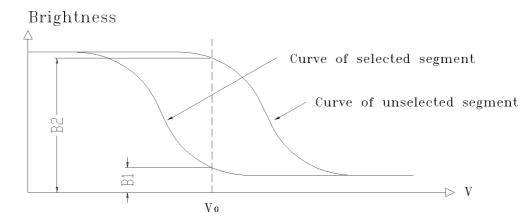
Item		Symbol	Condition		Min.	Тур.	Max.	Unit
Vioving	Anala	$\theta_{\mathbf{x}}$	C >2	θ _y =0°	-30		20	Dag
Viewing Angle		θу	Cr≥2	θ _x =0°	-30)	30	Deg
Contrast Ratio		Cr	$\theta_{\mathbf{x}} = \theta_{\mathbf{y}} = 0$	=0°	3.0	-	-	
Response	Turn on	Ton	$\theta_{\mathbf{x}} =$	=0°	-	-	300	
Time	Turn off	Toff	θ_{y} =	=0°	-	-	300	ms

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle



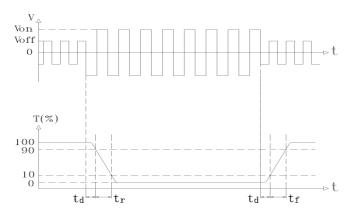
7.2.2 Definition of Contrast Ratio



Contrast Ratio =
$$B2/B1 = \frac{unselected state brightness}{selected state brightness}$$

Measuring Conditions:

1) Ambient Temperature: 25 °C; 2) Frame frequency: 64Hz 7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$ Turn off time: $t_{off} = t_d + t_f$ Measuring Condition:

1) Operating Voltage: 12.0V 2) Frame frequency: 64Hz

8. Reliability

8.1 Content of Reliability Test

Ta=25°C

			<u> </u>
No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	80°C
	Storage	storage temperature for a long time	96H
2	Low Temperature	Endurance test applying the low	-30°C
	Storage	storage temperature for a long time	96H
		Endurance test applying the	
3	High Temperature	electric stress (voltage & current)	70 ℃
	Operation	and the thermal stress to the	96H
		element for a long time	9011
	Low Temperature	Endurance test applying the	-20° ℃
4	Operation	electric stress under low	96H
	Орегиноп	temperature for a long time	
	High Temperature	Endurance test applying the high	40 ℃
5	/Humidity Storage	temperature and high humidity	90%RH
	Trainially Storage	storage for a long time	96H
	Temperature Cycle	Endurance test applying the low	
		and high temperature cycle	20°C /60°C
6		-20°C ← 25°C ← 60°C ← 25°C	-20°C/60°C
		30min 5min 30min 5min ←———————————————————————————————————	10 cycles
		1 cycle	
	Viloudian Tari	Endones test and in the	10Hz~150Hz,
7	Vibration Test	Endurance test applying the	50m/s^2 ,
	(package state)	vibration during transportation	40min
	Shock Test	Endurance test applying the shock	Half- sine wave,
8	(package state)	during transportation	100m/s^2 ,
	(Package state)	Ų 1	11ms
	Atmospheric	Endurance test applying the	40kPa
9	Pressure Test	atmospheric pressure during	40KFa 16H
		transportation by air	1011

8.2 Failure Judgment Criterion

Criterion			T	est l	Iter	n N	o.			Failure Judgement Criterion	
Item	1	2	3	4	5	6	7	8	9	randre Judgement Criterion	
Basic Specification	1	1	1	1	1	V	V	V	√	Out of the basic Specification	
Electrical specification	V	V	1	V	V					Out of the electrical specification	
Mechanical Specification							1	V		Out of the mechanical specification	
Optical Characteristic	V	1	1	1	1	1			√	Out of the optical specification	
Note	Fo	or te	est i	ten	n re	fer	to 8	3.1			
Remark			sp fica			atio	n =	= (Opti	ical specification + Mechanical	

9. QUALITY LEVEL

Examination	At T _a =25°C		Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL		
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Ap	See Appendix A			Major 1.0 Minor 2.5		
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Ap	pendix B		II	Major 1.0 Minor 2.5		

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

10. Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature: $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

Appendix AInspection items and criteria for appearance defects

Items	Contents	Criteria			
Leakage		Not permitted	l		
Rainbow		According to	the lir	mit specimen	
	Wrong polarizer attachment	Not permitted	1		
Polarizer	Bubble between	Not counted		Max. 3 defects al	llowed
	polarizer and glass	ф<0.3mm		0.3mm≤¢≤0.5r	nm
	Scratches of polarizer	According to	the lir	nit specimen	
Black spot		Not counted	Max	. 3 spots allowed	
(in viewing area)		X<0.2mm	0.2m	nm≤X≤0.5mm	Max. 3
	lα	X=(a+b)/2		spots (lines)	
Black line (in viewing		Not counted	Max. 3 lines allowed		allowed
area)	b - b	a<0.02mm	0.021	mm≤a≤0.05mm b≤2.0mm	
Progressive cracks		Not permitted	I		

Appendix BInspection items and criteria for display defects

Items		Contents	Criteria		
Open segmen	nt or ope	n common	Not permitted		
Short			Not permitted		
Wrong view	ing angle		Not permitted		
Contrast radi	io uneve	n	According to	the limit specimen	
Crosstalk			According to	the limit specimen	
		+ + a	Not counted	Max.3 dots allowed	
	2		X<0.1mm	0.1mm≤X≤0.2mm	
Pin holes	1	Q-1-	X=(a+b)/2		Max.3 dots
and cracks in segment		- -]	Not counted	Max.2 dots allowed	allowed
(DOT)		+	A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm	
Black spot			Not counted	Max.3 spots allowed	
(in viewing area)			X<0.1mm	0.1mm≤X≤0.2mm	
urcu)	-	α	X=(a+b)/2	Max.3 spots	
Black line	<u>+</u> _(Not counted	Max.3 lines allowed	(lines) allowed
(in viewing area)	ا	b	a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm	

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria		
Transfor- mation of segment		Not counted	Max. 2 defects allowed	
		x<0.1mm	0.1mm≤x≤0.2mm	
		x=(a+b)/2		
				Max.3 defects
		Not counted	Max. 1 defects allowed	allowed
		a<0.1mm	0.1mm≤a≤0.2mm D>0	
		Max.2 defects allowed 0.8W≤a≤1.2W a=measured value of width W=nominal value of width		