



60V INPUT, 12V 15mA REGULATOR TRANSISTOR

Description

The ZXTR2112FQ monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 12V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

This linear regulator is designed to meet the stringent requirements of automotive applications.

Applications

Supply voltage regulation for:

- 24V to 12V Rails
- Other Customized Input Rails

Features

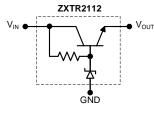
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage 15 to 60V (For Regulated Output Voltage)
- Output Voltage 12V ± 10%
- Fully Integrated into a SOT23 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

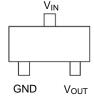
- Case: SOT23
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.008 grams (Approximate)



Top View



Internal Device Schematic



Top V

Top Vie	w
Pin-Ou	ıt

Pin Name	Pin Function
VIN	Input Supply
GND	Power Ground
Vout	Voltage Output

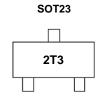
Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTR2112FQ-7	Automotive	2T3	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.htmlfor more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



2T3 = Product Type Marking Code



Absolute Maximum Ratings (Voltage relative to GND, @TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	VIN	-0.3 to 60	V
Continuous Input & Output Current	I _{IN} , I _{OUT}	320	mA
Peak Pulsed Input & Output Current	I _{IM} , I _{OM}	2	Α
Maximum Voltage applied to V _{OUT}	V _{OUT(max)}	Smaller of V _{IN} +5V or 17V	V

Maximum Current (@V_{IN} = 24V) (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 8)	Іоит	50	mA
Duland Output Current	(Note 9)	,	2,000	A
Pulsed Output Current	(Note 10)	Іом	500	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 6)	Pn	625	mW
Power Dissipation	(Note 7)	PD	500	IIIVV
Thormal Posistance, Junction to Ambient	(Note 6)	D	200	2011
Thermal Resistance, Junction to Ambient	(Note 7)	R _{0JA}	250	
Thermal Resistance, Junction to Lead	(Note 11)	$R_{\theta JL}$	197	°C/W
Thermal Resistance, Junction to Case	(Note 11)	R ₀ JC	17	
Maximum Operating Junction and Storage Temper	$T_{J_1}T_{STG}$	-65 to +150	°C	

ESD Ratings (Note 12)

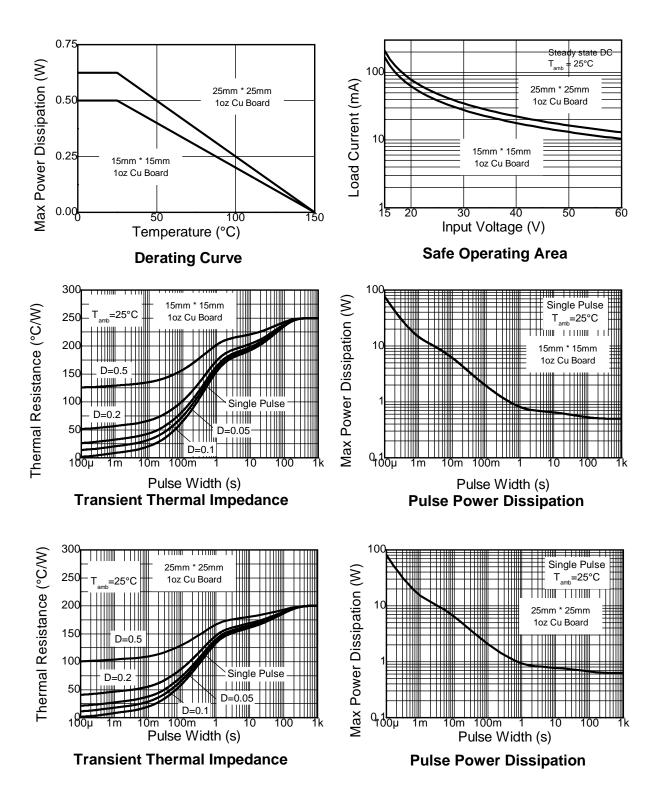
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

Notes:

- 6. For a device mounted with the V_{IN} lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
- 7. Same as Note 6, except mounted on 15mm x 15mm 1oz copper.
- 8. Same as Note 6, whilst operating at V_{IN}=24V. Refer to Safe Operating Area for other Input Voltages.
- 9. Same as Note 6, except measured with a single pulse width = $100\mu s$ and $V_{IN}=24V$.
- 10. Same as Note 6, except measured with a single pulse width = 10ms and $V_{\mbox{\scriptsize IN}}\!=\!24V.$
- 11. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (at the end of the V_{IN} lead). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
- 12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 13)	Vout	10.8	12	13.2	>	$V_{IN} = 24V$, $I_{OUT} = 15mA$
		_	20	50		$V_{IN} = 18$ to 24V, $I_{OUT} = 15$ mA
Line Regulation (Notes 13 & 14)	ΔV_OUT	_	130	1	mV	V _{IN} = 18 to 60V, I _{OUT} = 15mA
		_	150	1		$V_{IN} = 15$ to 60V, $I_{OUT} = 15$ mA
Temperature Coefficient	ΔV _{OUT} /ΔΤ		10.4		mV/°C	$T_J = -40$ °C to $+125$ °C
Temperature Coefficient	Δνουτ/Δι		10.4		IIIV/ C	$V_{IN} = 24V$, $I_{OUT} = 15mA$
Load Regulation (Notes 13 & 15)	ΔVουτ	_	-30	-100	mV	$I_{OUT} = 10 \text{ to } 20\text{mA}, V_{IN} = 24\text{V}$
Load Negalation (Notes 15 & 15)	ΔVOUT		-190	-300	111 V	$I_{OUT} = 0.1 \text{ to } 50\text{mA}, V_{IN} = 24\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	15	1	ı	>	
Quiescent Current	1-	_	160	360	μA	$V_{IN} = 15V$, $I_{OUT} = 10\mu A$
Quiescent Current	ΙQ	_	3,500	6,000	μΑ	$V_{IN} = 60V$, $I_{OUT} = 10\mu A$
Dawar Cunnly Dejection Datio			50		dB	C _{OUT} = 100nF, I _{OUT} = 15mA,
Power Supply Rejection Ratio	ΔVΙΝ / ΔVουτ		50		uБ	$V_{OUT} = 12V$, $V_{IN} = 15$ to 60V, $f = 100$ Hz

Notes: 13. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.

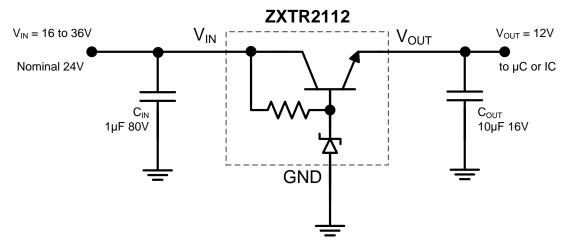
14. Line regulation: $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 24V) - V_{OUT} (@V_{IN} = 18V)$

 $\Delta V_{OUT} = V_{OUT}(@V_{IN}=60V) - V_{OUT}(@V_{IN}=15V)$ $\Delta V_{OUT} = V_{OUT}(@V_{IN}=60V) - V_{OUT}(@V_{IN}=18V)$

15. Load regulation: ΔV_{OUT}= V_{OUT}(@I_{OUT}= 20mA)- V_{OUT}(@I_{OUT}= 10mA)

 $\Delta V_{OUT} = V_{OUT} (@I_{OUT} = 50 \text{mA}) - V_{OUT} (@I_{OUT} = 0.1 \text{mA})$

Typical Application Circuit



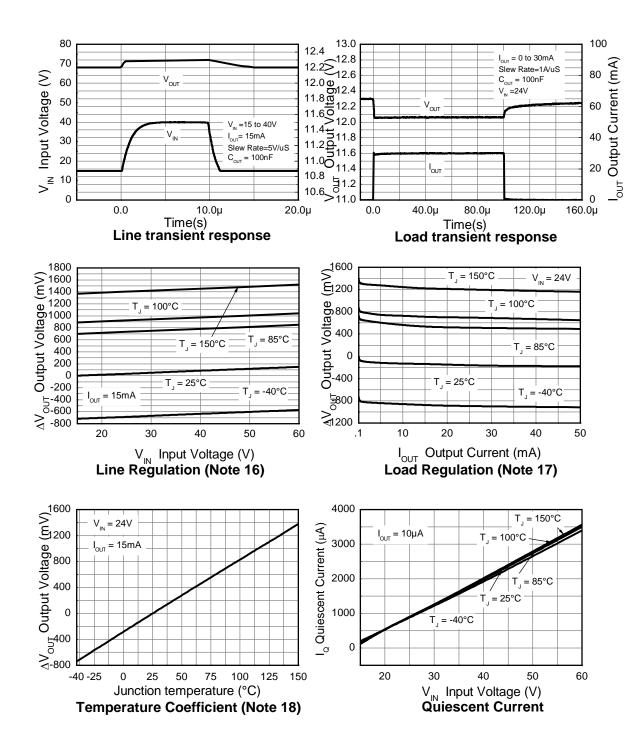
Example of a 12V regulated supply from a nominal 24V for powering a Controller IC.

Pin Functions

Pin Name	Pin Function	Notes
V _{IN}	Input Supply	Input voltage can vary from -0.3V to 60V with respect to GND; for V_{OUT} regulated then 15V \leq $V_{IN} \leq$ 60V. It is recommended to connect a 1 μ F capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
Vouт	Voltage Output	Outputs a regulated 12V when 15V \leq V _{IN} \leq 60V. When V _{IN} $<$ 15V, then V _{OUT} maximum = V _{IN} $-$ 1V. The pin can be pulled high to a maximum of +17V with respect to GND, or +5V with respect to V _{IN} , whichever is lower. It is recommended to connect a 10 μ F capacitor to GND and a minimum of 10 μ A to be drawn from V _{OUT} to maintain regulation.



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)



Notes: 16. Line Regulation $\Delta VOUT = VOUT - VOUT(@VIN = 15V, IOUT = 15mA, TJ = +25°C)$.

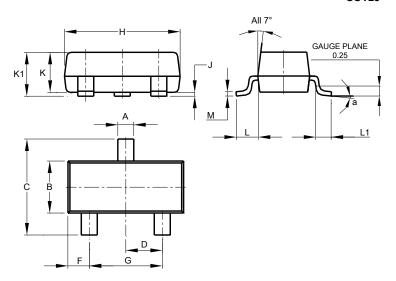
^{17.} Load Regulation ΔVOUT = VOUT – VOUT(@ VIN = 24V, IOUT = 0.1mA, TJ = +25°C).
18. Temperature Coefficient ΔVOUT = VOUT – VOUT(@ VIN = 24V, IOUT = 15mA, TJ = +25°C).



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

SOT23

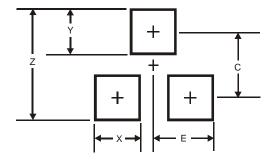


	SOT23					
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	a 8°					
All	Dimens	ions in	mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

SOT23



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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