

ZXTN19060CG 60V NPN low sat medium power transistor in SOT223

Summary

 $BV_{CEO} > 60V$

BV_{CEX} > 160V

 $BV_{ECO} > 6V$

 $I_{C(cont)} = 7A$

V_{CE(sat)} < 50mV @ 1A

 $R_{CE(sat)} = 30m\Omega$

 $P_{D} = 3.0W$



Complementary part number ZXTP19060CG

Description

Packaged in the SOT223 outline this new low saturation NPN transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

Features

- · Higher power dissipation SOT223 package
- · High peak current
- Low saturation voltage
- 160V forward blocking voltage
- · 6V reverse blocking voltage

Applications

- · Motor drive
- · Lamp, relay and solenoid drive

Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTN19060CGTA	7	12	1000

□с ∏В

Device marking

ZXTN19 060C

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Collector-Base voltage	V _{CBO}	160	V
Collector-Emitter voltage (forward blocking)	V _{CEX}	160	V
Collector-Emitter voltage	V _{CEO}	60	V
Emitter-Collector voltage (reverse blocking)	V _{ECX}	6	V
Emitter-Base voltage	V _{EBO}	7	V
Continuous Collector current ^(c)	I _C	7	Α
Base current	I _B	1	Α
Peak pulse current	I _{CM}	12	Α
Power dissipation at T _A =25°C ^(a)	P _D	1.2	W
Linear derating factor		9.6	mW/°C
Power dissipation at T _A =25°C ^(b)	P _D	1.6	W
Linear derating factor		12.8	mW/°C
Power dissipation at T _A =25°C ^(c)	P _D	3.0	W
Linear derating factor		24	mW/°C
Power dissipation at T _A =25°C ^(d)	P _D	5.3	W
Linear derating factor		42	mW/°C
Power dissipation at T _C =25°C ^(e)	P _D	10.2	W
Linear derating factor		81	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

Thermal resistance

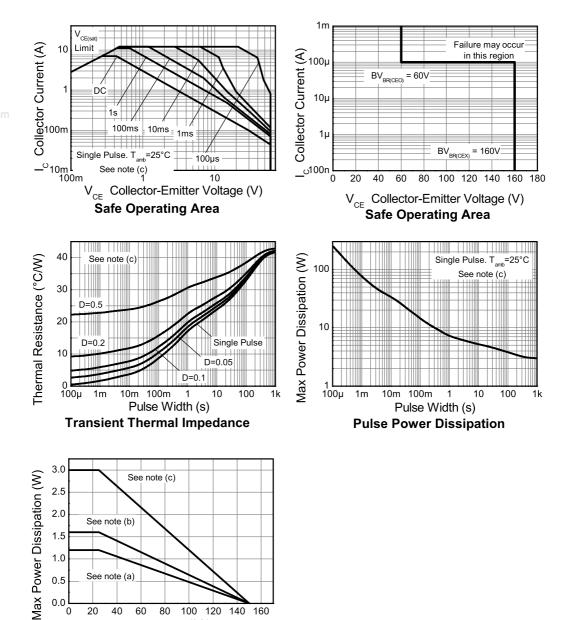
Parameter	Symbol	Limit	Unit
Junction to ambient ^(a)	$R_{\Theta JA}$	104	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	78	°C/W
Junction to ambient ^(c)	$R_{\Theta JA}$	42	°C/W
Junction to ambient ^(d)	$R_{\Theta JA}$	23.5	°C/W
Junction to case ^(e)	$R_{\Theta JC}$	12.3	°C/W

NOTES

- (a) For a device surface mounted on 15mm x 15mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) Mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (c) Mounted on 50mm x 50mm x 0.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.
- (d) As (c) above measured at t<5 seconds.
- (e) Junction to case (collector tab). Typical

Thermal characteristics

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Temperature (°C) **Derating Curve**

Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated).

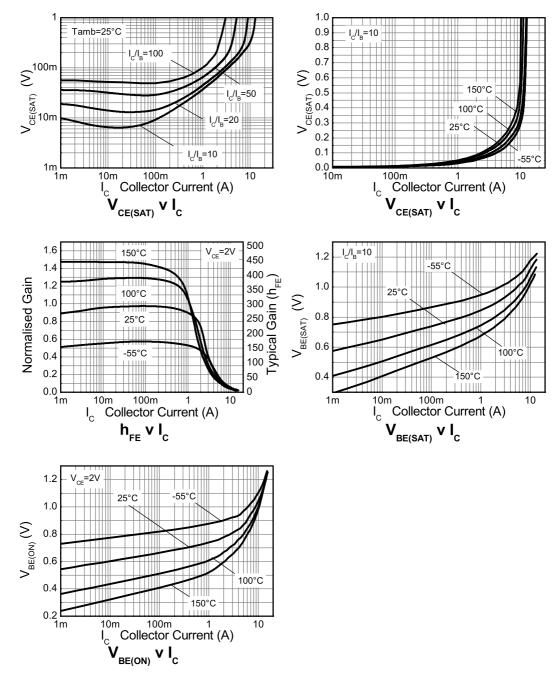
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-Base breakdown voltage	BV _{CBO}	160	200		V	$I_C = 100 \mu A$
Collector-Emitter breakdown voltage (forward blocking)	BV _{CEX}	160	200		V	I_C = 100 μ A, R_{BE} < 1 $k\Omega$ or -1V < V_{BE} < 0.25V
Collector-Emitter breakdown voltage	BV _{CEO}	60	75		V	I _C = 10mA ^(*)
Emitter-Collector Breakdown Voltage (Reverse Blocking)	BV _{ECX}	6	7		V	$I_E = 100 \mu A, R_{BC} < 1 k\Omega$ or $0.25 V > V_{BC} > -0.25 V$
Emitter-Collector breakdown voltage (reverse blocking)	BV _{ECO}	6	7		V	I _E = 100μA
Emitter-Base breakdown voltage	BV _{EBO}	7	8.3		V	$I_E = 100 \mu A$
Collector-Base cut-off current	I _{CBO}		<1	50 0.5	nA μA	V _{CB} = 160V V _{CB} = 160V, T _{amb} = 100°C
Collector-Emitter cut-off current	I _{CEX}			100	nA	$V_{CE} = 160V, R_{BE} < 1k\Omega$ or $-1V < V_{BE} < 0.25V$
Emitter cut-off current	I _{EBO}		<1	50	nA	V _{EB} = 5.6V
Collector-Emitter	V _{CE(sat)}		37	50	mV	$I_C = 1A$, $I_B = 100 \text{mA}^{(*)}$
saturation voltage			105	155	mV	$I_C = 1A$, $I_B = 10mA^{(*)}$
			110	150	mV	$I_C = 2A$, $I_B = 40mA^{(*)}$
			200	300	mV	$I_C = 7A$, $I_B = 700 \text{mA}^{(*)}$
Base-Emitter saturation voltage	V _{BE(sat)}		1050	1150	mV	$I_C = 7A$, $I_B = 700 \text{mA}^{(*)}$
Base-Emitter turn-on voltage	V _{BE(on)}		960	1050	mV	$I_C = 7A$, $V_{CE} = 2V^{(*)}$
Static forward current	h _{FE}	200	300	500		$I_C = 100 \text{mA}, V_{CE} = 2V^{(*)}$
transfer ratio		160	220			$I_C = 2A, V_{CE} = 2V^{(*)}$
		25	40			$I_C = 7A$, $V_{CE} = 2V^{(*)}$
Transition frequency	f _T		130		MHz	I _C = 50mA, V _{CE} = 10V f = 100MHz
Input capacitance	C _{ibo}		310	400	pF	$V_{EB} = 0.5V, f = 1MHz^{(*)}$
Output capacitance	C _{obo}		19.7	25	pF	V _{CB} = 10V, f = 1MHz ^(*)
Delay time	t _d		27.3		ns	
Rise time	t _r		13.2		ns	$I_C = 500 \text{mA}, V_{CC} = 10 \text{V},$
Storage time	t _s		682		ns	$I_{B1} = -I_{B2} = 50 \text{mA}$
Fall time	t _f		90.3		ns	

NOTES

(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$

Typical characteristics

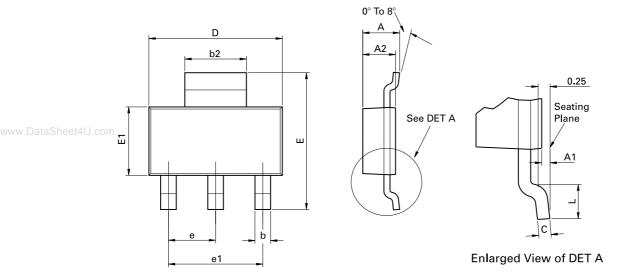
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Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

Dim.	Millin	neters	Inc	hes	Dim.	Millimeters		Inches	
	Min.	Max.	Min.	Max.	Diiii.	Min.	Max.	Min.	Max.
Α	-	1.80	-	0.071	D	6.30	6.70	0.248	0.264
A1	0.02	0.10	0.0008	0.004	е	2.30 BSC		0.0905 BSC	
A2	1.55	1.65	0.0610	0.0649	e1	4.60 BSC		0.181 BSC	
b	0.66	0.84	0.026	0.033	E	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
С	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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