

Product Summary

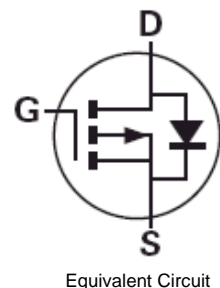
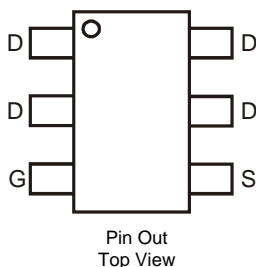
BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
-20V	200mΩ @ V _{GS} = -4.5V	-2.3A

Description

This new generation of high density MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

Applications

- DC - DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control



Features and Benefits

- Low On-resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

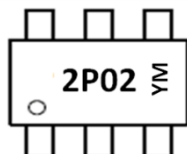
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.018 grams (Approximate)

Ordering Information (Note 4)

Part Number	Reel Size (inch)	Tape Width (mm)	Quantity Per Reel
ZXM62P02E6TA	7	8	3,000
ZXM62P02E6TC	13	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



2P02 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: C = 2015)
 M or \bar{M} = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Code	C	D	E	F	G	H	I	J	K	L	M	N

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Absolute Maximum Ratings

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current	V _{GS} = -4.5V	T _A = +25°C (Note 6)	I _D	-2.3	A
		T _A = +70°C (Note 6)		-1.7	
Pulsed Drain Current		(Note 7)	I _{DM}	-13	A
Continuous Source Current (Body Diode)		(Note 6)	I _S	-1.9	A
Pulsed Source Current (Body Diode)		(Note 7)	I _{SM}	-13	A
Power Dissipation at T _A = +25°C Linear Derating Factor		(Note 5)	P _D	1.1 8.8	W mW/°C
Power Dissipation at T _A = +25°C Linear Derating Factor		(Note 6)	P _D	1.7 13.7	W mW/°C
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

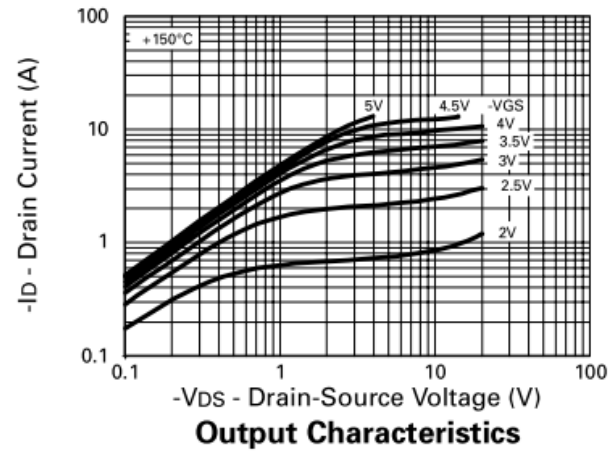
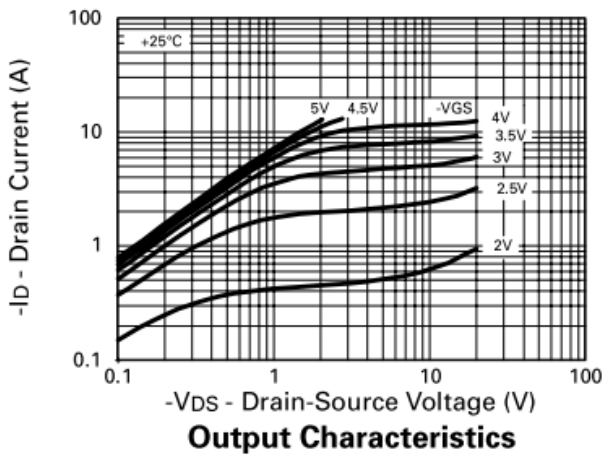
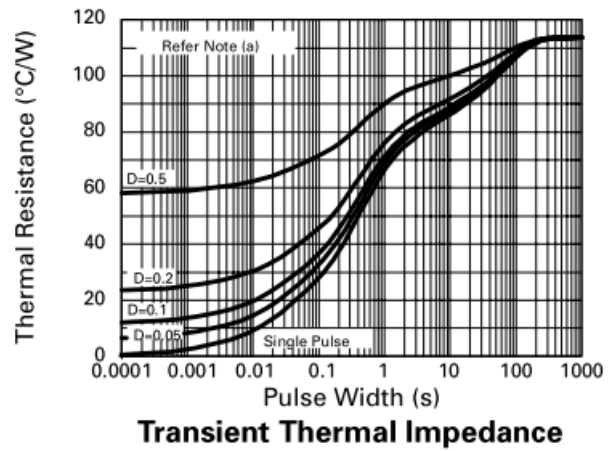
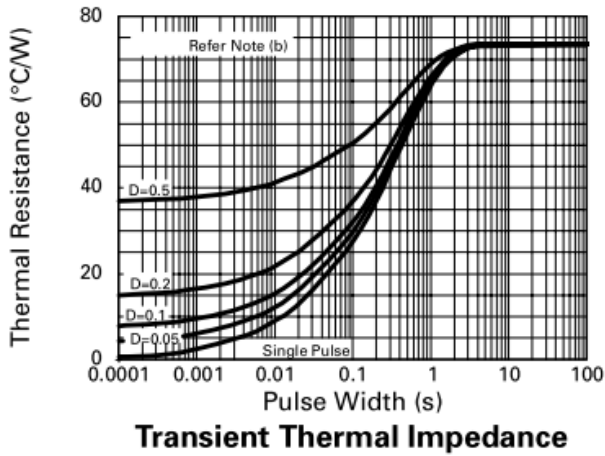
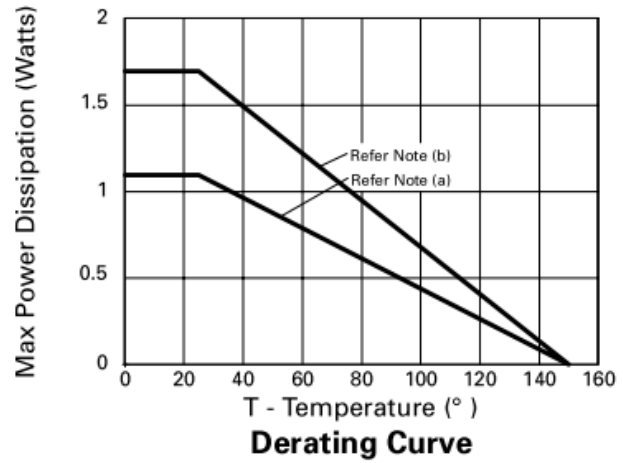
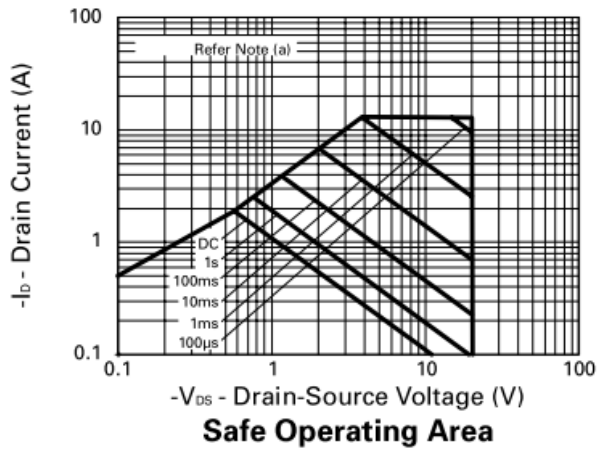
Thermal Resistance

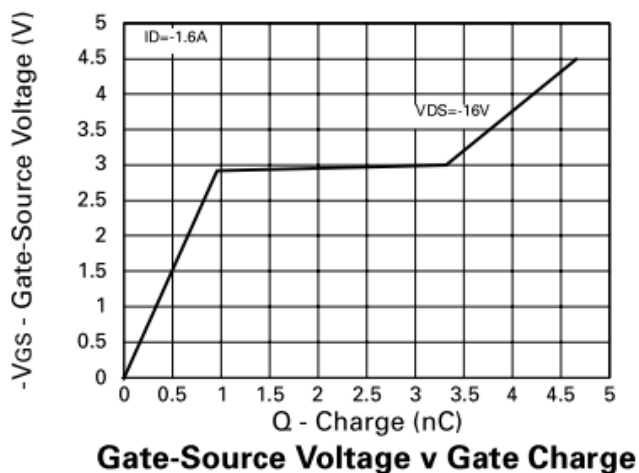
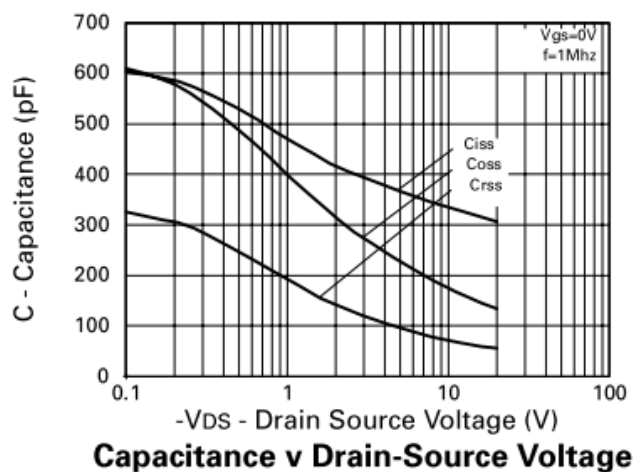
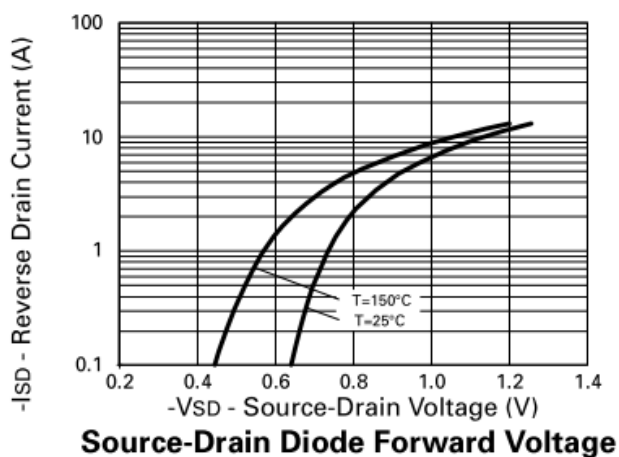
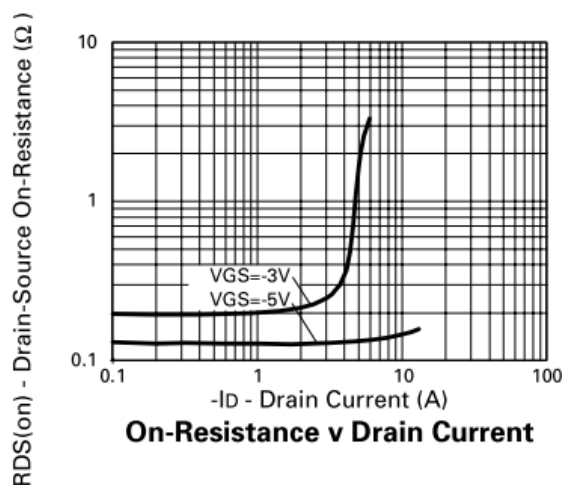
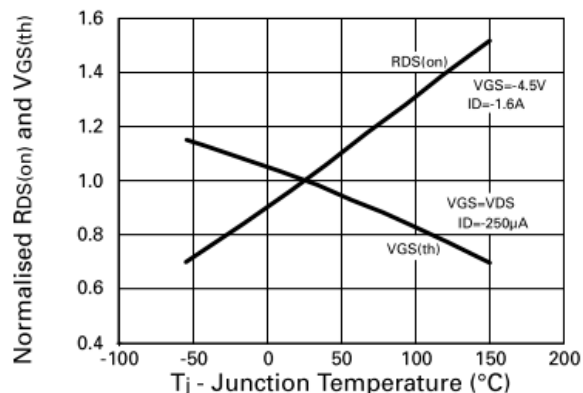
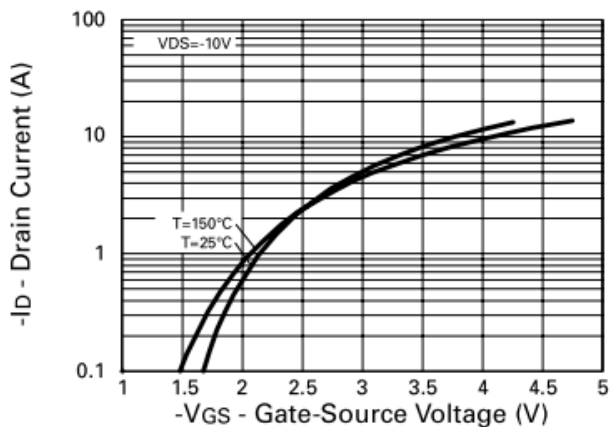
Characteristic		Symbol	Value	Unit
Junction to Ambient	(Note 5)	$R_{\theta JA}$	113	$^{\circ}C/W$
	(Note 6)		73	

Electrical Characteristics (@ $T_A = +25^{\circ}C$, unless otherwise stated.)

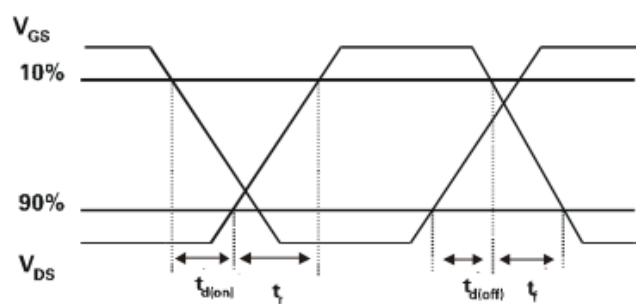
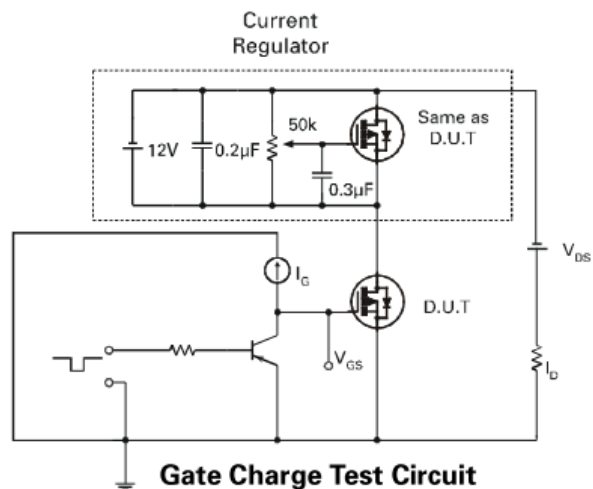
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
STATIC						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	I _D = -250μA, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
Gate Threshold Voltage	V _{GS(TH)}	-0.7	—	—	V	I _D = -250μA, V _{DS} = V _{GS}
Static Drain-Source On-Resistance (Note 8)	R _{DS(ON)}	—	—	0.2	Ω	V _{GS} = -4.5V, I _D = -1.6A
				0.375		V _{GS} = -2.7V, I _D = -0.8A
Forward Transconductance (Note 10)	g _{fs}	1.5	—	—	S	V _{DS} = -10V, I _D = -0.8A
DYNAMIC (Note 10)						
Input Capacitance	C _{iss}	—	320	—	pF	V _{DS} = -15V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	150	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	75	—	pF	
SWITCHING (Notes 9 and10)						
Total Gate Charge	Q _g	—	—	5.8	nC	V _{DS} = -16V, V _{GS} = -4.5V I _D = -1.6A (Refer to test circuit)
Gate-Source Charge	Q _{gs}	—	—	1.25	nC	
Gate-Drain Charge	Q _{gd}	—	—	2.8	nC	
Turn-On Delay Time	t _{D(ON)}	—	4.1	—	ns	V _{DD} = -10V, I _D = -1.6A, R _G = 6Ω, R _D = 6.1Ω (Refer to test circuit)
Turn-On Rise Time	t _R	—	15.4	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	12.0	—	ns	
Turn-Off Fall Time	t _F	—	19.2	—	ns	
SOURCE-DRAIN DIODE						
Diode Forward Voltage (Note 8)	V _{SD}	—	—	-0.95	V	T _J = +25°C, I _S =-1.6A, V _{GS} =0V
Reverse recovery time (Note 10)	t _{RR}	—	22.5	—	ns	T _J = +25°C, I _F =-1.6A, di/dt= 100A/μs
Reverse recovery charge (Note 10)	Q _{RR}	—	10.4	—	nC	

- Notes:
- For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 - For a device surface mounted on FR-4 PCB measured at $t \leq 5$ secs.
 - Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.
 - Measured under pulsed conditions. Width= 300 μs ; duty cycle $\leq 2\%$.
 - Switching characteristics are independent of operating junction temperatures.
 - For design aid only, not subject to production testing.

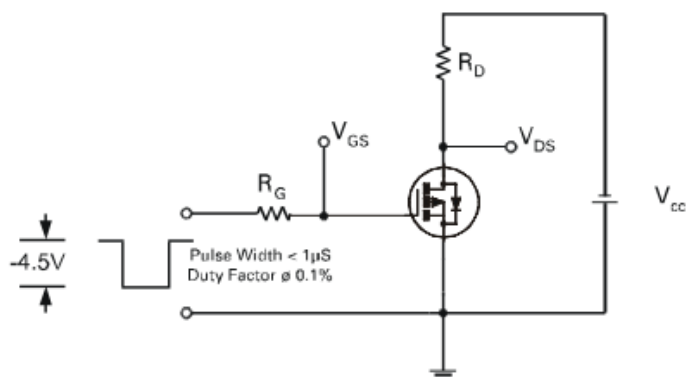




Basic Gate Charge Waveform



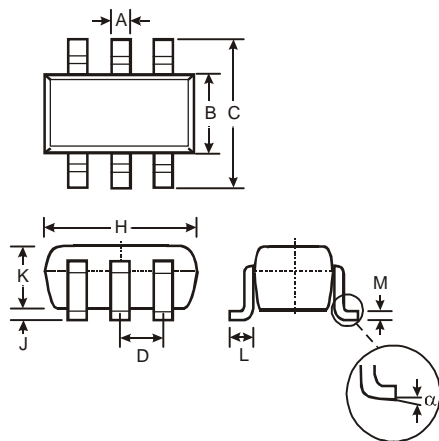
Switching Time Waveforms



Switching Time Test Circuit

Package Outline Dimensions

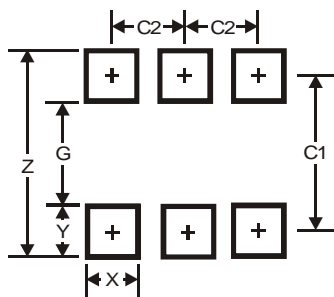
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
α	0°	8°	—
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

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