

ZSP4422A Electroluminescent Lamp Driver Datasheet

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ASIX ELECTRONICS CORPORATION

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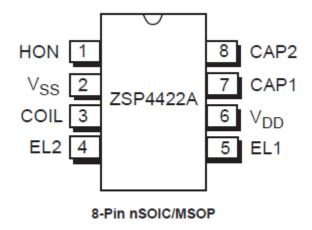


Features

- +2.2V to +5.0V battery operation
- 50nA typical standby current
- High voltage output typical 140V_{PP}
- · Internal oscillator

Applications

- PDAs
- · Cellular phones
- · Remote controls
- · Handheld computers



General Description

The ZSP4422A is a high voltage output DC-AC converter that can operate from a +2.2V to +5.0V power supply. The ZSP4422A is designed with our proprietary high voltage BiCMOS technology and is capable of supplying typical 140VPP signals, making it ideal for driving small electroluminescent lamps. The device features 50nA (typical) standby current, for use in low power portable products. One external inductor is required to generate the high voltage, and an external capacitor is used to select the oscillator frequency. The ZSP4422A is offered in an 8-pin narrow SOIC package or an 8-pin MSOP package. For delivery in die form, please consult the factory.







Absolute Maximum Ratings

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

V _{DD}	+7.0V
Input Voltages/Currents HON (pin 1) COIL (pin 3)	
Lamp Output	230V _{pp}
Operating Temperature	40°C to +85°C
Storage Temperature	–65°C to +150°C

Power Dissipation Per Package

8-pin nSOIC	(derate	6.14mW/°C	above	+70°C	500mW
8-pin MSOP (derate	4.85mW/°C	above	+70°C)	400mW

Storage Considerations

Storage in a low humidity environment is preferred. Large high density plastic packages are moisture sensitive and should be stored in Dry Vapor Barrier Bags. Prior to usage, the parts should remain bagged and stored below 40°C and 60%RH. If the parts are removed from the bag, they should be used within 168 hours or stored in an environment at or below 20%RH. If the above conditions cannot be followed, the parts should be baked for 12 hours at 125°C in order to remove moisture prior to soldering. ASIX ships product in Dry Vapor Barrier Bags with a humidity indicator card and desiccant pack. The humidity indicator should be below 30%RH. The MSL of this product is 3.

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1 Electrical Characteristics

TA = +25°C, VDD = +3.0V, CLAMP = 17nF with 100Ω series resistor, Coil = 5mH (Rs = 18 Ω); Cosc = 100pF, unless otherwise noted.

Symbol	Parameter	Condition	Min	Тур	Max	Units
V_{DD}	Supply Voltage		2.2	3.0	5.0	V
I _{COIL} + I _{DD}	Supply Current	VDD = +3.0V, VHON = +3.0V		20	30	mΑ
		VDD = +5.0V, $VHON = +5.0V$		40	60	mA
V _{COIL}	Coil Voltage		V_{DD}		5.0	V
V _{HON}	HON Input Voltage					
11011	LOW: EL off		-0.25	0	0.25	V
	HIGH: EL on		V _{DD} - 0.25	V_{DD}	V _{DD} +0.25	V
I _{HON}	HON Current	VDD = VHON = +3.0V		25	60	uA
$I_{SD} = I_{COIL} + I_{DD}$	Shutdown Current	VDD = +3.0V, $VHON = LOW$		50	500	nA
		VDD = +5.0V, $VHON = LOW$		0.3		uA
INDUCTOR DRIV	/E	·				
fCOIL = fLAMP * 32	Coil Frequency			11.2		KHz
	Coil Duty Cycle			94		%
IPK-COIL	Peak Coil Current	Guaranteed by design			60	mA
EL LAMP OUTP	ÚT	<u> </u>	•	•	•	
fLAMP	EL Lamp Frequency	$TA = +25^{\circ}C, VDD = +3.0V$	250	350	500	Hz
		TA = -40°C to +85°C, $VDD = +3.0V$	200		700	Hz
VPP	Peak-to-Peak Output	$TA = +25^{\circ}C$, $VDD = +2.2V$	60	80		V
	Voltage	TA = +25°C, $VDD = +3.0$ V	110	140		V
		$TA = +25^{\circ}C$, $VDD = +5.0V$	180	200		V
		TA = -40°C to $+85$ °C, $VDD = +3.0V$	70	85		V





Block Diagram

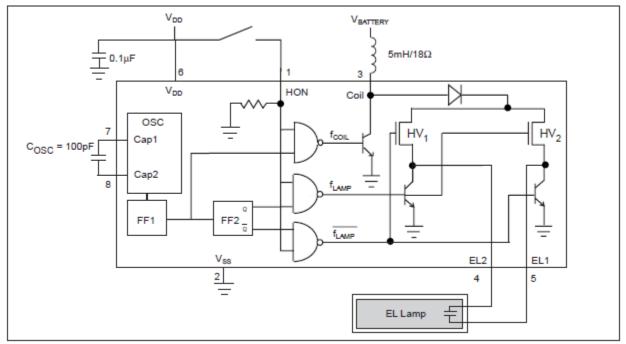
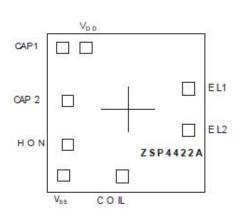


Figure 1. ZSP4422A Block Diagram

Bonding Diagram/Die Photo

Bonding Diagram



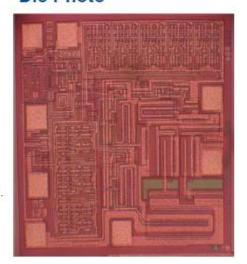
PAD	X	Υ
EL1	556.5	179.0
EL2	556.2	-151.0
COIL	-19.5	-517.0
V _{ss}	-568.0	-517.0
HON	-549.0	-256.5
CAP2	-549.0	93.5
CAP1	-568.0	-516.5
V _{D D}	-349.0	517.0

NOTES:

- 1. Dimensions a re in microns unless othe rwise noted.
 2. Bonding pads a re 125x125 typical
 3. Outside dimensions a re maximum, including scribe area.
 4. Die thickness is 11 mils +/- 1.
 5. Pad center coordinates are relative to die center.

- 6. Die substrate down-bonds to Vss (GND)
- Die mask num ber is MS129.
- 8. Die size 1346 x 1447 (53 x 57 mils)

Die Photo



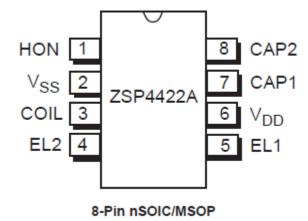




4 Pin Description

Pin Number	Pin Name	Pin Description
1	HON	Enable for driver operation: high = active; low = inactive
2	V _{SS}	Power supply common: connect to ground.
3	COIL	Coil input: connect coil from VDD to this pin.
4	EL2	Lamp driver output 2: connect to EL lamp.
5	EL1	Lamp driver output 1: connect to EL lamp.
6	V_{DD}	Power supply for driver: connect to system VDD.
7	CAP1	Capacitor Input 1: connect to Cosc.
8	CAP2	Capacitor Input 2: connect to Cosc.

5 Pin Configuration







6 Circuit Description

The ZSP4422A is made up of three basic circuit elements, an oscillator, coil, and switched H-bridge network. The oscillator provides the device with an on-chip clock source used to control the charge and discharge phases for the coil and lamp. An external capacitor connected between pins 7 and 8 allows the user to vary the oscillator frequency from 32KHz to 400KHz. In general, increasing the $C_{\rm OSC}$ capacitor will increase the lamp output.

The suggested oscillator frequency is 90KHz (Cosc =100pF). The oscillator output is internally divided to create two internal control signals, fCoIL and fLAMP. The oscillator output is internally divided down by 8 flip-flops, a 90KHz signal will be divided into 8 frequencies; 45KHz, 22.5KHz, 11.2KHz, 5.6KHz, 2.8KHz, 1.4KHz, 703Hz, and 352Hz. The third flip-flop output (8KHz) is used to drive the coil (see *Figure 1*) and the eighth flip-flop output (250Hz) is used to drive the lamp. Although the oscillator frequency can be varied to optimize the lamp output, the ratio of fCOIL/fLAMP will always equal 32.

The on-chip oscillator of the ZSP4422A can be overdriven with an external clock source by removing the Cosc capacitor and connecting a clock source to pin 8. The clock should have a 50% duty cycle and range from VDD to ground. An external clock signal may be desirable in order to synchronize any parasitic switching noise with the system clock. The maximum external clock frequency that can be supplied is 400KHz.

The coil is an external component connected from VBATTERY to pin 3 of the ZSP4422A. Energy is stored in the coil according to the equation $EL = 1/2(LI)^2$, where I is the peak current flowing in the inductor. The current in the inductor is time dependent and is set by the "ON" time of the coil switch: I = (VL/L)(toN), where VL is the voltage across the inductor. At the moment the switch closes, the current in the inductor is zero and the entire supply voltage (minus the VSAT of the switch) is across the inductor. The current in the inductor will then ramp up at a linear rate. As the current in the inductor builds up, the voltage across the inductor will decrease due to the resistance of the coil and the "ON" resistance of the switch: VL = (VBATTERY - IRL - VSAT). Since the voltage across the inductor is decreasing, the current ramp-rate also decreases which reduces the current in the coil at the end of ton the energy stored in the inductor per coil cycle and therefore the light output. The other important issue is that maximum current (saturation current) in the coil is set by the design and manufacturer of the coil. If the parameters of the application such as VBATTERY, L, RL or ton cause the current in the coil to increase beyond its rated ISAT, excessive heat will be generated and the power efficiency will decrease with no additional light output. The ASIX ZSP4422A is final tested using a 5mH/18 Ω coil from Hitachi Metals. For suggested coil sources see, "Coil Manfacturers."

The supply VDD can range from +2.2V to +5.0V. It is not necessary that VDD = VBATTERY. VBATTERY should not exceed max coil current specification. The majority of the current goes through the coil and is typically much greater than IDD.

The fCOIL signal controls a switch that connects the end of the coil at pin 3 to ground or to open circuit. The fCOIL signal is a 94% duty cycle signal switching at 1/8 the oscillator frequency. For a 64KHz oscillator fCOIL is 8KHz. During the time when the fCOIL signal is high, the coil is connected from VBATTERY to ground and a charged magnetic field is created in the coil. During the low part of fCOIL, the ground connection is switched open, the field collapses and the energy in the inductor is forced to flow toward the high voltage H-bridge switches. fCOIL will send 16 of these charge pulses (see *Figure 5*) to the lamp, each pulse increases the voltage drop across the lamp in discrete steps. As the voltage potential approaches its maximum, the steps become smaller (see *Figure 4*).





The H-bridge consists of two proprietary low on-resistance high-voltage switches. These two switches control the polarity of how the lamp is charged. The high-voltage switches are controlled by the flamp signal which is the oscillator frequency divided by 256. For a 64KHz oscillator, flamp = 256Hz. The direction of current flow is determined by which high-voltage switch is enabled. One full cycle of the H-bridge will create 16 voltage steps from ground to 80V (typical) on pins 4 and 5 which are 180 degrees out of phase from each other (see *Figure 6*). A differential representation of the outputs is shown in *Figure 7*.

Layout Considerations

The ZSP4422A circuit board layout must observe careful analog precautions. For applications with noisy voltage power supplies a 0.1µF low ESR decoupling capacitor must be connected from VDD to ground. Any high voltage traces should be isolated from any digital clock traces or enable lines. A solid ground plane connection is strongly recommended. All traces to the coil or to the high voltage outputs should be kept as short as possible to minimize capacitive coupling to digital clock lines and to reduce EMI emissions.

Electroluminescent Technology

What is Electroluminescence?

An EL lamp is basically a strip of plastic that is coated with a phosphorous material which emits light (fluoresces) when a high voltage (>40V) which was first applied across it, is removed or reversed. Long periods of DC voltages applied to the material tend to breakdown the material and reduce its lifetime. With these considerations in mind, the ideal signal to drive an EL lamp is a high voltage sine wave. Traditional approaches to achieving this type of waveform included discrete circuits incorporating a transformer, transistors, and several resistors and capacitors. This approach is large and bulky, and cannot be implemented in most hand held equipment.

ASIX now offers low power single chip driver circuits specifically designed to drive small to medium sized electroluminescent panels. All that is required is one external inductor and capacitor. Electroluminescent backlighting is ideal when used with LCD displays, keypads, or other backlit readouts. Its main use is to illuminate displays in dim to dark conditions for momentary periods of time. EL lamps typically consume less than LEDs or bulbs making them ideal for battery powered products. Also, EL lamps are able to evenly light an area without creating "hot spots" in the display. The amount of light emitted is a function of the voltage applied to the lamp, the frequency at which it is applied, the lamp material used and its size, and lastly, the inductor used. There are many variables which can be optimized for specific applications.



7 Typical Application

Contact the factory for any technical or application support.

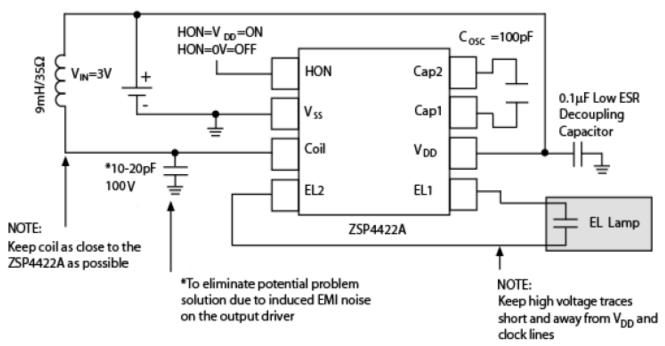


Figure 2. Typical Application Circuit

8 Test Circuit

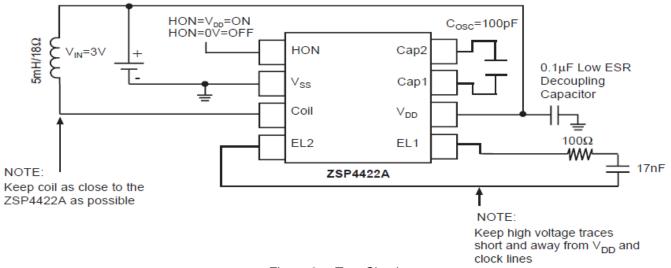


Figure 3. Test Circuit



9 Waveforms

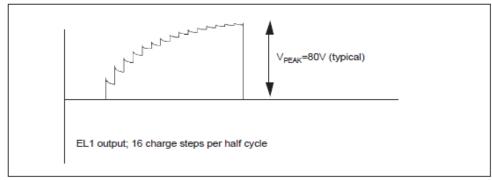


Figure 4. EL Output Voltage in Discrete Steps at EL1 Output

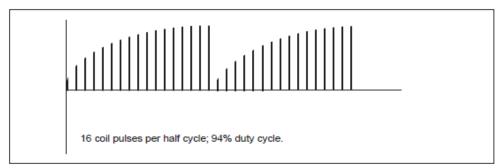


Figure 5. Voltage Pulses Released from the Coil to the EL Driver Circuitry

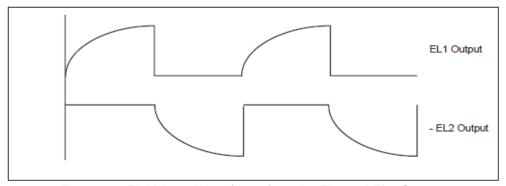


Figure 6. EL Voltage Waveforms from the EL1 and EL2 Outputs

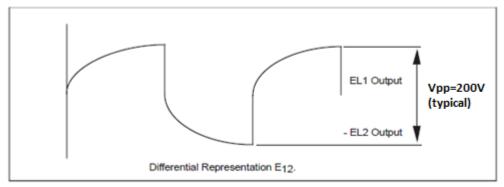
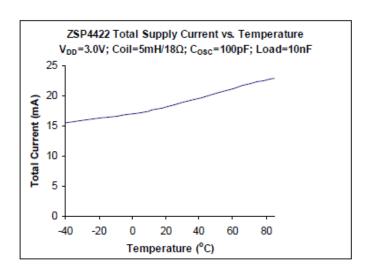


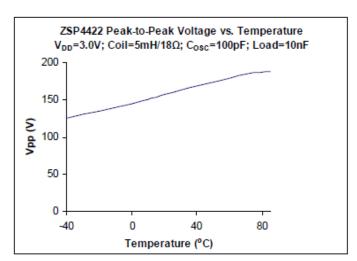
Figure 7. EL Differential Output Waveform of the EL1 and EL2 Outputs

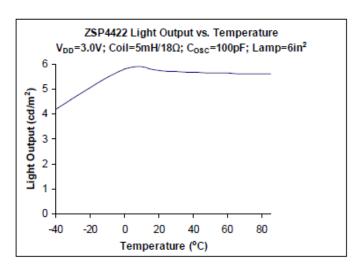


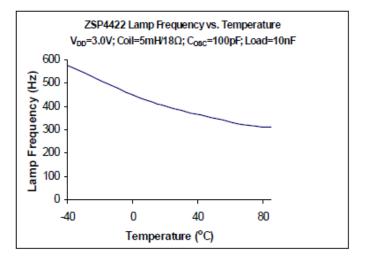


10 Typical Performance Characteristics











11 Manufacturers

Coil Manufacturers

Hitachi Metals

Material Trading Division 2101 S. Arlington Heights Road, Suite 116 Arlington Heights, IL 60005-4142 Phone: 1-800-777-8343 Ext. 12

(847) 364-7200 Ext. 12 Fax: (847) 364-7279

Hitachi Metals Ltd. Europe

Immernannstrasse 14-16, 40210 Dusseldorf, Germany Contact: Gary Loos Phone: 49-211-16009-0 Fax: 49-211-16009-29

Hitachi Metals Ltd.

Kishimoto Bldg. 2-1, Marunouchi 2-chome, Chiyoda-Ku, Tokyo,

Contact: Mr. Noboru Abe Phone: 3-3284-4936 Fax: 3-3287-1945

Hitachi Metals Ltd. Singapore

78 Shenton Way #12-01, Singapore 079120 Contact: Mr. Stan Kaiko Phone: 222-8077 Fax: 222-5232

Hitachi Metals Ltd. Hong Kong

Room 1107, 11/F., West Wing, Tsim Sha. Tsui Center 66 Mody Road, Tsimshatsui East, Kowloon, Hong Kong Phone: 2724-4188 Fax: 2311-2095

Murata

2200 Lake Park Drive, Smyrna Georgia 30080 U.S.A. Phone: (770) 436-1300 Fax: (770) 436-3030

Murata European

Holbeinstrasse 21-23, 90441 Numberg, Postfachanschrift 90015 Phone: 011-4991166870 Fax: 011-49116687225

Murata Taiwan Electronics

225 Chung-Chin Road, Taichung, Taiwan, R.O.C. Phone: 011 88642914151 Fax: 011 88644252929

Murata Electronics Singapore

200 Yishun Ave. 7, Singapore 2776, Republic of Singapore Phone: 011 657584233 Fax: 011 657536181

Murata Hong Kong

Room 709-712 Miramar Tower, 1 Kimberly Road, Tsimshatsui, Kowloon, Hong Kong Phone: 011-85223763898 Fax: 011-85223755655

Panasonic.

6550 Katella Ave Cypress, CA 90630-5102 Phone: (714) 373-7366 Fax: (714) 373-7323

Sumida Electric Co., LTD.

5999, New Wilke Road, Suite #110 Rolling Meadows, IL,60008 U.S.A. Phone: (847) 956-0666 Fax: (847) 956-0702

Sumida Electric Co., LTD.

4-8, Kanamachi 2-Chrome, Katsushika-ku, Tokyo 125 Japan Phone: 03-3607-5111 Fax: 03-3607-5144

Sumida Electric Co., LTD.

Block 15, 996, Bendemeer Road #04-05 to 06, Singapore 339944 Republic of Singapore Phone: 2963388 Fax: 2963390

Sumida Electric Co., LTD.

14 Floor, Eastern Center, 1065 King's Road, Quarry Bay, Hong Kong Phone: 28806688 Fax: 25659600

Polarizers/Transflector Manufacturers

Nitto Denko

Yoshi Shinozuka Bayside Business Park 48500 Fremont, CA. 94538 Phone: 510 445 5400 Fax: 510 445-5480

Top Polarizer- NPF F1205DU Bottom - NPF F4225 or (F4205) P3 w/transflector

Transflector Material

Astra Products
Mark Bogin
P.O. Box 479
Baldwin, NJ 11510
Phone (516)-223-7500
Fax (516)-868-2371

EL Lamp Manufacturers

Leading Edge Ind. Inc.

11578 Encore Circle Minnetonka, MN 55343 Phone 1-800-845-6992

Midori Mark Ltd.

1-5 Komagata 2-Chome Taita-Ku 111-0043 Japan Phone: 81-03-3848-2011

NEC Corporation

Yumi Saskai 7-1, Shiba 5 Chome, Minato-ku, Tokyo 108-01, Japan Phone: (03) 3798-9572 Fax: (03) 3798-6134

Seiko Precision

Shuzo Abe 1-1, Taihei 4-Chome, Sumida-ku, Tokyo, 139 Japan Phone: (03) 5610-7089 Fax: (03) 5610-7177

Gunze Electronics

2113 Wells Branch Parkway Austin, TX 78728 Phone: (512) 752-1299 Fax: (512) 252-1181

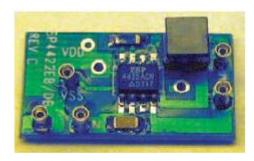




12 Ordering Information

Part Number	Temperature Range	Package Type	Green Package
ZSP4422ACN	-40°C to +85°C	8-pin nSOIC	N/A
ZSP4422ALCN	-40°C to +85°C	8-pin nSOIC Green	
ZSP4422ACU	-40°C to +85°C	8-pin MSOP	N/A
ZSP4422ALCU	-40°C to +85°C	8-pin MSOP Green	
ZSP4422ACX	-40°C to +85°C	Die in Wafflepack	N/A
ZSP4422ANEB	N/A	nSOIC Evaluation Board	N/A
ZSP4422AUEB	N/A	MSOP Evaluation Board	N/A

Please contact the factory for pricing and availability on Tape-on-Reel options.





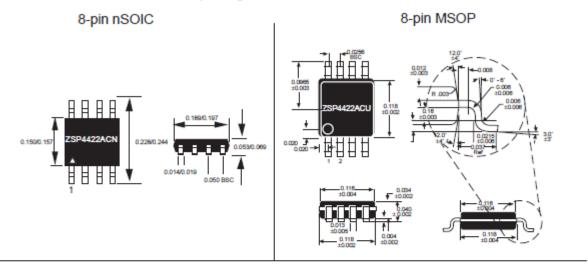
Please contact the factory for EL driver design support and availability of custom-made evaluation demo boards. See our web site for AN007 Application Note regarding requirements for custom-made evaluation demo boards.

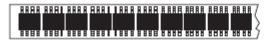




13 Package Information

All package dimensions in inches

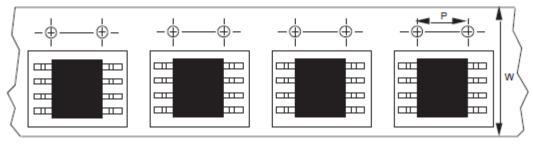




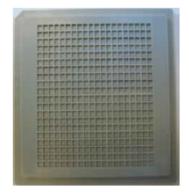


95 ZSP4422ACN per tube

50 ZSP4422ACU per tube



nSOIC-8 13" reels: P=8mm, W=12mm MSOP-8 13" reels: P=8mm, W=12mm			
Pkg.	Minimum qty per reel	Standard qty per reel	Maximum qty per reel
ACN and ACU	500	2500	3000



400 ZSP4422ACX die per wafflepack

Waffle tray size = 1996 x 1996 mils Waffle pocket size cavity = 60 x 70 mils Waffle depth size cavity = 22 mils





Revision History

Revision	Date	Comment
V0.2	2005/01/01	Initial release to customers
V2.00	2015/06/03	1. Changed to ASIX Electronics Corp. logo, strings and contact information.
		2. Added ASIX copyright legal header information.
		3. Modified the Revision History table format.
		4. Modified some Vpp descriptions.







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