

Osptek Display

2.8"TFT LCD Module With Touch Panel

Model No:

YDP280BT001-V4

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1. Introduction

1.1 Scope of application

This specification applies to the LCD module that is supplied by Shenzhen Osprey Optoelectronics Technology Co., Ltd. module should be designed for mobile phone use.

LCD specification: Dots 240xRGBx320.

As to basic specification of the driver IC, refer to the IC (Orise:ST7789V) specification and data book.

All material & processing of the LCD module should be Lead Free.

1.2 TFT features:

Structure: TFT PANNEL+IC +FPC+BL+CTP;

ALL O'CLOCK Type LCD

240 dot-segment and 320 dot-common outputs;

262K Color can be selected by software;

White LED back light;

16bit MCU interface

TP interface: I2C

1.3 Applications:

Motorcycle dashboard

Electric Animation Dashboard

Medical display

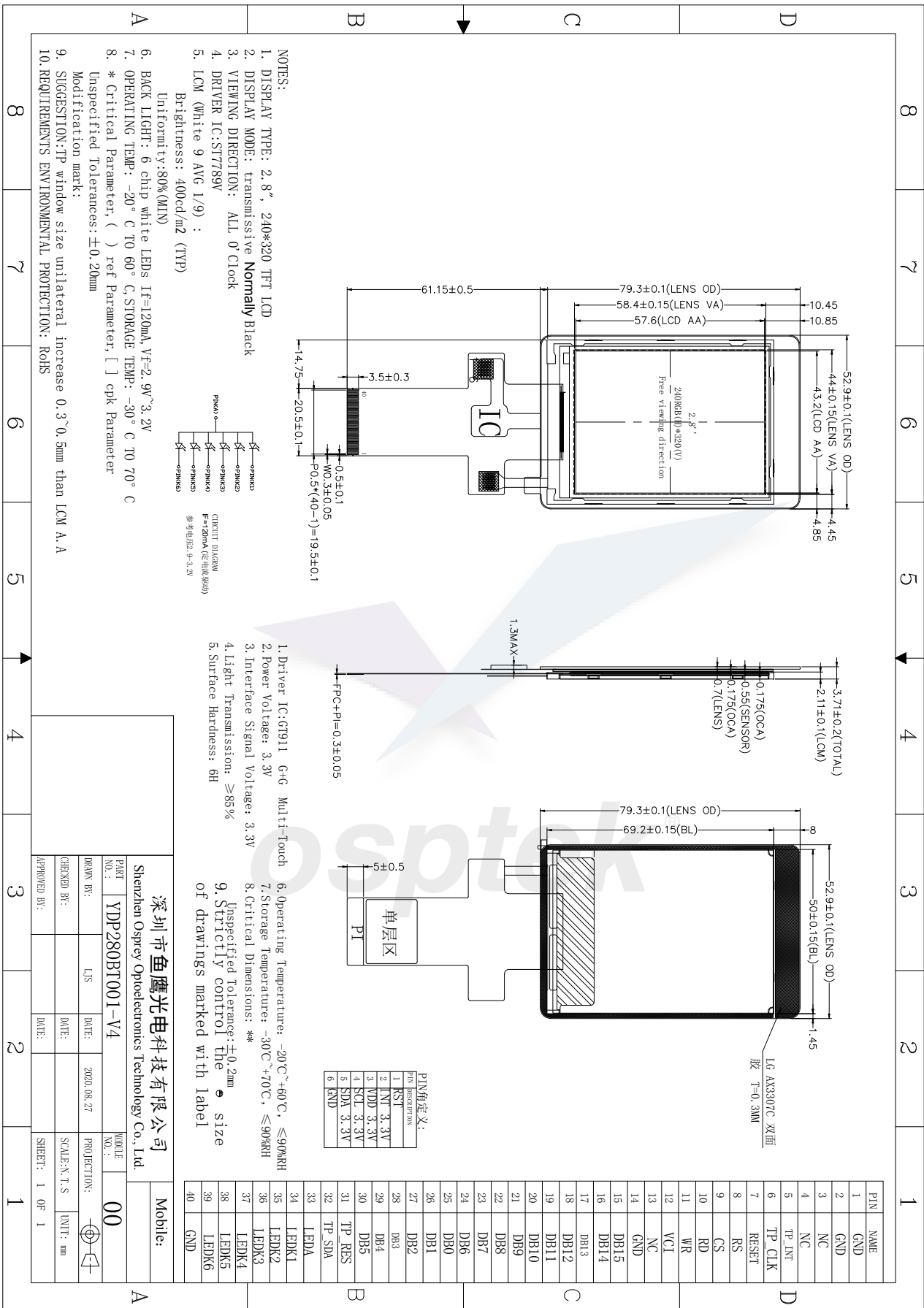
Engineering vehicle display

Outdoor highlight equipment



2. LCM General specification

ITEM	Standard value	Unit
LCD Type	Normally Black	--
Drive element	TFT active matrix	--
Number of pixels	240*3RGB(H)X320(V)	Dots
Pixel arrangement	RGB stripe	--
Pixel Pitch (H*V)	0.180(H)x 0.180(V)	mm
Active area	43.20(H) × 57.60(V)	mm
Viewing direction	ALL O'CLOCK	
TFT Driver IC	ST7789V	-
TFT interface	16bit MCU interface	-
Approx. Weight	TBD	g
LCM Size(W*H*T)	50.00(W) x69.20(H) x2.11(T)	mm
LCM+CTP Size(W*H*T)	52.90(W) x79.30(H) x3.71(T)	mm
Touch structure	G+G	
Touch Driver IC	GT911	-
Touch Interface	I2C	



3.Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit
LCM Operating Temperature	T _{OPR}	-20	+60	°C
LCM Storage Temperature	T _{STG}	-30	+70	°C
TP Operating Temperature & Humidity(20% ~ 90%RH)	T _{OPR}	-20	+60	°C
TP SStorage Temperature & Humidity(20% ~ 90%RH)	T _{STG}	-30	+70	°C
Humidity	RH	10	90	%

4.Electrical Characteristics

4.1 TFT DC Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage for(DC/DC)	VDD	2.5	2.8	3.3	V	
TFT Gate ON Voltage	VGH	12	--	18	V	Note 2
TFT Gate OFF Voltage	VGL	-10	--	-6	V	Note 2
TFT Common Voltage	Vcom	-2	--	5	V	Note 1

Note 1: VCOM value should be adjusted by different condition to optimize Flicker Value.

Note 2: VGH and VGL are the operating voltages of TFT gate.

4.2 Back-Light Unit Characteristics

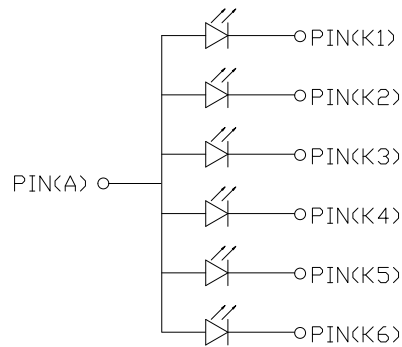
The back-light system is an edge-lighting type with 6 white LEDs. The characteristics of the back-light are shown in the following tables.

Characteristics	Symbol	Min.	Type	Max.	Unit	Notes
Forward Voltage	V _F	2.9	--	3.2	V	-
Forward current	I _F	--	120	--	mA	-
Luminance(With LCD)	L _v	--	400	--	cd/m ²	-
LED life time	N/A	--	30,000	--	Hr	Note 1

Note:

- (1) The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_L=30mA/LED. The LED life time could be decreased if operating I_L is larger than 45mA/LED.

Backlight circuit diagram shown in below:



5. Module Function Description

LCM PIN Description:

Pin No.	Symbol	LCM Description	remarks
1~2	GND	Ground	
3~4	NC	No connection	
5	TP_INT	CTP_STOP Singnal	
6	TP -SCL	CTP_Clock Singnal	
7	RESET	Global reset pin	
8	RS	Register select signal.	
9	CS	Chip select signal input pin	
10	RD	Read data at the rising edge.	
11	WR	Write strobe signal	
12	VCI	Power supply : 2.5V ~ 3.3V	
13	NC	No connection	
14	GND	Ground	
15~24	DB15~DB6	Data bus	
25~30	DB0~DB5	Data bus	
31	TP_RESET	CTP_ RESET PIN	
32	TP-SDA	CTP_Data Singnal	
33	LEDA	Power supply for backlight anode input terminal.	
34~39	LEDK1~LEDK6	Power supply for backlight cathode input terminal.	
40	GND	Ground	

6. Timing Characteristics

8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

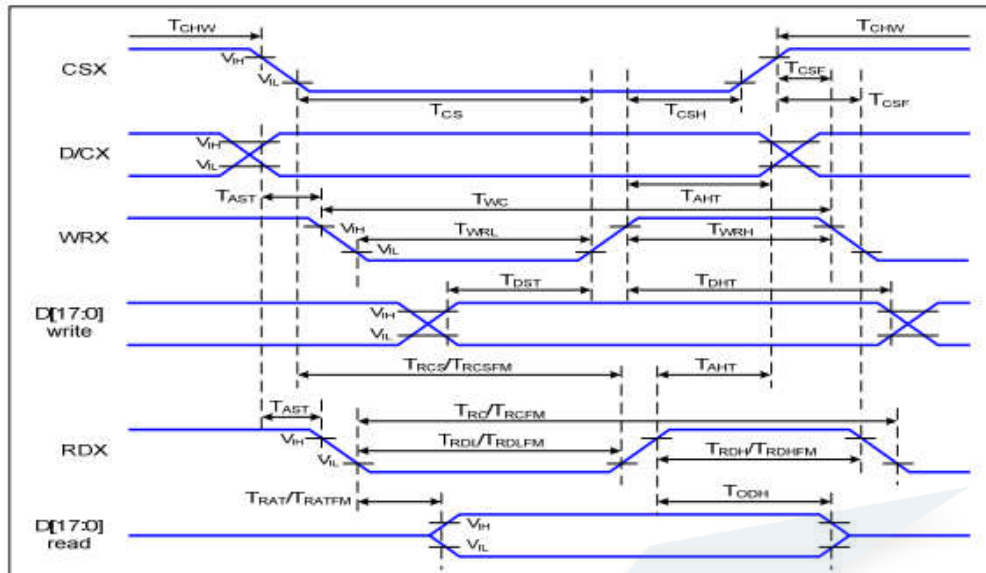


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDH}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHF}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLF}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF

	T_{DHT}	Data hold time	10		ns
	T_{RAT}	Read access time (ID)		40	ns
	T_{RATFM}	Read access time (FM)		340	ns
	T_{ODH}	Output disable time	20	80	ns

Table 4 8080 Parallel Interface Characteristics

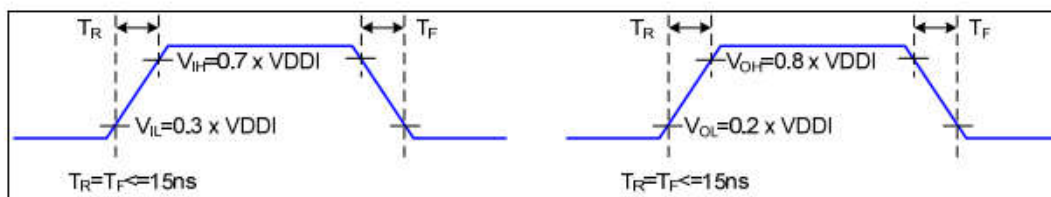


Figure 2 Rising and Falling Timing for I/O Signal

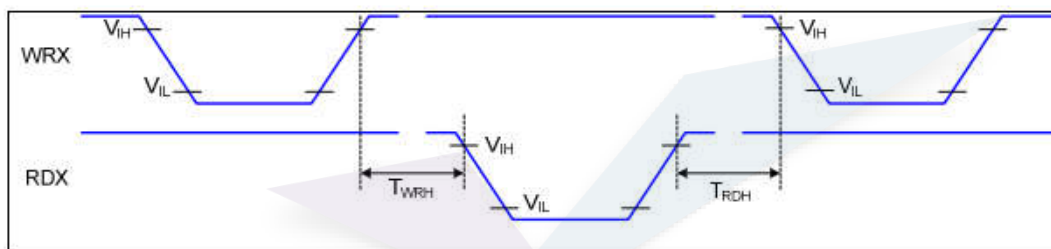


Figure 3 Write-to-Read and Read-to-Write Timing

Note: The rising time and falling time (T_R , T_F) of input signal and fall time are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for input signals.

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Reset Timing:

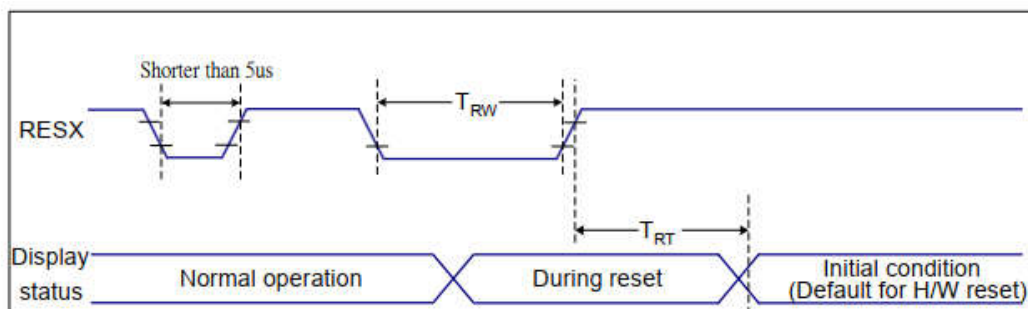


Figure 7 Reset Timing

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5) 120 (Note 1, 6, 7)	ms

Table 9 Reset Timing

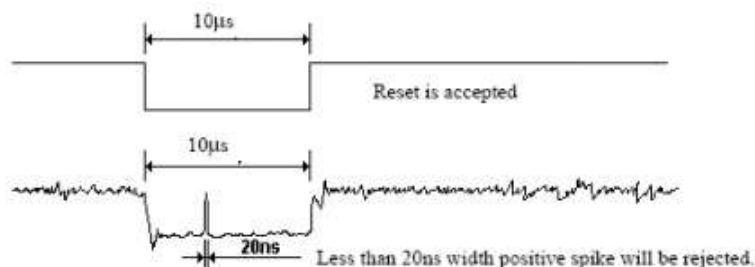
Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

7.Optical Characteristics

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Threshold Voltage		Vsat		4.1	4.3	4.5	V	Fig.1
		Vth		1.6	1.8	2.0	V	
Viewing Angle	Horizontal	Θ3	CR>10	70	80		°	Note 1
		Θ9		70	80		°	
	Vertical	Θ12		70	80		°	
		Θ6		70	80		°	
Contrast Ratio		CR	Θ= 0°	500	800			Note 2
Transmittance		T(%)	Θ= 0°	4.7	5.0			Note 3
NTSC		%	Θ= 0°	65	70			Only CF
Reproduction Of color	Red	Rx	Θ= 0°	0.640	0.655	0.670		Note 4 *Color filter Glass with OC
		Ry		0.312	0.327	0.342		
	Green	Gx		0.271	0.286	0.301		
		Gy		0.577	0.592	0.607		
	Blue	Bx		0.123	0.138	0.153		
		By		0.096	0.111	0.126		
White		Wx	Θ= 0°	0.288	0.303	0.318		
		Wy		0.314	0.329	0.344		
Response Time		Tr+Tf	Θ= 0°		30	35	ms	Note 5

Note:

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIG.2).
2. Contrast measurements shall be made at viewing angle of Θ= 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIG. 2) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Transmittance is the value with Polarizer.
 4. The color chromaticity coordinates specified in Table1 shall be calculated from The spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the C/F. Measurement condition is C - light source & Halogen Lamp
 5. The electro-optical response time measurements shall be made as FIG.3 by switching the "data" input signal ON and OFF.
- The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .

Figure 1. The definition of V_{th} & V_{sat}

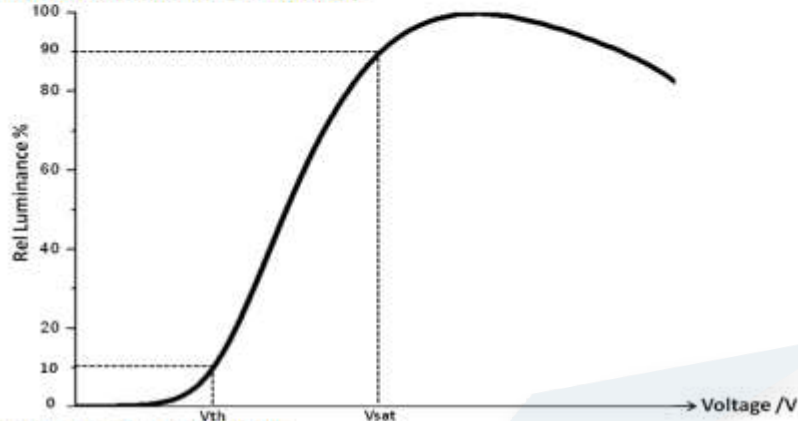


Figure 2. Measurement Set Up

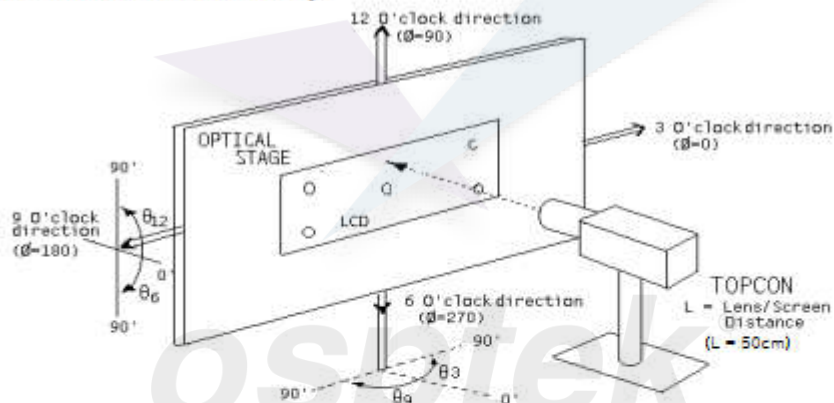
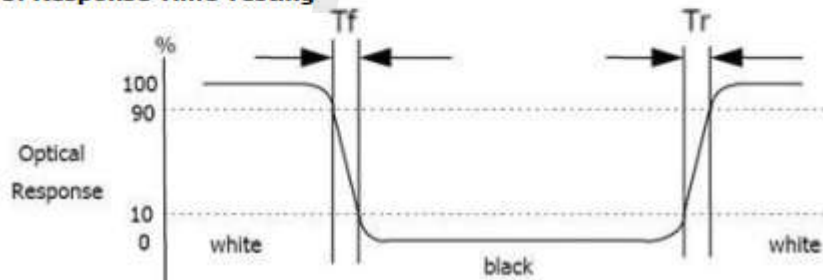


Figure 3. Response Time Testing



Note 4: Definition of contrast ratio:

Contrast ratio is measured at the center point of LCD and calculated by the following formula:

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

8. Reliability Test Item

No.	Test Item	Test Condition	Notes
1	High Temp. Storage	+70°C / 96H	1. Functional test is OK. Missing Segment, short, unclear segment non-display, display abnormally and liquid crystal leakage are un-allowed. 2. No low temperature bubbles, end seal loose and fall, frame rainbow.
2	Low Temp. Storage	-30°C / 96H	
3	High Temp. Operating	+60°C / 96H	
4	Low Temp. Operating	-20°C / 96H	
5	High Temperature / Humidity storage	50°C x 90%RH / 96H	
6	Thermal and cold shock	Static state, -20°C (30min) ~60°C (30min), 50 cycles	

9. Inspection standards

9.1 AQL (Acceptable Quality Level)

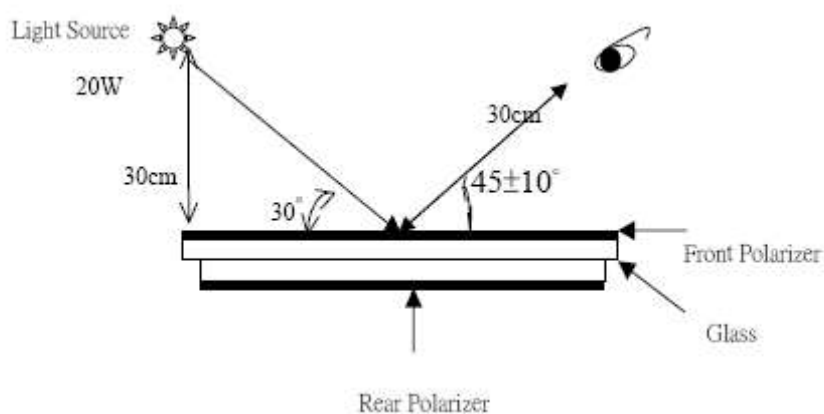
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	1.0	1.5

9.2 Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000 ± 200 . (Darkroom's lux: 100 ± 50), About an angle of incidence 30°, a distance of 30 cm with an angle of 45° to check the products without uncovering the film!

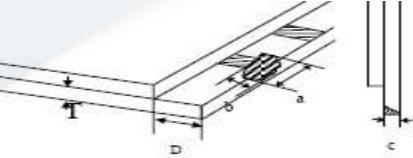
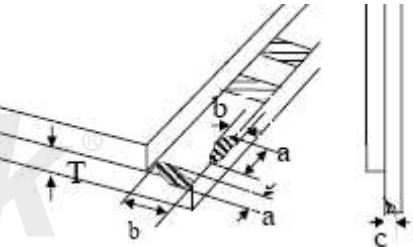
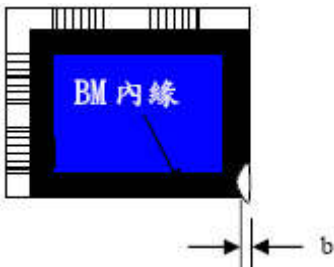
(As shown below)



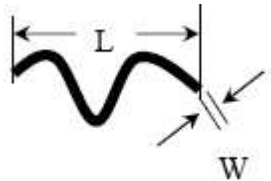
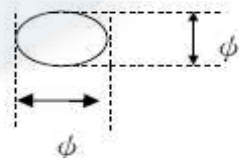
9.3 Inspection item and criteria

9.3.1 Visual inspection criterion in immobility

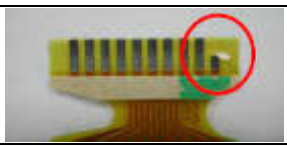
9.3.1.1 Glass defect

N O	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	1. Linear cracks panel 【Reject】 2. Nonlinear crack contrast by limited sample	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) $b \leq 1/3$ Pin width(non bonding area) 【Accept】 2) bonding area $\leq 0.5\text{mm}$ 【Accept】	A: Length, b: Width
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) $b \leq 1/3$ of effective length for bonding electrode 【Accept】	a: length, b: Width, c: Thickness 
5	Pin-side,non-conductive area damaged (minor defect)	1) Damage area don't touch the ITO (Including contraposition mark, except scribing mark) 【Accept】 2) $C < T$ $b \leq BM1/3$ of width 【Accept】 3) $c = T$ b not touch the seal glue 【Accept】 4) a disregards	a: Length, b: Width c: Thickness 
6	Non-pin-side damage (minor defect)	$c < T$ 1) b exceeds $1/3 BM$ 【Reject】 $c = T$ b not touch the seal glue 【Reject】	c: Thickness b: width of damage 

9.3.1.2 LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
1	Fiber 、glass cratch 、polarizer scratch/folded (minor defect)	Specification	Allowable	note1:L: Length, W: Width note2: disregard if out of AA 
		$W \leq 0.03\text{mm}$	disregard	
		$0.03\text{mm} < W \leq 0.05\text{mm};$ $L \leq 4.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm};$ $L \leq 4.0\text{mm}$	1	
		$W > 0.1\text{mm}; L > 4.0\text{mm}$	0	
2	Polarizer bubble 、 concave and convex (minor defect)	$\varphi \leq 0.2\text{mm}$	disregard	note1: φ $= (L+W)/2$, L:Length, W :Width note2:disregard if out of AA
		$0.2\text{mm} < \varphi \leq 0.3\text{mm}$	2	
		$0.3\text{mm} < \varphi \leq 0.5\text{mm}$	1	
		$0.5\text{mm} < \varphi$	0	
3	Black dots 、dirty dots 、impurities 、 eye winker (minor defect)	$\varphi \leq 0.15\text{mm}$	disregard	note2:disregard if out of AA 
		$0.15\text{mm} < \varphi \leq 0.25\text{mm}$	2	
		$0.25\text{mm} < \varphi \leq 0.3\text{mm}$	1	
		$0.3\text{mm} < \varphi$	0	
4	Polarizer prick (minor defect)	$\varphi \leq 0.1\text{mm}$	disregard	note1: φ $= (L+W)/2$, L=Length, W=Width note2:the distance between two dots>5mm
		$0.1\text{mm} < \varphi \leq 0.25\text{mm}$	3	
		$\varphi > 0.25\text{mm}$	0	

9.3.1.3 FPC

NO	Defect item	Criteria		Remark
1	Copper screen peel (minor defect)	Copper screen peel 【Reject】		
2	No release tape or peel	No release tape or peel 【Reject】		
3	Dirty dot and impurity of FPC for customer using side (minor defect)	Specification	Allowable	Note1: Cannot have stride ITO impurities
		$\varphi \leq 0.3\text{mm}$	2	
		$\varphi > 0.3$	0	

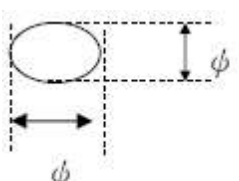
9.3.1.4 Black tape & Mara tape

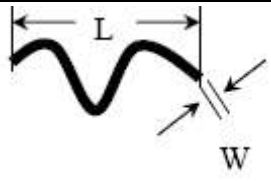
NO	Defect item	Criteria	Remark
1	FPC or H/S black tape (minor defect)	1. shift spec: 1) glue to the polarize 【Reject】 2) IC bare 【Reject】 2. left-and-right spec: 1) exceed of FPC edge or H-S edge 【Reject】 2) IC bare 【Reject】	
2	No black tape (major defect)	No black tape 【Reject】	
3	Tape position mistake (minor defect)	Not by engineering drawing	
4	Mara tape defect (minor defect)	Peel before pulling the protecting film 【Reject】	

9.3.1.5 Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon (major defect)	Uncover the ITO and circuit area 【Reject】	note: compared by engineering
2	Taffy glue (major defect)	1. Uncover the reveal copper area 【Reject】 2. Cover layer 0.3mm(Min)~3.0mm(Max) 【Reject】	note: if customer has special requirement, refer to the technical document
3	Depth of glue covering (major defect)	Depth of glue covering overtop front Polarizer 【Reject】	Except of the special requirement

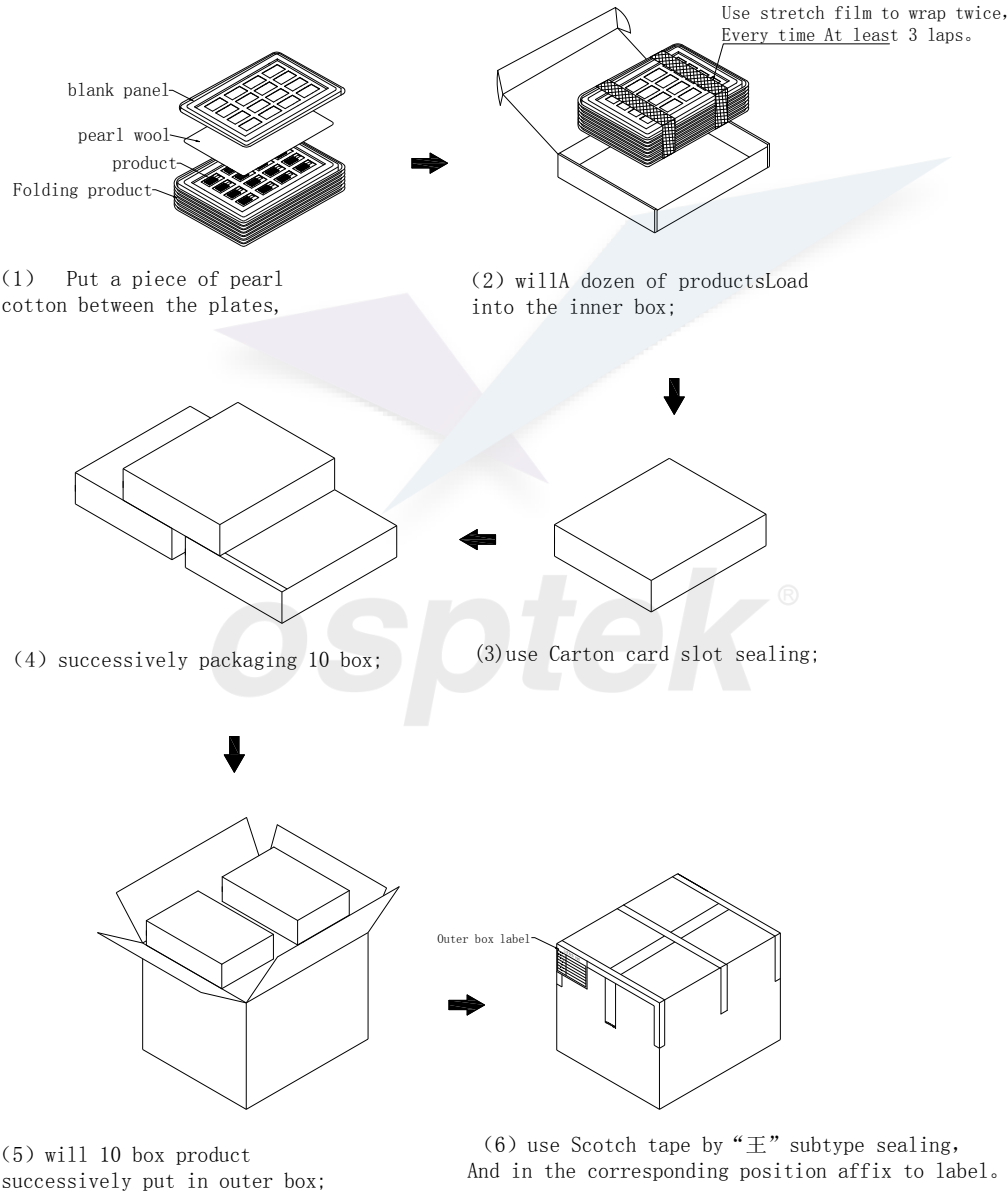
9.3.2Electrical criteria

NO	Defect item	Criteria	Remark	
1	No display (major defect)	No display 【Reject】		
2	Missing line (major defect)	Missing line 【Reject】		
3	Seg-com light and dark (major defect)	Seg-com light and dark 【Reject】	ND filter 2% test	
4	No display in immobility (major defect)	No display in immobility 【Reject】		
5	Flicker of Pattern (major defect)	Flicker of Pattern 【Reject】		
6	Mura (major defect)	ND filter 2%test		
7	Over current (major defect)	Over current 【Reject】		
8	Voltage out of specification (major defect)	Voltage out of specification 【Reject】		
9	Pattern blur, error code (major defect)	Pattern blur, error code 【Reject】		
10	Dark light, Flicker (major defect)	Dark light, Flicker 【Reject】		
11	Black/white dots 、 Dirty dots 、 eye winker (major defect)	Specification	Allowable	Note1:disregard if out of  AA
		$\varphi \leq 0.15\text{mm}$	disregard	
		$0.15\text{mm} < \varphi \leq 0.25\text{mm}$	2	
		$0.25\text{mm} < \varphi \leq 0.3\text{mm}$	1	
		$0.3\text{mm} < \varphi$	0	
12	Fiber 、 glass crutch 、 Polarizer scratch/folded (major defect)	$W \leq 0.03\text{mm}$	disregard	Note1:L: Length, W: Width Note2: disregard if out of AA
		$0.03\text{mm} < W \leq 0.05\text{mm}$ $L \leq 4.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm}$	1	

		$L \leq 4.0\text{mm}$		
		$W > 0.1\text{mm}; L > 4.0\text{mm}$	0	

10. Packing Method

packaging sketch map:



- END -