



产品规格手册

PRODUCT SPECIFICATION MANUAL

XW6005

N-Ch Fast Switching MOSFETs
SOT23-3L/60V/5A

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

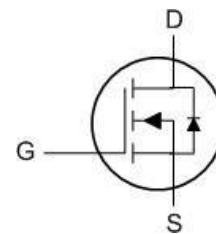
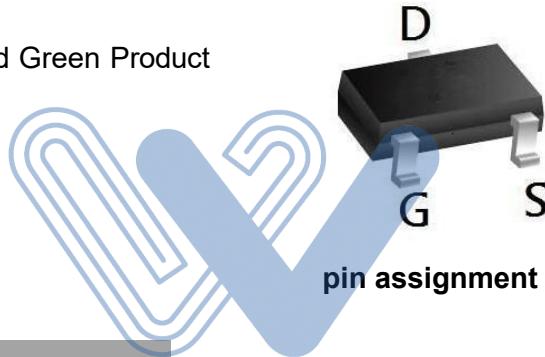
BVDSS	RDS(on)	ID
60V	40mΩ	5 A

Description

The XW6005 is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications.

The XW6005 meet the RoHS and Green Product

SOT-23-3L Pin Configuration



Schematic diagram

Package Marking and Ordering Information

Product	Package	Marking	Packing	Min Unit Quantity
XW6005	SOT23-3L	XIN WO TECHNOLOGY	3000PCS/Reel	3000PCS

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		60	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_c = 25^\circ\text{C}$	5	A
		$T_c = 100^\circ\text{C}$	3.8	A
I_{DM}	Pulsed Drain Current ^{note1}		30	A
EAS	Single Pulsed Avalanche Energy ^{note2}			mJ
P_D	Power Dissipation	$T_c = 25^\circ\text{C}$	3	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		73	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V},$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1	1.6	2.5	V
$R_{\text{DS}(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{\text{GS}}=10\text{V}, I_D=15\text{A}$	-	40	49	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=10\text{A}$	-	45	63	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	825	-	pF
C_{oss}	Output Capacitance		-	49	-	pF
C_{rss}	Reverse Transfer Capacitance		-	41	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}, I_D=4.5\text{A}, V_{\text{GS}}=10\text{V}$	-	14	-	nC
Q_{gs}	Gate-Source Charge		-	2.9	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	5.2	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=30\text{V}, I_D=2\text{A}, R_L=6.7\Omega, R_G=3\Omega, V_{\text{GS}}=10\text{V}$	-	5	-	ns
t_r	Turn-on Rise Time		-	2.6	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	16.1	-	ns
t_f	Turn-off Fall Time		-	2.3	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_S=15\text{A}$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C}, I_F=15\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	35	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	53	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}, V_{\text{DD}}=30\text{V}, V_{\text{G}}=10\text{V}, L=0.5\text{mH}, R_g=25\Omega, I_{\text{AS}}=6.1\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure1: Output Characteristics

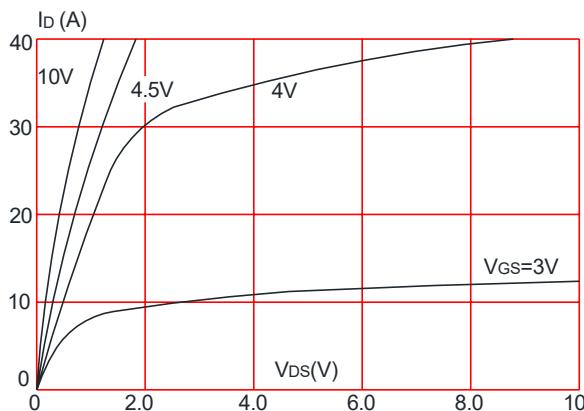


Figure 3: On-resistance vs. Drain Current

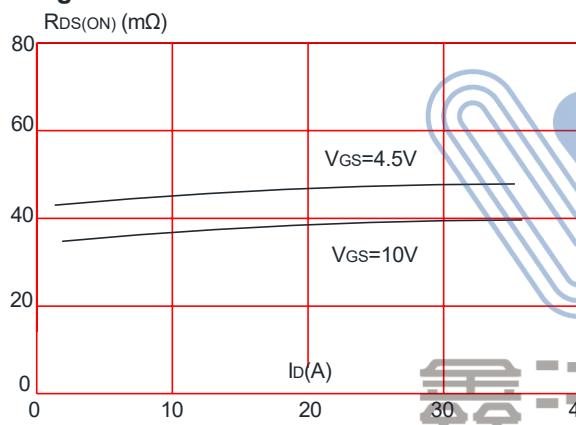


Figure 5: Gate Charge Characteristics

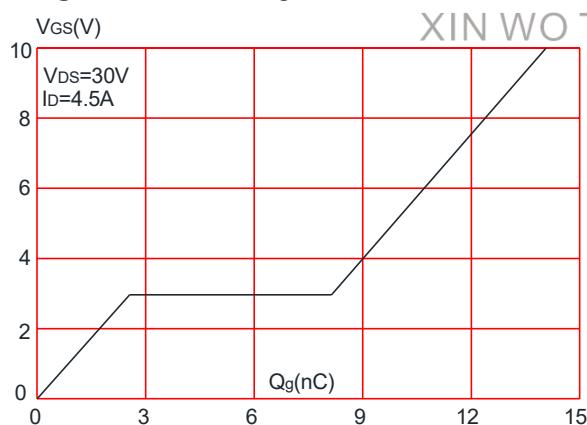


Figure 2: Typical Transfer Characteristics

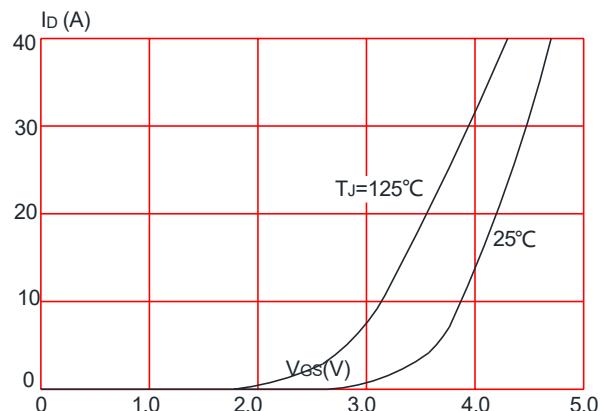
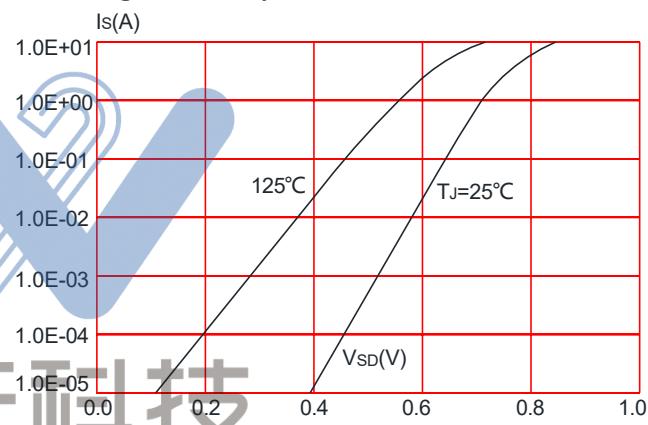


Figure 4: Body Diode Characteristics



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Figure 6: Capacitance Characteristics

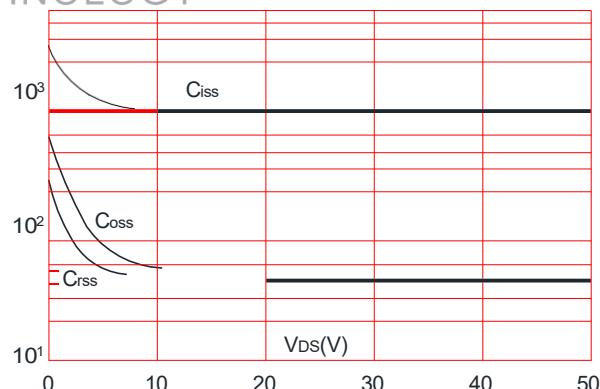


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

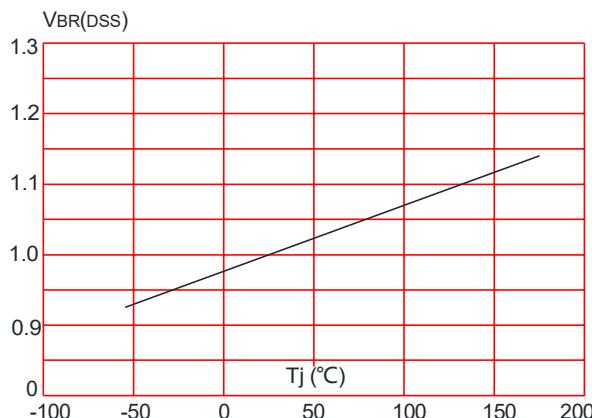


Figure 8: Normalized on Resistance vs. Junction Temperature

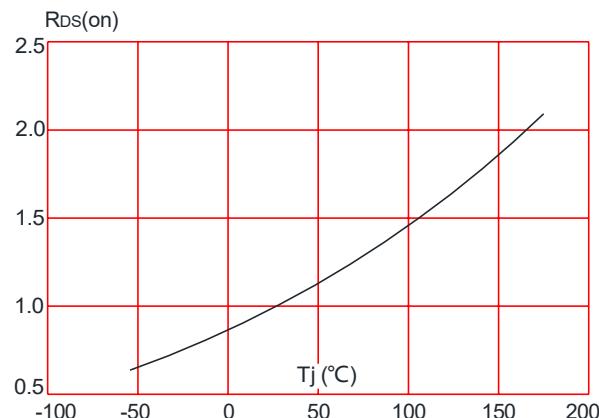


Figure 9: Maximum Safe Operating Area

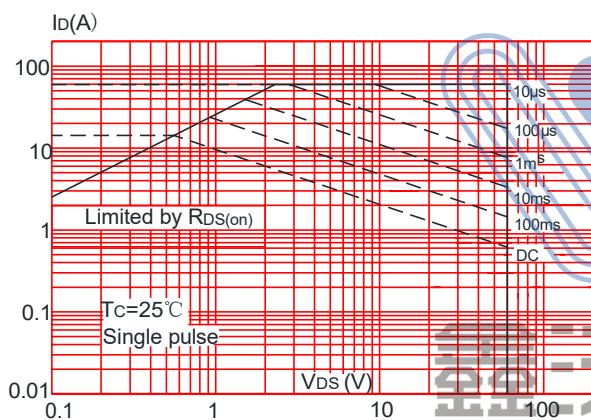


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

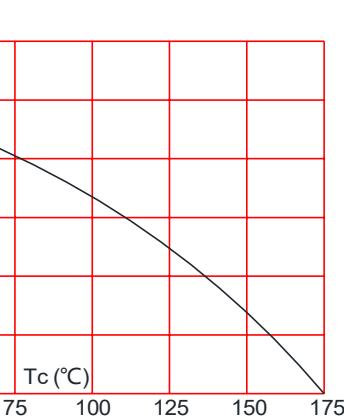
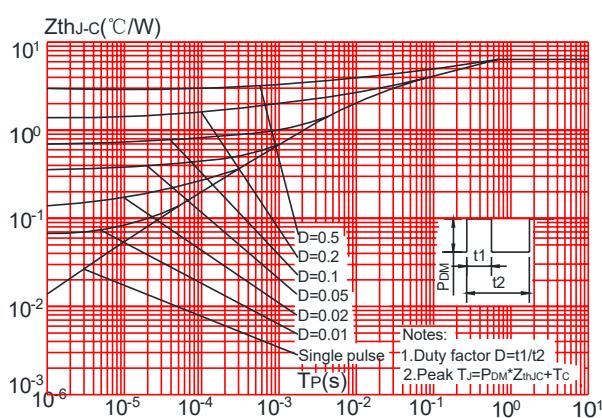
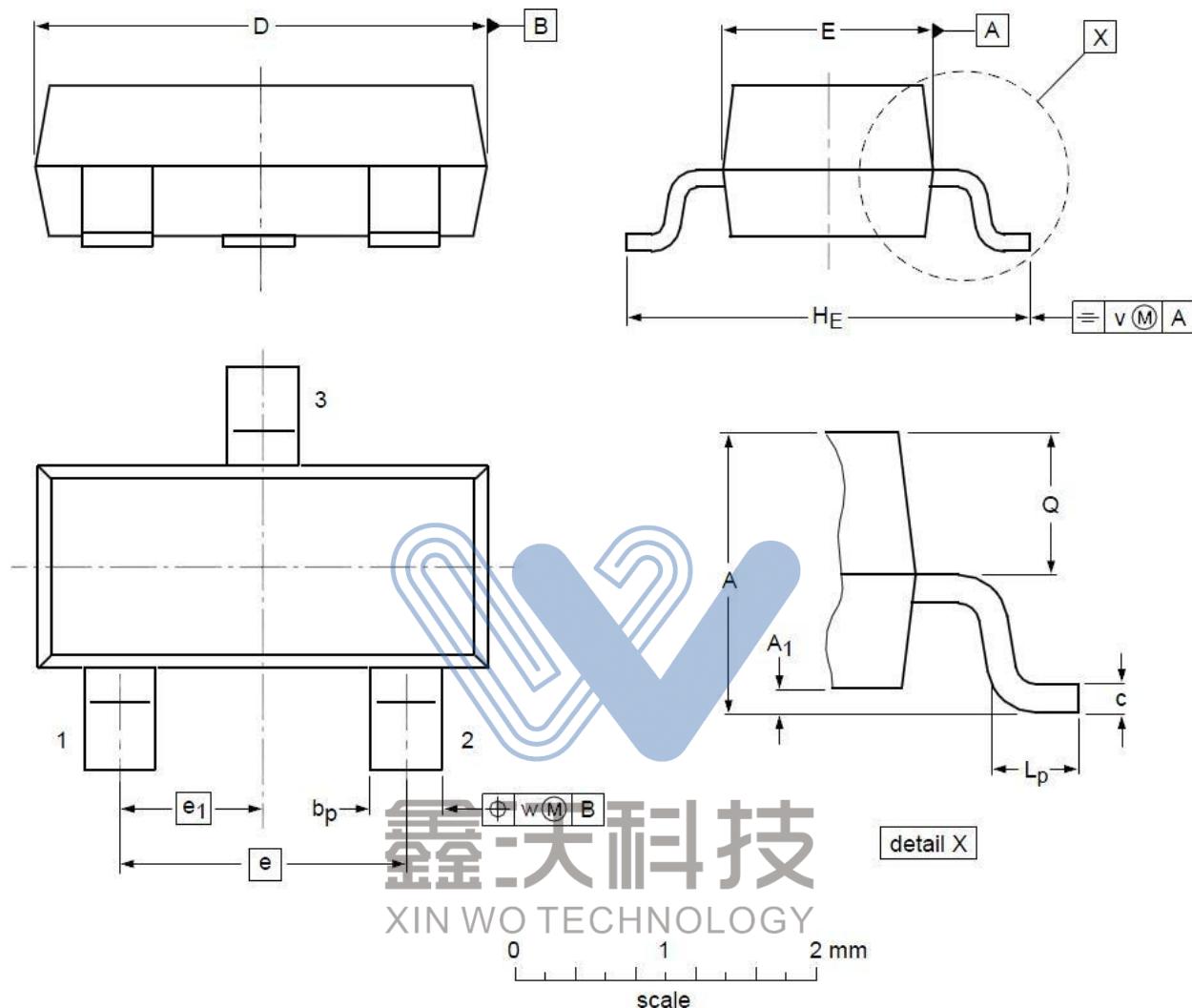


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Package Mechanical Data-SOT-23-3L



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A₁	0.01	0.05	0.10
b_p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e₁	--	0.95	--
H_E	2.25	2.40	2.55	L_p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				