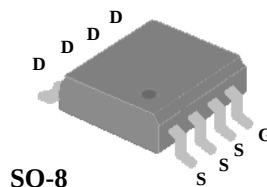
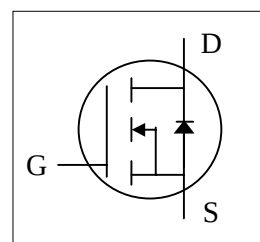


- ▼ 100% R_g & UIS Test
- ▼ Simple Drive Requirement
- ▼ Low On-resistance
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	60V
$R_{DS(ON)}$	6.5m Ω
I_D^3	15A



Description

XP6NA6R5 series are innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The SO-8 package is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for voltage conversion or switch applications.

Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10V^3$	15	A
$I_D@T_A=70^\circ\text{C}$	Drain Current, $V_{GS} @ 10V^3$	12	A
I_{DM}	Pulsed Drain Current ¹	50	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	2.5	W
E_{AS}	Single Pulse Avalanche Energy ⁴	31.2	mJ
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_j	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient ³	50	$^\circ\text{C}/\text{W}$

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =14A	-	-	6.5	mΩ
		V _{GS} =6V, I _D =7A	-	-	11	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2.2	-	3.6	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =14A	-	38	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =48V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V, V _{DS} =0V	-	-	±0.1	uA
Q _g	Total Gate Charge ⁵	I _D =14A	-	33	52.8	nC
Q _{gs}	Gate-Source Charge ⁵	V _{DS} =30V	-	10.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge ⁵	V _{GS} =10V	-	7.5	-	nC
t _{d(on)}	Turn-on Delay Time ⁵	V _{DS} =30V	-	13	-	ns
t _r	Rise Time ⁵	I _D =1A	-	9	-	ns
t _{d(off)}	Turn-off Delay Time ⁵	R _G =10Ω	-	31	-	ns
t _f	Fall Time ⁵	V _{GS} =3.3V	-	60	-	ns
C _{iss}	Input Capacitance ⁵	V _{GS} =0V	-	1850	2960	pF
C _{oss}	Output Capacitance ⁵	V _{DS} =50V	-	300	-	pF
C _{rss}	Reverse Transfer Capacitance ⁵	f=1.0MHz	-	20	-	pF
R _g	Gate Resistance	f=1.0MHz	-	1.2	2.4	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =1.9A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ⁵	I _S =14A, V _{GS} =0V,	-	33	-	ns
Q _{rr}	Reverse Recovery Charge ⁵	dI/dt=100A/μs	-	24	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² 2oz copper pad of FR4 board, t ≤10sec, 125°C/W when mounted on min. copper pad.
- 4.Starting T_j=25°C, V_{DD}=30V, L=0.1mH, R_G=25Ω, V_{GS}=10V
- 5.Guaranteed by design.
- 6.These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=150°C.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT, AUTOMOTIVE OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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XSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

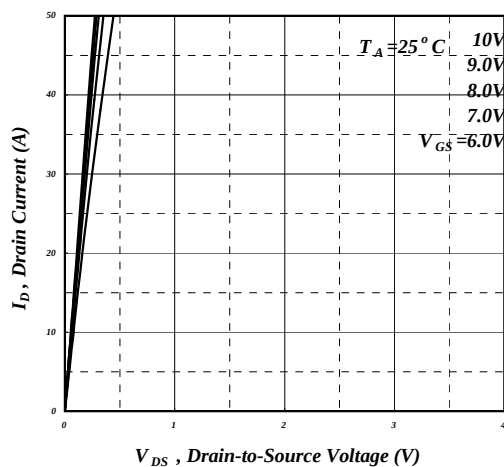


Fig 1. Typical Output Characteristics

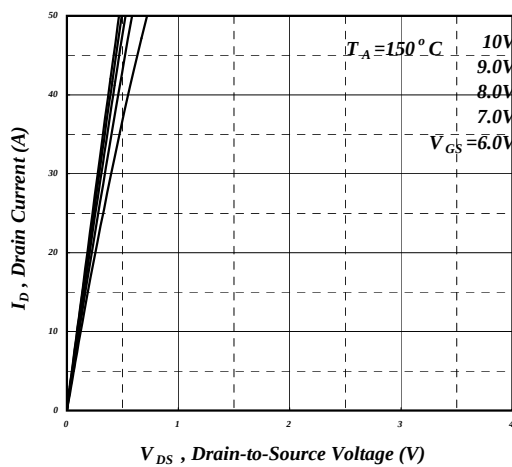


Fig 2. Typical Output Characteristics

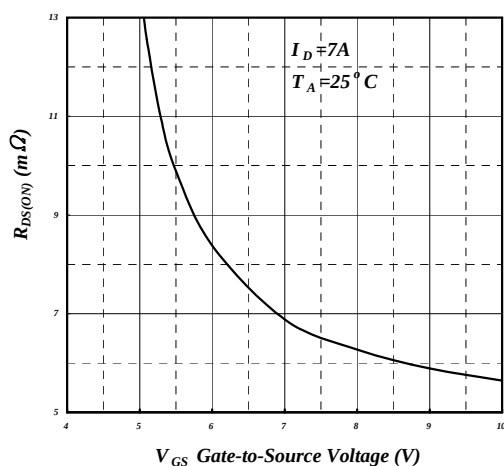


Fig 3. On-Resistance v.s. Gate Voltage

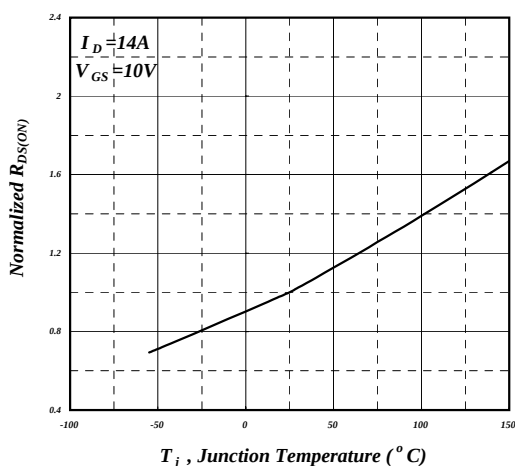


Fig 4. Normalized On-Resistance v.s. Junction Temperature

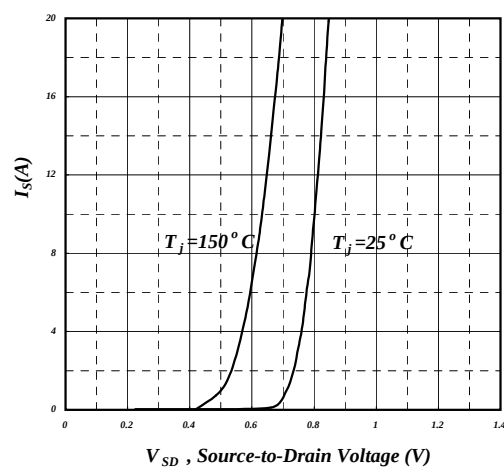


Fig 5. Forward Characteristic of Reverse Diode

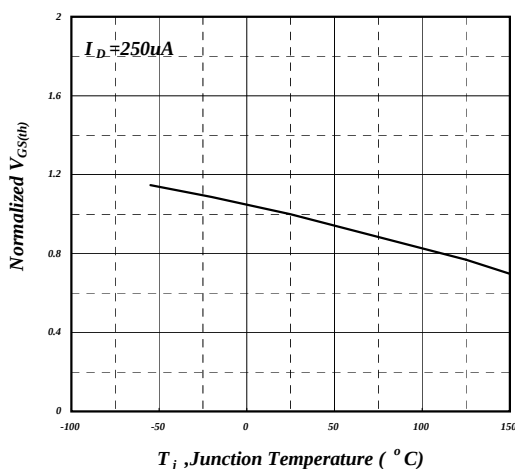


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

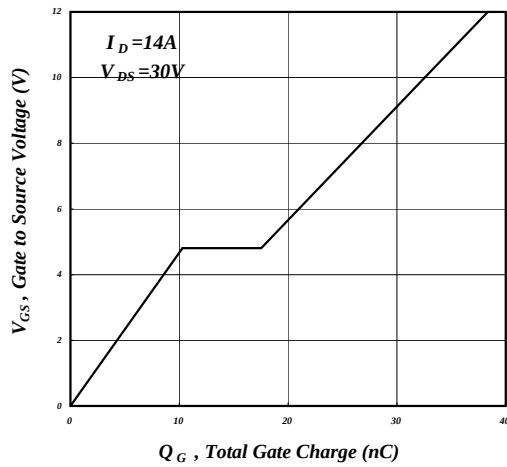


Fig 7. Gate Charge Characteristics

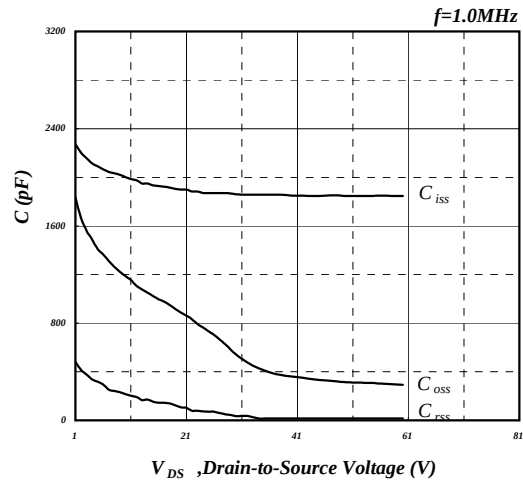


Fig 8. Typical Capacitance Characteristics

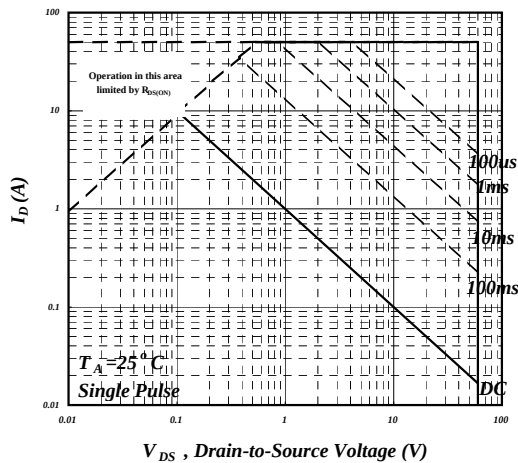


Fig 9. Maximum Safe Operating Area⁶

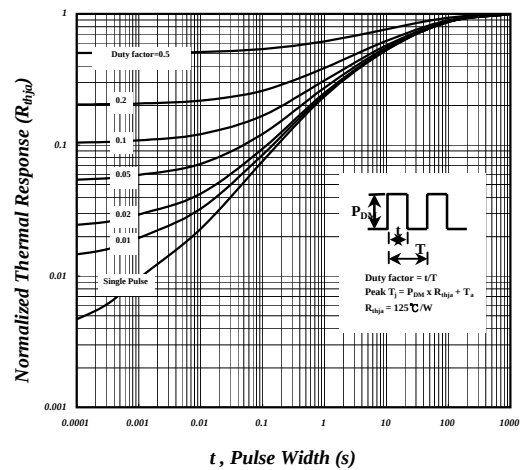


Fig 10. Effective Transient Thermal Impedance

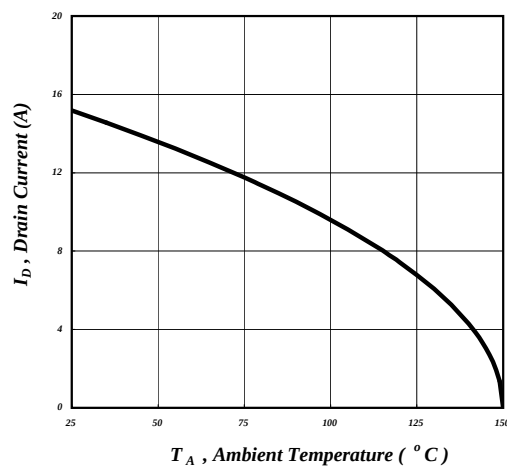


Fig 11. Drain Current v.s. Ambient Temperature

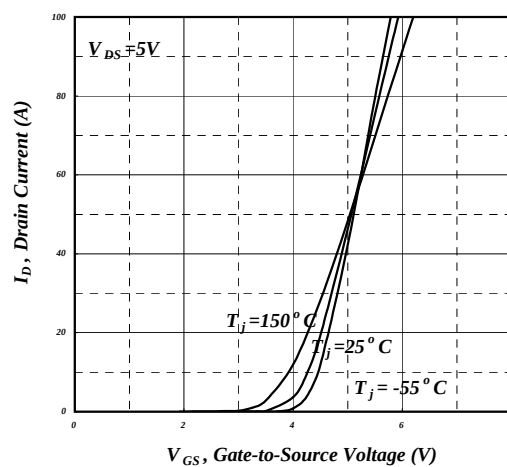


Fig 12. Transfer Characteristics

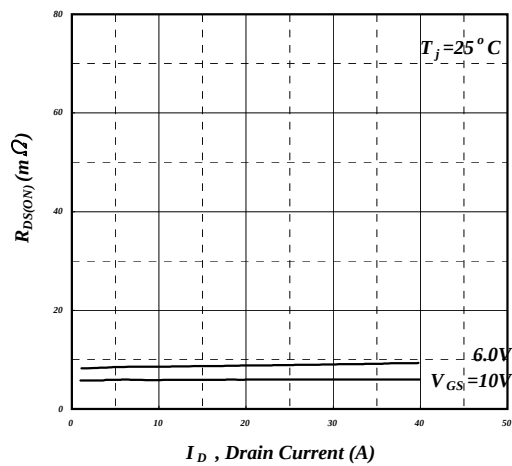


Fig 13. Typ. Drain-Source on State Resistance

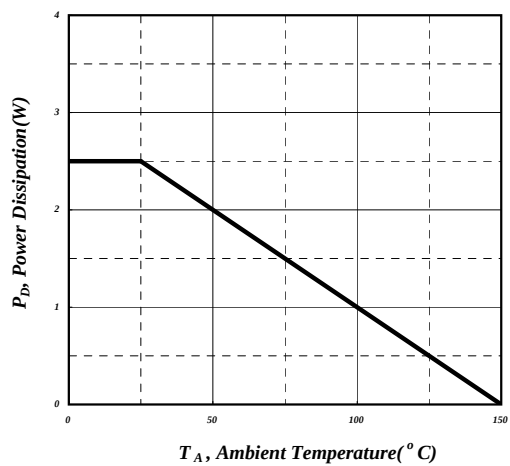
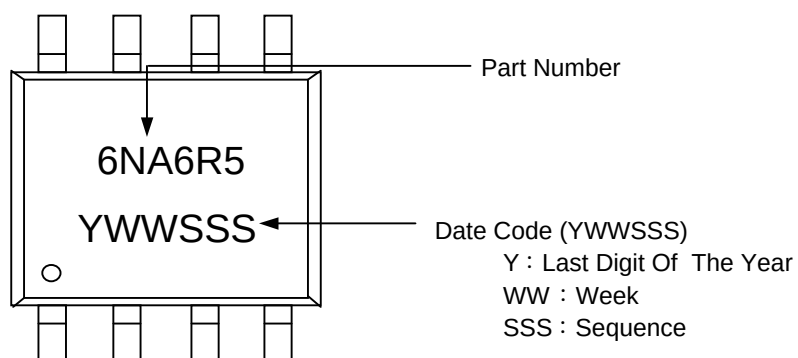
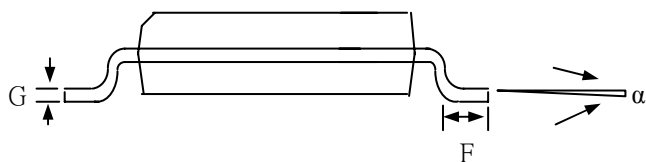
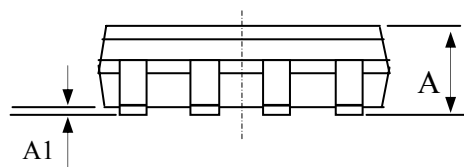
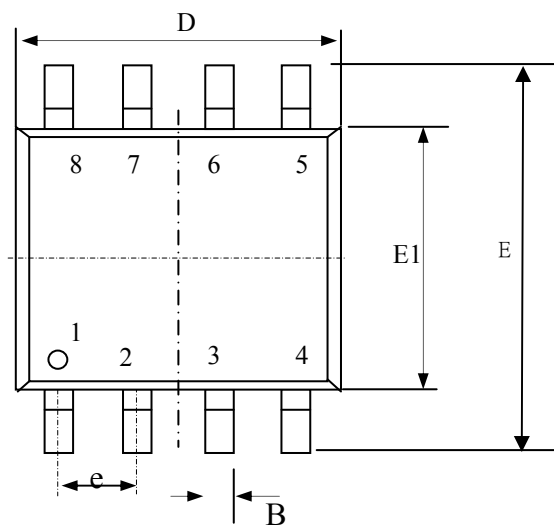


Fig 14. Total Power Dissipation

MARKING INFORMATION



Package Outline : SO-8



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.05	0.15	0.25
B	0.30	0.41	0.51
D	4.80	5.05	5.30
E	5.79	6.00	6.20
E1	3.70	3.90	4.10
e	1.27 TYP		
G	0.17	0.21	0.25
F	0.38	0.83	1.27
α	0°	4°	8°

- 1.All Dimension Are In Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

SO-8 FOOTPRINT :