

XP6679GP-A

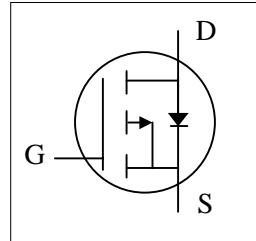
Halogen-Free Product



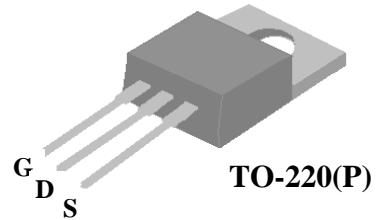
P-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ Lower On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	-40V
$R_{DS(ON)}$	13.5mΩ
I_D	-65A



Description

XP6679 series are innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-220 package is widely preferred for all commercial-industrial through hole applications. The low thermal resistance and low package cost contribute to the worldwide popular package.

Absolute Maximum Ratings@ $T_j=25^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	+25	V
$I_D @ T_C=25^{\circ}\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-65	A
$I_D @ T_C=100^{\circ}\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-41	A
I_{DM}	Pulsed Drain Current ¹	-260	A
$P_D @ T_C=25^{\circ}\text{C}$	Total Power Dissipation	89	W
	Linear Derating Factor	0.71	W/ $^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-c}	Maximum Thermal Resistance, Junction-case	1.4	$^{\circ}\text{C}/\text{W}$
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	62	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	-	-0.02	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-28\text{A}$	-	-	13.5	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-20\text{A}$	-	-	20	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.8	-	-2.5	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-24\text{A}$	-	24	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
	Drain-Source Leakage Current ($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=-32\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-250	μA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=+25\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge	$I_{\text{D}}=-16\text{A}$	-	43	70	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-32\text{V}$	-	7	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=-4.5\text{V}$	-	26	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=-20\text{V}$	-	11	-	ns
t_r	Rise Time	$I_{\text{D}}=-16\text{A}$	-	40	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{\text{GS}}=-10\text{V}$	-	50	-	ns
t_f	Fall Time	$R_D=0.8\Omega$	-	80	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	2870	4590	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=-25\text{V}$	-	960	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	740	-	pF
R_g	Gate Resistance	f=1.0MHz	-	2.5	3.75	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_{\text{S}}=-20\text{A}, V_{\text{GS}}=0\text{V}$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{S}}=-16\text{A}, V_{\text{GS}}=0\text{V},$ $dI/dt=-100\text{A}/\mu\text{s}$	-	37	-	ns
Q_{rr}	Reverse Recovery Charge		-	42	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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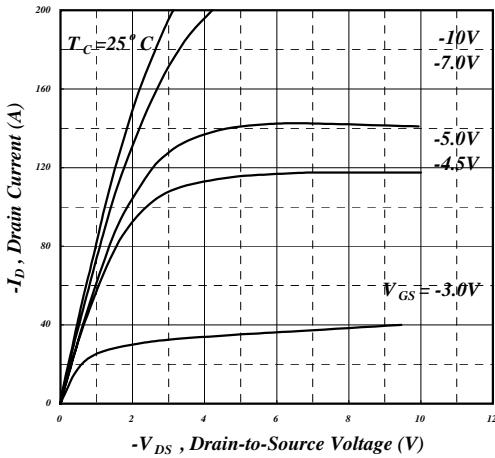


Fig 1. Typical Output Characteristics

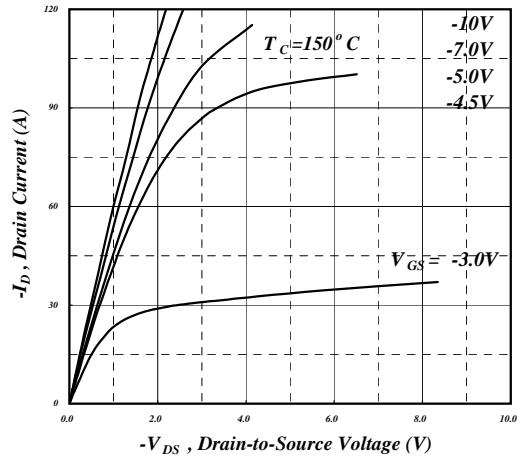


Fig 2. Typical Output Characteristics

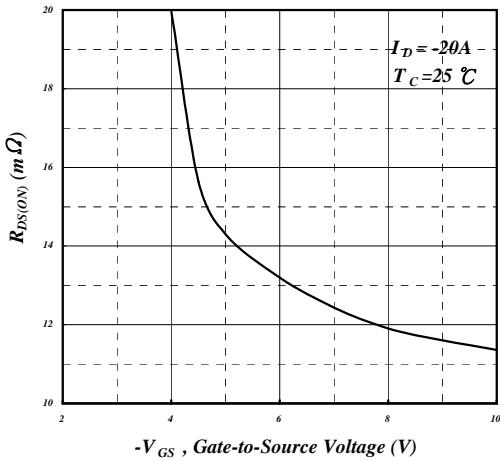


Fig 3. On-Resistance v.s. Gate Voltage

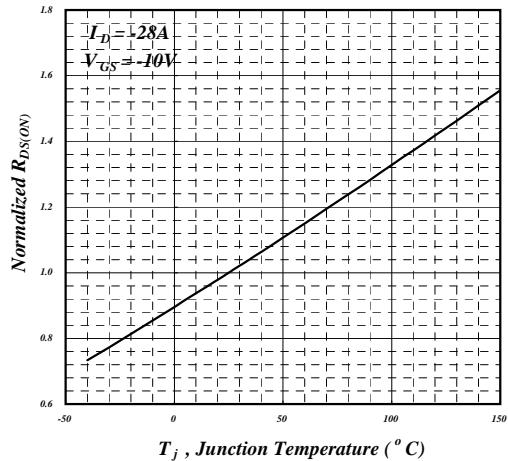


Fig 4. Normalized On-Resistance v.s. Junction Temperature

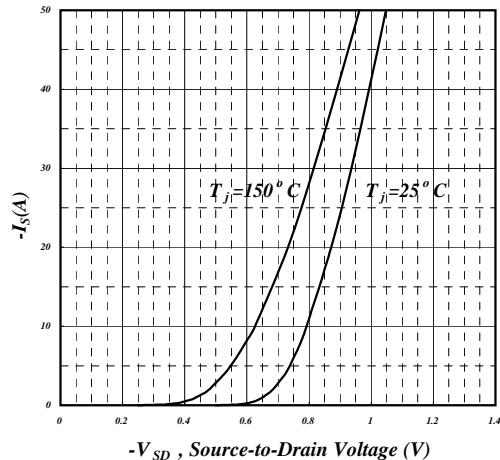


Fig 5. Forward Characteristic of Reverse Diode

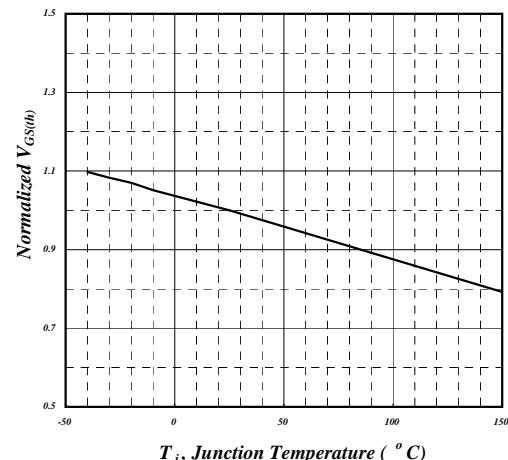


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

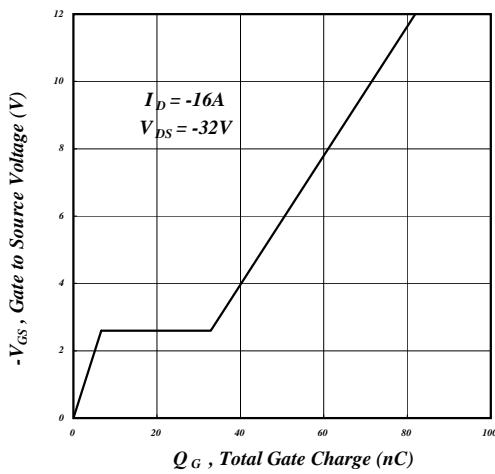


Fig 7. Gate Charge Characteristics

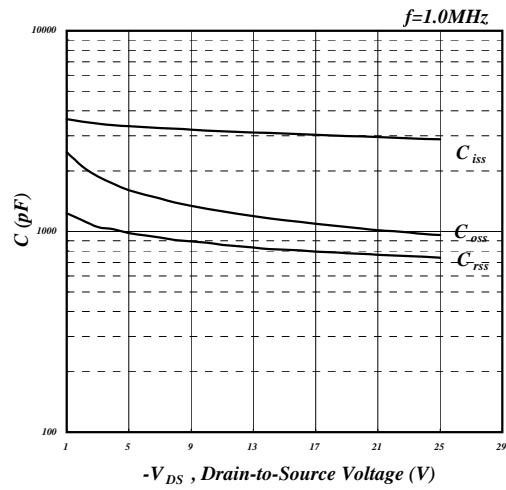


Fig 8. Typical Capacitance Characteristics

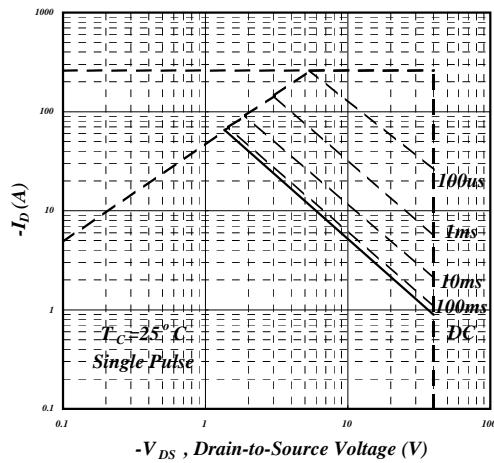


Fig 9. Maximum Safe Operating Area

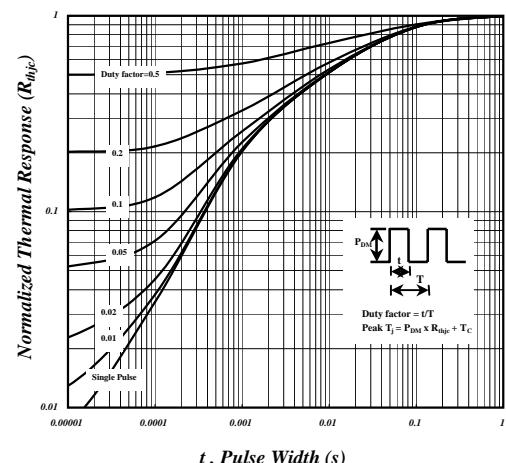


Fig 10. Effective Transient Thermal Impedance

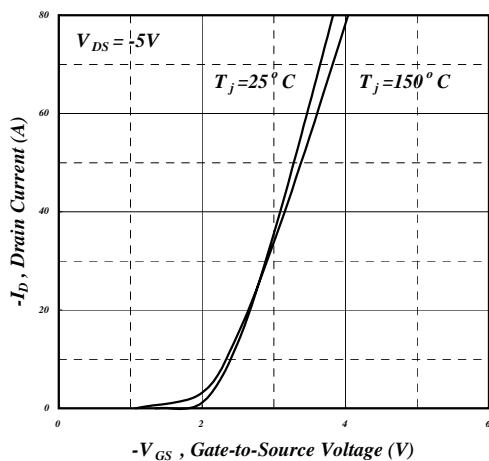


Fig 11. Transfer Characteristics

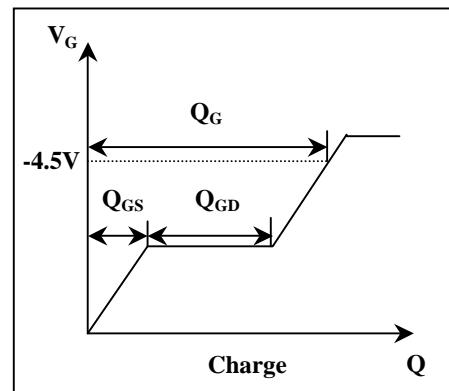
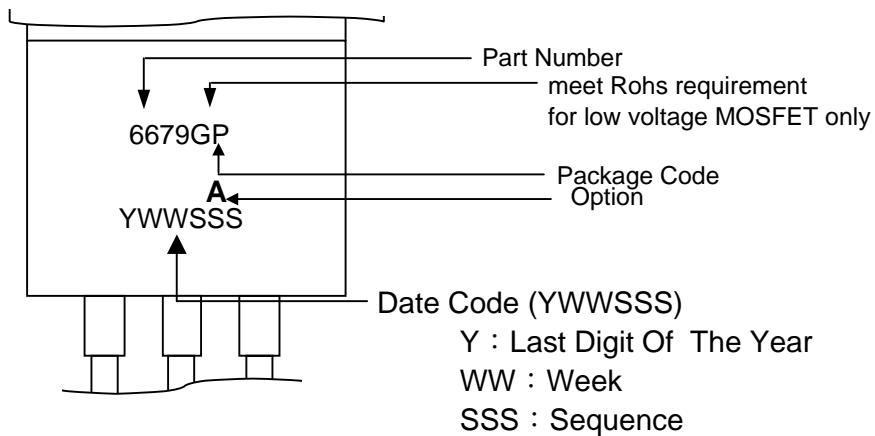


Fig 12. Gate Charge Waveform

MARKING INFORMATION

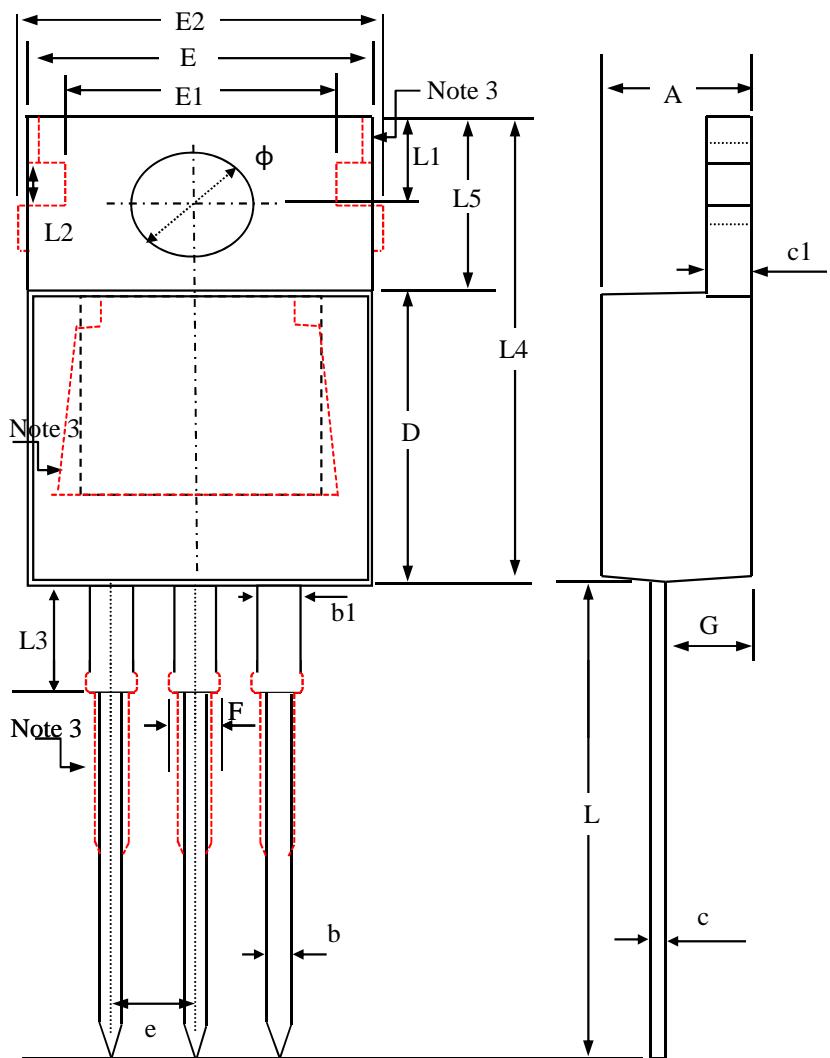
Date Code (YWWSSS)

Y : Last Digit Of The Year

WW : Week

SSS : Sequence

Package Outline : TO-220



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	4.20	4.50	4.80
b	0.60	0.80	1.00
b1	1.10	1.38	1.80
c	0.30	0.48	0.65
c1	1.10	1.30	1.50
E	9.70	10.00	10.40
E1	7.40	8.30	9.20
e	2.54 (ref.)		
L	12.70	13.60	14.50
L1	2.50	2.75	3.00
L2	1.00	1.40	1.80
L3	2.60	3.35	4.10
L4	14.30	15.15	16.00
L5	6.00	6.40	6.80
φ	3.40	3.70	4.00
D	8.30	8.85	9.40
F	1.20	1.41	1.85
G	2.20	2.60	3.00
E2	—	—	11.50

Note:

1. All Dimensions Are in Millimeters.
2. Dimension Does Not Include Mold Protrusions.
3. Thermal PAD and Pin contour is for reference, it may has little difference by option.

TO-220 FOOTPRINT :

