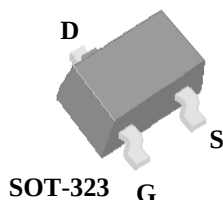
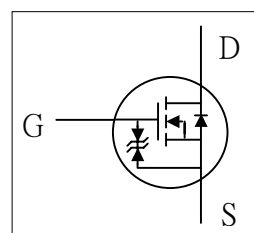


- ▼ Lower Gate Charge
- ▼ Capable of 1.8V Gate Drive
- ▼ Optimal DC/DC Battery Application
- ▼ Halogen Free & RoHS Compliant Product



BV_{DSS}	20V
$R_{DS(ON)}$	65m Ω
I_D^3	2.1A



Description

XP1334 series are innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

Absolute Maximum Ratings@ $T_J=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 8	V
$I_D@T_A=25^\circ\text{C}$	Drain Current ³ , V_{GS} @ 4.5V	2.1	A
$I_D@T_A=70^\circ\text{C}$	Drain Current ³ , V_{GS} @ 4.5V	1.7	A
I_{DM}	Pulsed Drain Current ¹	8	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	0.35	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³	360	$^\circ\text{C}/\text{W}$

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =2A	-	-	65	mΩ
		V _{GS} =2.5V, I _D =1.5A	-	-	75	mΩ
		V _{GS} =1.8V, I _D =1A	-	-	85	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.3	-	1	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =2A	-	12	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V	-	-	10	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±8V, V _{DS} =0V	-	-	±30	uA
Q _g	Total Gate Charge	I _D =2A	-	9	14.4	nC
Q _{gs}	Gate-Source Charge	V _{DS} =10V	-	1	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	2.5	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V	-	6	-	ns
t _r	Rise Time	I _D =1A	-	7	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	18	-	ns
t _f	Fall Time	V _{GS} =5V	-	3	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	570	912	pF
C _{oss}	Output Capacitance	V _{DS} =10V	-	70	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	60	-	pF
R _g	Gate Resistance	f=1.0MHz	-	2.4	4.8	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =1.2A, V _{GS} =0V	-	-	1.2	V
t _{rr}	Reverse Recovery Time	I _S =2A, V _{GS} =0V,	-	14	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	7	-	nC

Notes:

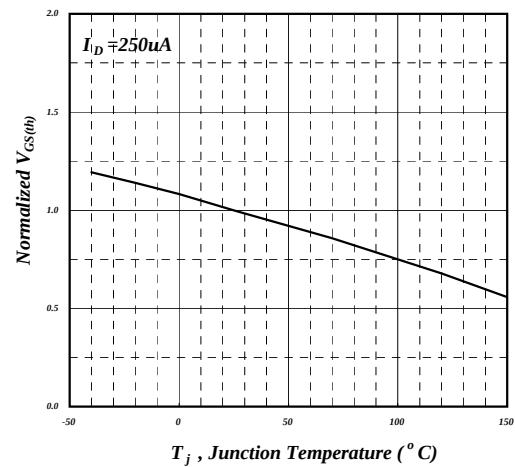
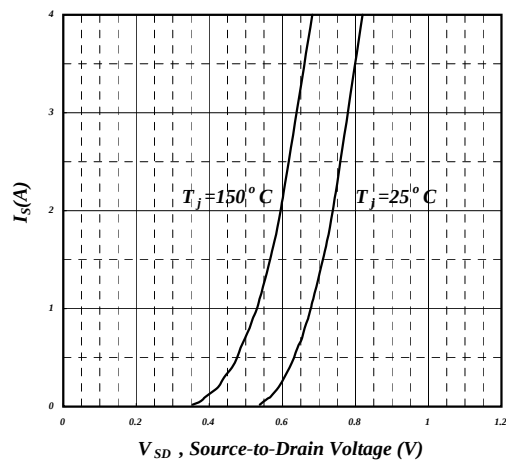
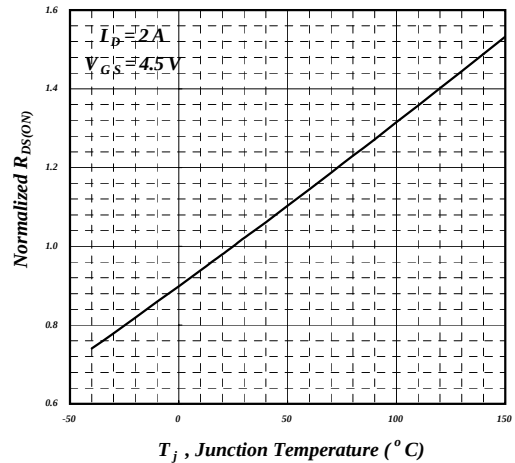
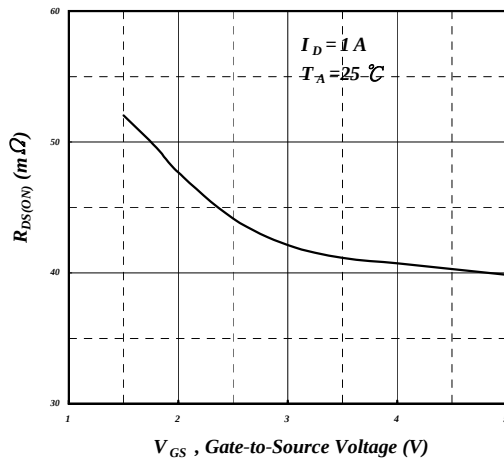
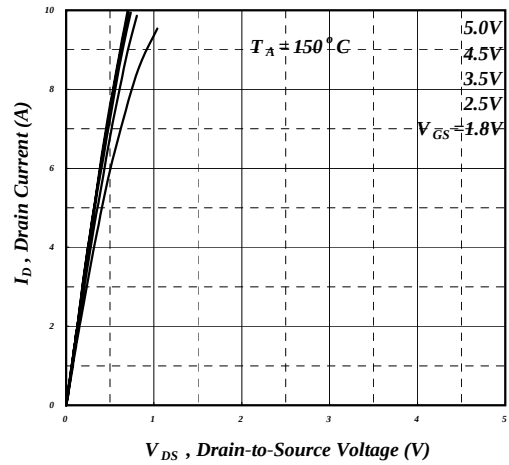
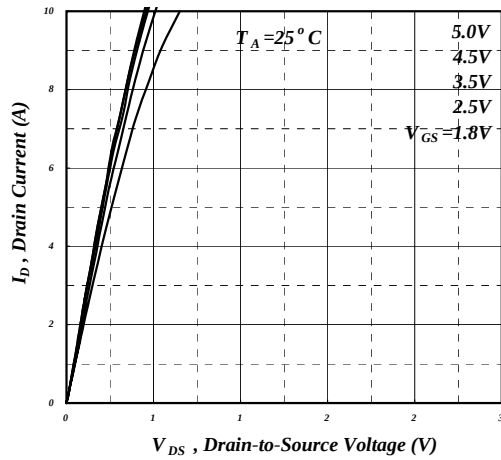
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on FR4 board, t ≤ 10 sec.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

XSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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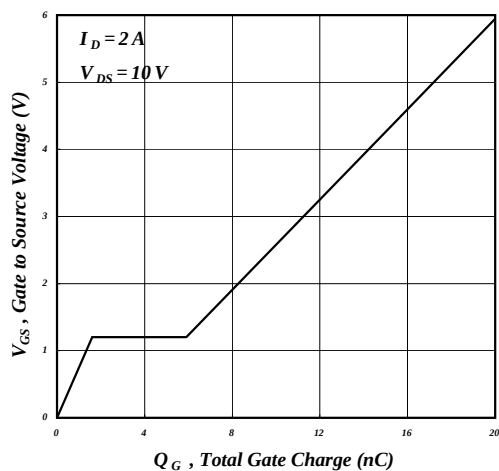


Fig 7. Gate Charge Characteristics

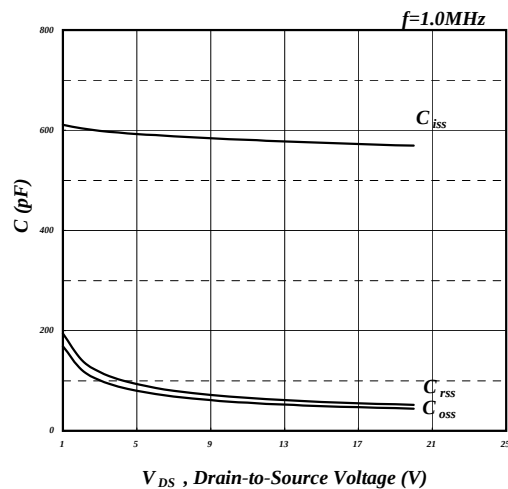


Fig 8. Typical Capacitance Characteristics

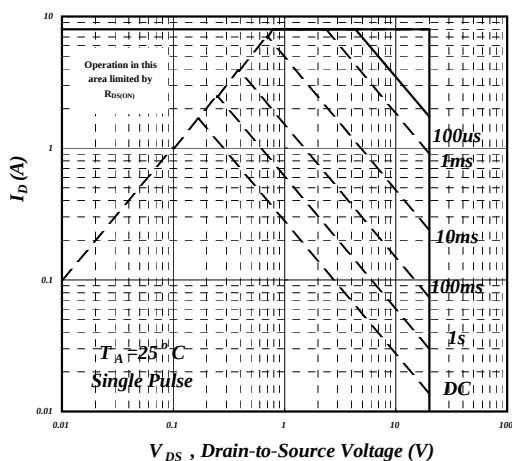


Fig 9. Maximum Safe Operating Area

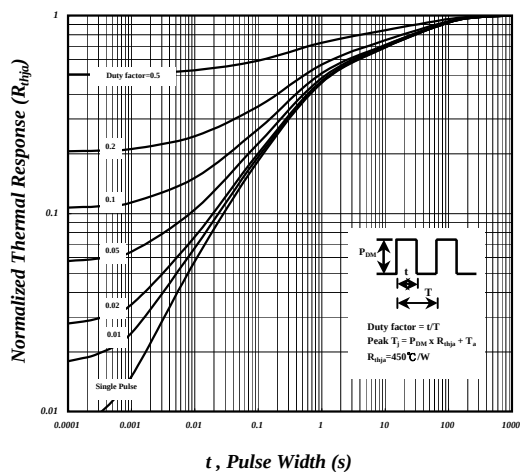


Fig 10. Effective Transient Thermal Impedance

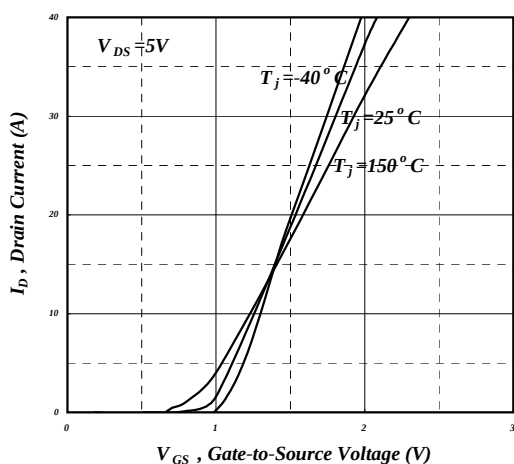


Fig 11. Transfer Characteristics

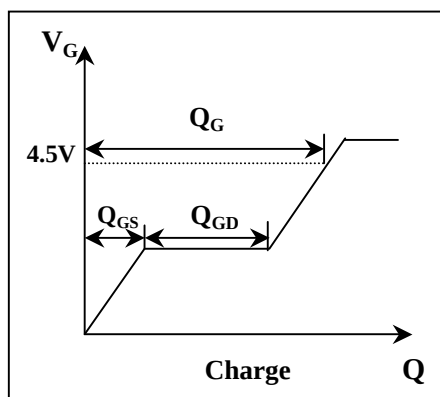
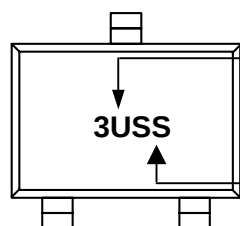


Fig 12. Gate Charge Circuit

MARKING INFORMATION

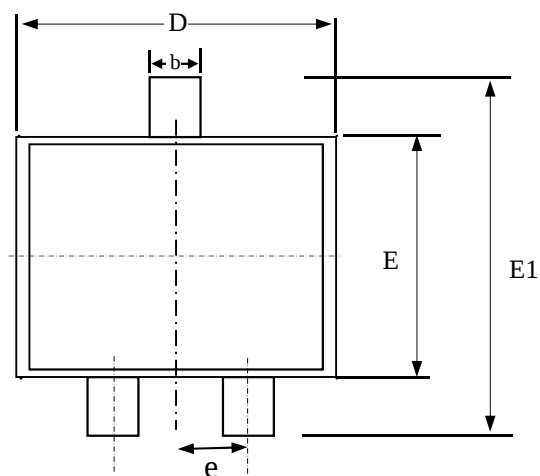
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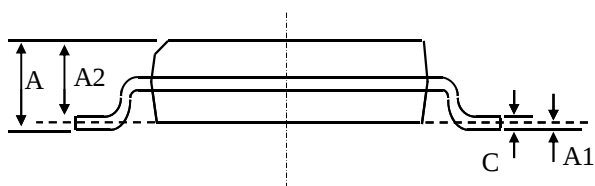
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SS:2003,2007,2011,2015,2019,2023...SS:2002,2006,2010,2014,2018,2022...SS:2001,2005,2009,2013,2017,2021...

Package Outline : SOT-323(SC-70-3L)



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	0.80	0.95	1.10
A1	0.00	0.05	0.10
A2	0.80	0.90	1.00
b	0.00	0.20	0.40
C	0.10	0.18	0.25
e	0.65 REF		
D	1.80	2.00	2.20
E1	1.75	2.10	2.45
E	1.15	1.25	1.35



- 1.All Dimension Are In Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

SOT-323(SC-70-3L) FOOTPRINT :

