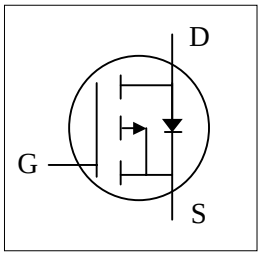
XP10P135M

Halogen-Free Product

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Low Gate Charge**
- ▼ **Fast Switching Characteristic**
- ▼ **Simple Drive Requirement**
- ▼ **RoHS Compliant & Halogen-Free**

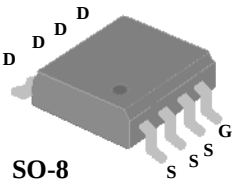


BV_{DSS}	-100V
$R_{DS(ON)}$	135mΩ
I_D[3]	-3A

Description

XP10P135 series are innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The SO-8 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



SO-8

Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25℃	Drain Current, V_{GS} @ 10V[3]	-3	A
I_D@T_A=70℃	Drain Current, V_{GS} @ 10V[3]	-2.4	A
I_{DM}	Pulsed Drain Current[1]	-20	A
P_D@T_A=25℃	Total Power Dissipation[3]	2.5	W
E_{AS}	Single Pulse Avalanche Energy[4]	12.5	mJ
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient[3]	50	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250uA	-100	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V, I_D=-3A	-	-	135	mΩ
		V_{GS}=-4.5V, I_D=-2A	-	-	150	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250uA	-1.3	-	-3	V
g_{fs}	Forward Transconductance	V_{DS}=-5V, I_D=-3A	-	10	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-80V, V_{GS}=0V	-	-	-25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V, V_{DS}=0V	-	-	±0.1	uA
Q_g	Total Gate Charge[5]	I_D=-3A	-	29	46	nC
Q_{gs}	Gate-Source Charge[5]	V_{DS}=-50V	-	6	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge[5]	V_{GS}=-10V	-	5	-	nC
$t_{d(on)}$	Turn-on Delay Time[5]	V_{DS}=-50V	-	12	-	ns
t_r	Rise Time[5]	I_D=-1A	-	5	-	ns
$t_{d(off)}$	Turn-off Delay Time[5]	R_G=3.3Ω	-	42	-	ns
t_f	Fall Time[5]	V_{GS}=-10V	-	18	-	ns
C_{iss}	Input Capacitance[5]	V_{GS}=0V	-	1525	2440	pF
C_{oss}	Output Capacitance[5]	V_{DS}=-80V	-	53	-	pF
C_{rss}	Reverse Transfer Capacitance[5]	f=1.0MHz	-	40	-	pF
Rg	Gate Resistance	f=1.0MHz	-	6.3	12.6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=-1.9A, V_{GS}=0V	-	-	-1.3	V
t_{rr}	Reverse Recovery Time[5]	I_S=-3A, V_{GS}=0V,	-	24	-	ns
Q_{rr}	Reverse Recovery Charge[5]	dI/dt=-100A/μs	-	26	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

3.Surface mounted on 1 in^2 copper pad of FR4 board, t ≤ 10s ; 125 ℃/W when mounted on Min. copper pad.

4.Starting T_j=25°C , V_{DD}=-50V , L=1mH , R_G=25Ω , V_{GS}=-10V.

5.Guaranteed by design.

6.These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}$=150°C.

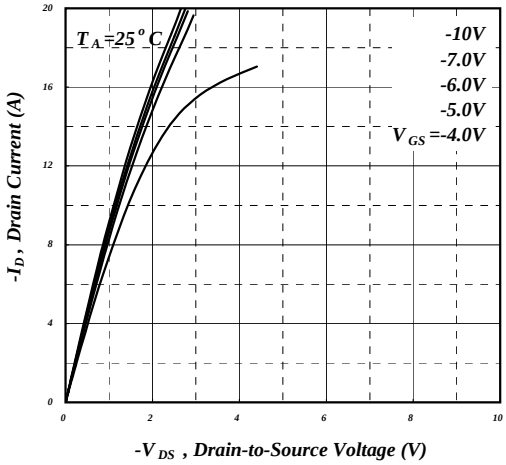


Fig 1. Typical Output Characteristics

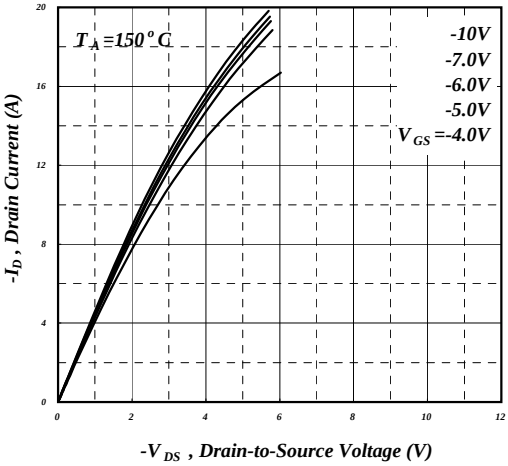


Fig 2. Typical Output Characteristics

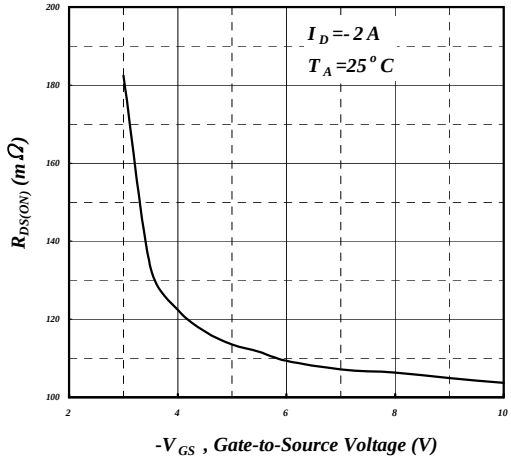


Fig 3. On-Resistance v.s. Gate Voltage

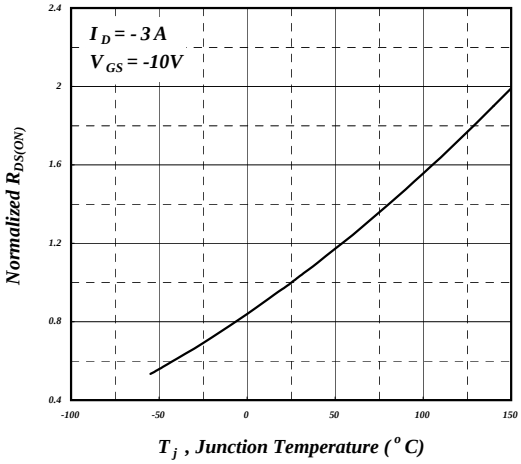


Fig 4. Normalized On-Resistance v.s. Junction Temperature

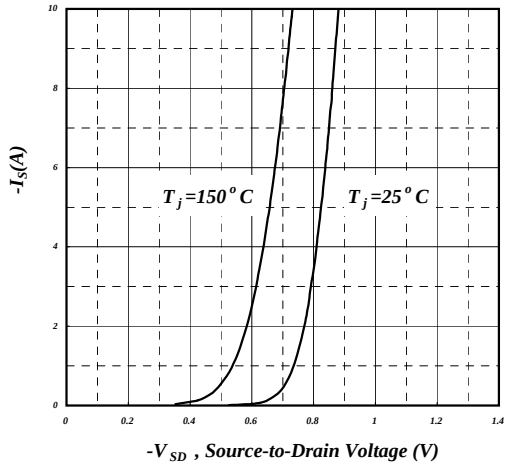


Fig 5. Forward Characteristic of Reverse Diode

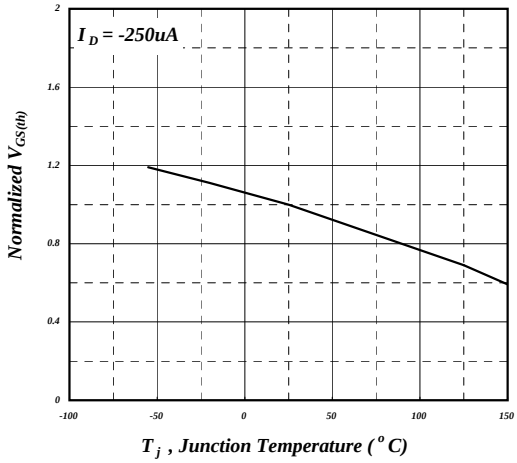


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

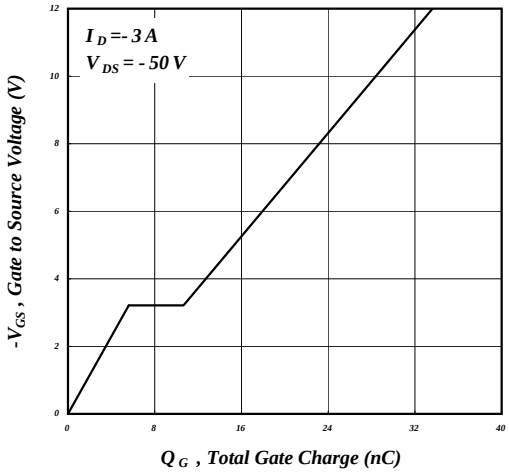


Fig 7. Gate Charge Characteristics

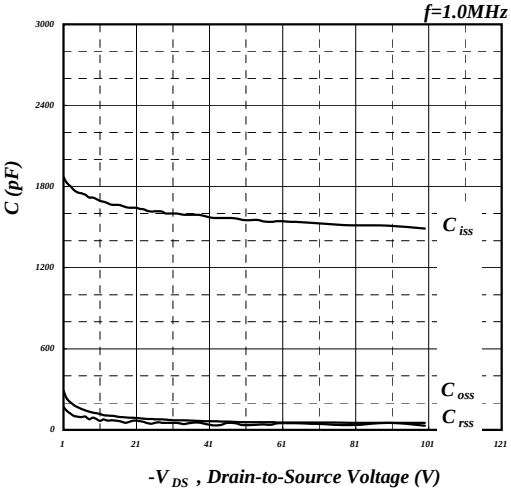


Fig 8. Typical Capacitance Characteristics

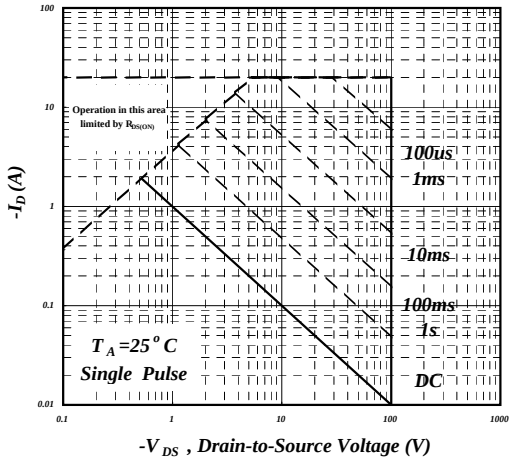


Fig 9. Maximum Safe Operating Area[6]

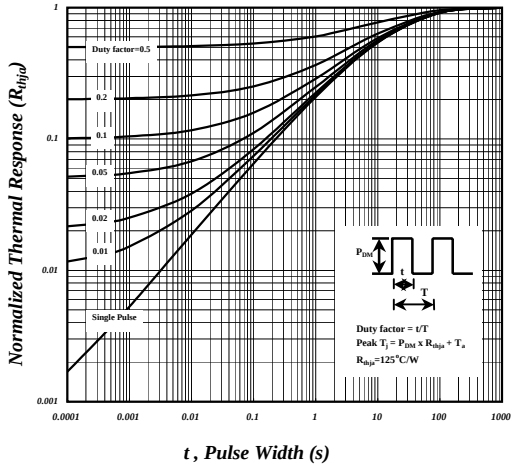


Fig 10. Effective Transient Thermal Impedance

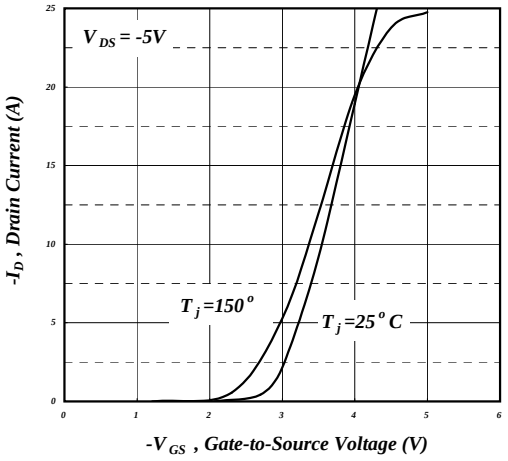


Fig 11. Transfer Characteristics

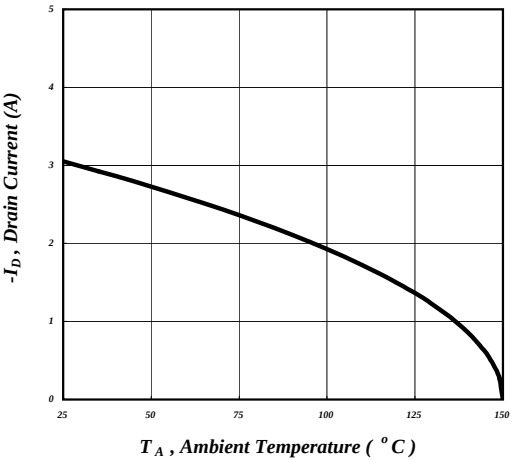


Fig 12. Drain Current v.s. Ambient Temperature

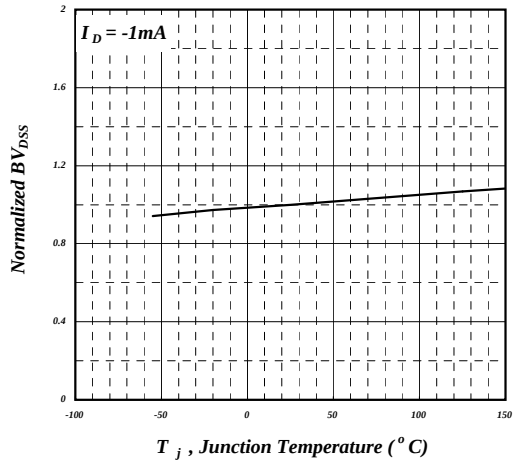


Fig 13. Normalized BV_{DSS} v.s. Junction Temperature

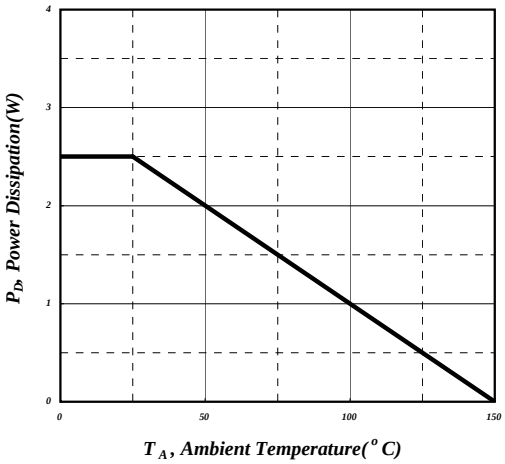


Fig 14. Total Power Dissipation

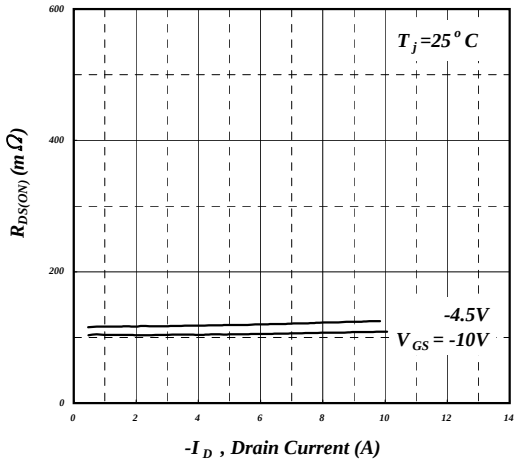
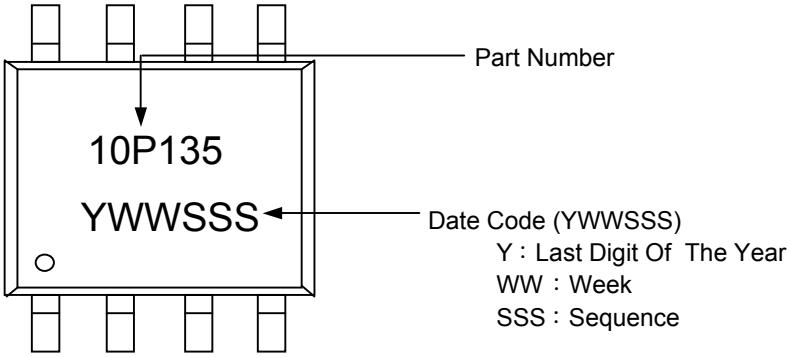
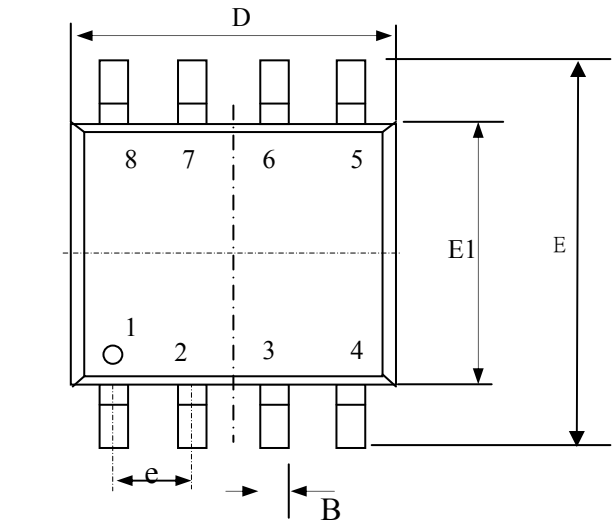


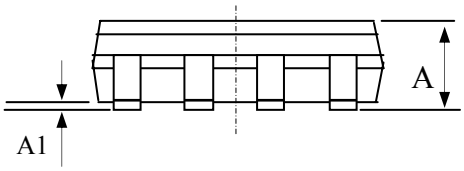
Fig 15. Typ. Drain-Source on State Resistance

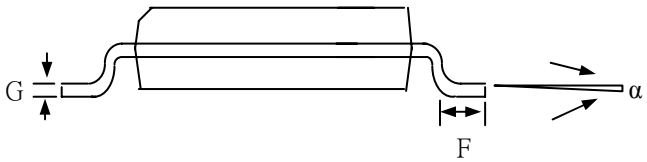
MARKING INFORMATION



Package Outline : SO-8







SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.05	0.15	0.25
B	0.30	0.41	0.51
D	4.80	5.05	5.30
E	5.79	6.00	6.20
E1	3.70	3.90	4.10
e	1.27 TYP		
G	0.17	0.21	0.25
F	0.38	0.83	1.27
α	0°	4°	8°

1.All Dimension Are In Millimeters.

2.Dimension Does Not Include Mold Protrusions.

Draw No. M1-M8-G-v03

SO-8 FOOTPRINT：