

35.0-45.0 GHz GaAs MMIC Low Noise Amplifier

May 2006 - Rev 10-May-06

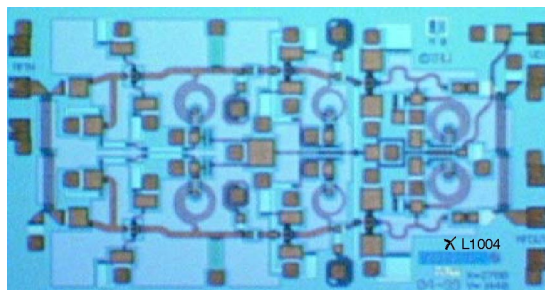
Features

- X Balanced Design
- X Excellent Input/Output Match
- X Self-biased Architecture
- X 17.0 dB Small Signal Gain
- X 1.8 dB Noise Figure
- X 100% On-Wafer RF, DC and Noise Figure Testing
- X 100% Visual Inspection to MIL-STD-883 Method 2010

General Description

Mimix Broadband's three stage balanced 35.0-45.0 GHz GaAs MMIC low noise amplifier has a small signal gain of 17.0 dB with a noise figure of 1.8 dB across the band. This MMIC uses Mimix Broadband's 0.15 μm GaAs PHEMT device model technology, and is based upon electron beam lithography to ensure high repeatability and uniformity. The chip has surface passivation to protect and provide a rugged part with backside via holes and gold metallization to allow either a conductive epoxy or eutectic solder die attach process. This device is well suited for Millimeter-wave Point-to-Point Radio, LMDS, SATCOM and VSAT applications.

Chip Device Layout



Absolute Maximum Ratings

Supply Voltage (Vd)	+5.5 VDC
Supply Current (Id)	130 mA
Input Power (Pin)	-5.0/-1.0 dBm
Storage Temperature (Tstg)	-65 to +165 °C
Operating Temperature (Ta)	-55 to MTTF Table ¹
Channel Temperature (Tch)	MTTF Table ¹

(1) Channel temperature affects a device's MTTF. It is recommended to keep channel temperature as low as possible for maximum life.

Electrical Characteristics (Ambient Temperature T = 25 °C)

Parameter	Units	Min.	Typ.	Max.	Min.	Typ.	Max.
Frequency Range (f)	GHz	35.0	-	40.0	40.0	-	45.0
Input Return Loss (S11)	dB	8.0	16.0	-	15.0	17.0	-
Output Return Loss (S22)	dB	15.0	18.0	-	15.0	18.0	-
Small Signal Gain (S21)	dB	15.0	17.0 ²	-	10.0	14.0 ²	-
Gain Flatness (ΔS_{21})	dB	-	+/-2.0	-	-	+/-2.0	-
Reverse Isolation (S12)	dB	-	-	-	-	-	-
Noise Figure (NF)	dB	-	1.8	3.0	-	2.0	3.0
Output Power for 1 dB Compression (P1dB)	dBm	-	+6.0	-	-	+6.0	-
Output Third Order Intercept Point (OIP3)	dBm	-	-	-	-	-	-
Drain Bias Voltage (Vd)	VDC	-	+4.0	-	-	+4.0	-
Supply Current (Id)	mA	-	85	-	-	85	-

(2) See plots for additional information.

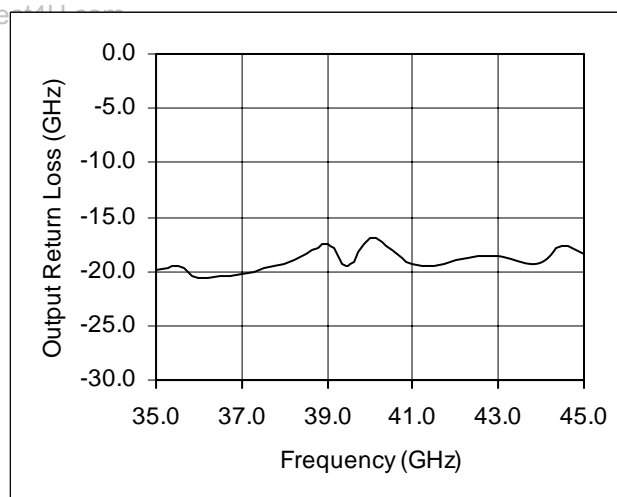
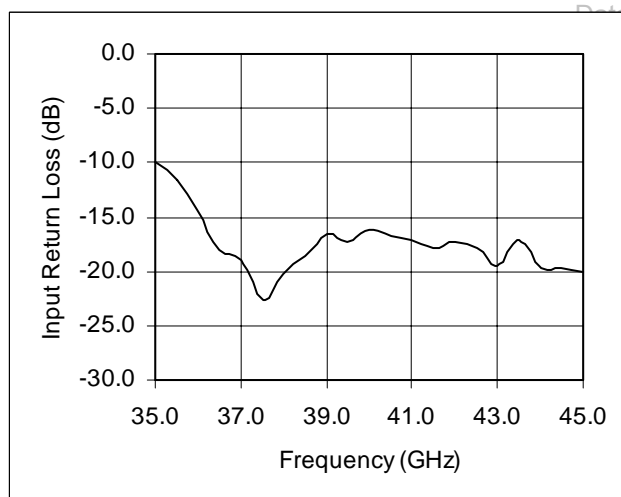
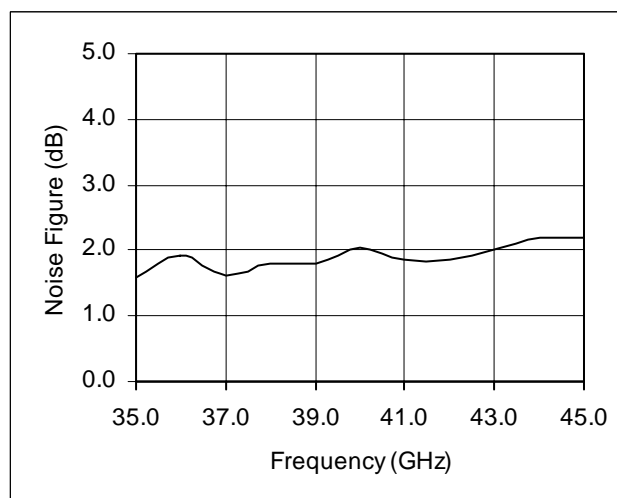
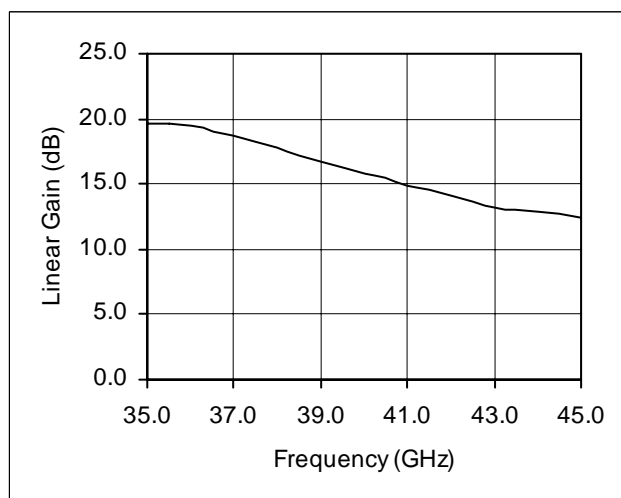
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Low Noise Measurements

Measured Performance Characteristics (Typical Performance at 25°C)

$V_d = 4.0$ V, $I_d = 87$ mA



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S-Parameters

Measured Performance Characteristics (Typical Performance at 25°C)

Vd = 4.0 V, Id = 87 mA

Freq GHz	S11 Mag	S11 Ang	S21 Mag	S21 Ang	S12 Mag	S12 Ang	S22 Mag	S22 Ang
20.00	0.27	13.18	0.79	144.02	0.00	152.61	0.44	87.73
21.00	0.22	1.85	1.29	119.49	0.00	-79.92	0.38	76.54
22.00	0.18	-10.15	1.88	90.50	0.00	74.69	0.30	65.76
23.00	0.15	-24.71	2.50	60.05	0.00	53.19	0.24	56.91
24.00	0.12	-41.16	3.06	30.05	0.00	-121.34	0.18	51.76
25.00	0.10	-59.48	3.58	1.22	0.00	-161.44	0.13	51.95
26.00	0.10	-80.95	4.01	-26.35	0.00	85.45	0.09	59.91
27.00	0.11	-101.16	4.46	-52.01	0.00	133.24	0.07	81.02
28.00	0.14	-117.51	4.98	-77.07	0.00	30.25	0.07	96.40
29.00	0.17	-134.20	5.50	-101.80	0.01	78.68	0.08	104.68
30.00	0.22	-153.32	6.11	-127.11	0.00	80.75	0.09	108.29
31.00	0.28	-171.06	6.76	-152.08	0.01	59.04	0.09	103.52
32.00	0.33	168.73	7.50	-177.94	0.01	81.24	0.09	108.57
33.00	0.37	143.19	8.34	155.30	0.00	-155.79	0.09	108.74
34.00	0.37	118.52	9.14	126.02	0.00	56.29	0.08	122.44
35.00	0.32	87.44	9.57	94.76	0.01	-37.27	0.10	117.37
36.00	0.19	68.27	9.34	62.41	0.01	-8.36	0.09	120.09
37.00	0.11	80.75	8.56	32.90	0.01	30.35	0.10	131.83
38.00	0.10	111.35	7.74	5.96	0.01	-86.95	0.11	127.63
39.00	0.15	111.84	6.82	-18.97	0.00	129.62	0.13	128.29
40.00	0.16	99.58	6.16	-41.88	0.01	160.76	0.14	130.21
41.00	0.14	92.60	5.59	-64.36	0.01	-33.24	0.11	128.30
42.00	0.14	90.18	5.09	-85.91	0.01	143.01	0.11	123.97
43.00	0.11	88.15	4.58	-106.14	0.01	-35.79	0.12	130.62
44.00	0.10	90.61	4.43	-127.87	0.01	-19.68	0.11	146.09
45.00	0.10	85.68	4.15	-149.06	0.02	96.05	0.12	132.15

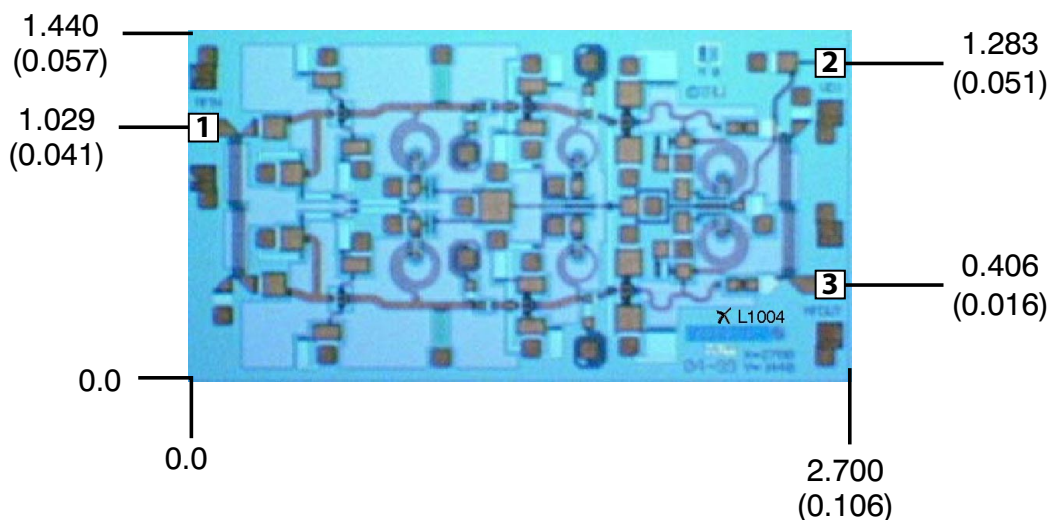
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Mechanical Drawing



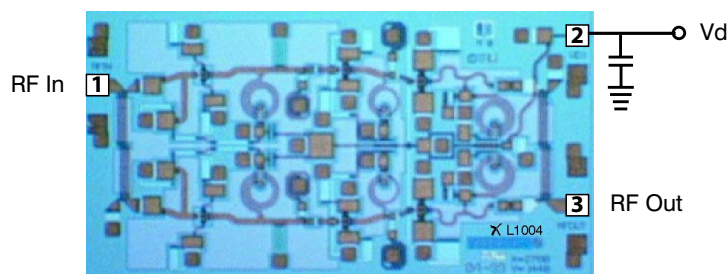
Bond Pad #1 (RF In)

Bond Pad #2 (Vd)

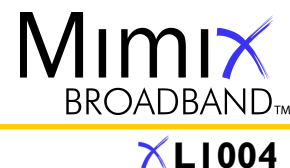
Bond Pad #3 (RF Out)

Bias Arrangement

Bypass Capacitors - See App Note [2]



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XLI004

App Note [1] Biasing - As shown in the bonding diagram, this device operates using a self-biased architecture and only requires a single bias voltage. Bias is nominally $V_d=4V$, $I_d=85mA$.

App Note [2] Bias Arrangement - The DC pad (V_d) should be connected to one DC bypass capacitor (~ 100 - 200 pF). Additional DC bypass capacitance (~ 0.01 μf) is also recommended. Capacitance should be as close to the device as possible.

MTTF Table (TBD)

These numbers were calculated based on accelerated life test information and thermal model analysis received from the fabricating foundry.

Backplate Temperature	Channel Temperature	Rth	MTTF Hours	FITs
55 deg Celsius	deg Celsius	C/W	E+	E+
75 deg Celsius	deg Celsius	C/W	E+	E+
95 deg Celsius	deg Celsius	C/W	E+	E+

Bias Conditions: $V_d=4.0V$, $I_d=85$ mA

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LI004

Handling and Assembly Information

CAUTION! - Mimix Broadband MMIC Products contain gallium arsenide (GaAs) which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- *Do not ingest.*
- *Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.*
- *Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.*

Life Support Policy - Mimix Broadband's products are not authorized for use as critical components in life support devices or systems without the express written approval of the President and General Counsel of Mimix Broadband. As used herein: (1) Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user. (2) A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ESD - Gallium Arsenide (GaAs) devices are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic containers, which should be opened in cleanroom conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickups or, with care, sharp tweezers.

Die Attachment - GaAs Products from Mimix Broadband are 0.100 mm (0.004") thick and have vias through to the backside to enable grounding to the circuit. Microstrip substrates should be brought as close to the die as possible. The mounting surface should be clean and flat. If using conductive epoxy, recommended epoxies are Ablestick 84-1LMI or 84-1LMIT cured in a nitrogen atmosphere per manufacturer's cure schedule. Apply epoxy sparingly to avoid getting any on to the top surface of the die. An epoxy fillet should be visible around the total die periphery. If eutectic mounting is preferred, then a fluxless gold-tin (AuSn) preform, approximately 0.001² thick, placed between the die and the attachment surface should be used. A die bonder that utilizes a heated collet and provides scrubbing action to ensure total wetting to prevent void formation in a nitrogen atmosphere is recommended. The gold-tin eutectic (80% Au 20% Sn) has a melting point of approximately 280°C (Note: Gold Germanium should be avoided). The work station temperature should be 310°C ± 10°C. Exposure to these extreme temperatures should be kept to minimum. The collet should be heated, and the die pre-heated to avoid excessive thermal shock. Avoidance of air bridges and force impact are critical during placement.

Wire Bonding - Windows in the surface passivation above the bond pads are provided to allow wire bonding to the die's gold bond pads. The recommended wire bonding procedure uses 0.076 mm x 0.013 mm (0.003" x 0.0005") 99.99% pure gold ribbon with 0.5-2% elongation to minimize RF port bond inductance. Gold 0.025 mm (0.001") diameter wedge or ball bonds are acceptable for DC Bias connections. Aluminum wire should be avoided. Thermo-compression bonding is recommended though thermosonic bonding may be used providing the ultrasonic content of the bond is minimized. Bond force, time and ultrasonics are all critical parameters. Bonds should be made from the bond pads on the die to the package or substrate. All bonds should be as short as possible.

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