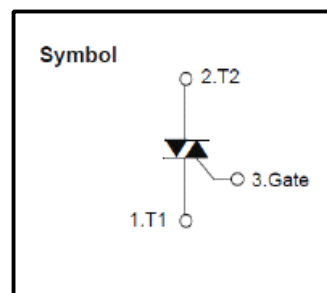


Sensitive Gate Triac

Features

- Repetitive Peak off -State Voltage:600V
- R.M.S On-State Current($I_T(RMS)$)=8A)
- High Commutation dv/dt
- Isolation Voltage (V_{ISO} =1500V AC)



General Description

Sensitive gate triggering Triac is suitable for direct coupling to TTL, CMOS and application such as various logic Functions, low power AC switching applications, such as fanspeed, small light controllers and home appliance equipment.



Absolute Maximum Ratings ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

symbol	Parameter	condition	Ratings	Units
V_{DRM}	Repetitive Peak Off-State Voltage		600	V
$I_T(RMS)$	R.M.S On-State Current	$T_c=89^{\circ}\text{C}$	8.0	A
I_{TSM}	Surge On-State Current	One Cycle, 50Hz/60Hz, Peak, Non-Repetitive	80/88	A
I^2t	I^2t		32	A^2s
P_{GM}	Peak Gate Power Dissipation		5.0	W
$P_{G(AV)}$	Average Gate Power dissipation		0.5	W
I_{GM}	Peak Gate Current		2.0	A
V_{GM}	Peak Gate Voltage		10	V
V_{ISO}	Isolation Breakdown voltage(R.M.S.)	A.C 1 minute	1500	V
T_J	Operating Junction Temperature		-40~125	$^{\circ}\text{C}$
T_{STG}	Storage Temperature		-40~150	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta Jc}$	Thermal Resistance Junction to Case	3.7	$^{\circ}\text{C/W}$

Electrical Characteristics(T_c=25℃ unless otherwise noted)

Symbol	Items		conditions	Ratin			Unit
				Min	Typ	Max	
I _{DRM}	Repetitive Peak Off-State Current		V _D =V _{DRM} ,Single Phase, Half Wave T _J =125℃	-	-	2.0	mA
V _{TM}	Peak On-State Voltage		I _T =12A,Inst.Measurement	-	-	1.4	V
I ⁺ _{GT1}	I	Gate Trigger Current	V _D =6V,R _L =10Ω	-	-	30	mA
I ⁻	II			-	-	30	
I ⁻	III			-	-	30	
V ⁺ _{GT1}	I	Gate Trigger Voltage	V _D =6V,R _L =10Ω	-	-	1.5	V
V _{GT1}	II			-	-	1.5	
V _{GT3}	III			-	-	1.5	
V _{GD}	Non-Trigger Gate Voltage		T _J =125℃ ,V _D =1/2V _{DRM}	0.2	-	-	V
(dv/dt) _c	Critical Rate of Rise Off-State Voltage at Commutation		T _J =125℃ ,[di/dt] _c =-4.0A/ms, V _D =2/3V _{DRM}	10	-	-	V/μs
I _H	Holding Current			-	15	-	mA

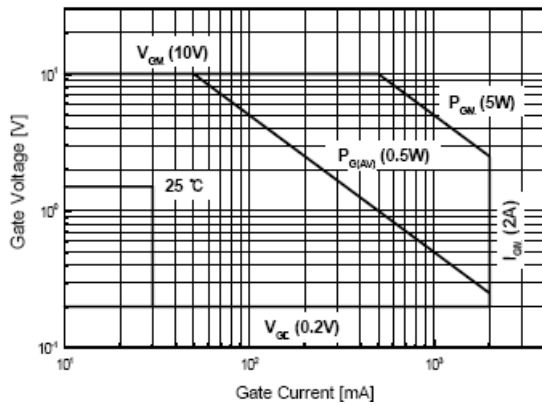


Fig1. Gate Characteristics

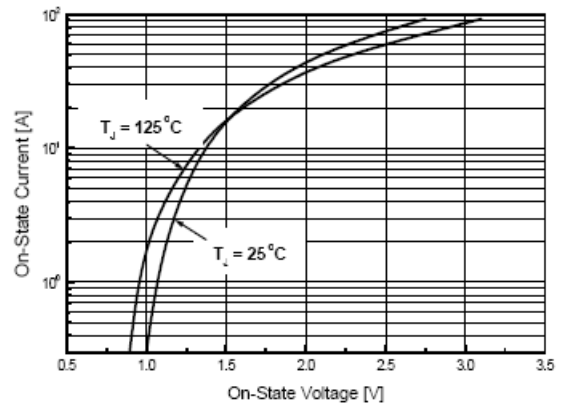


Fig.2 On-State Voltage

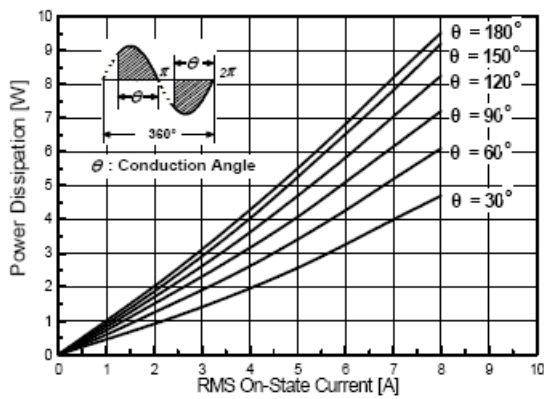


Fig.3 On State Current vs. Maximum Power Dissipation

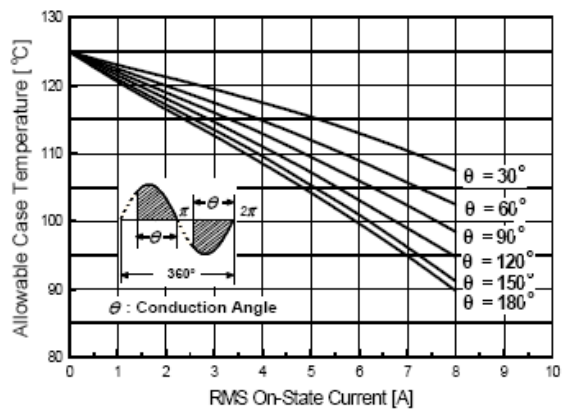


Fig.4 On State Current vs. Allowable Case Temperature

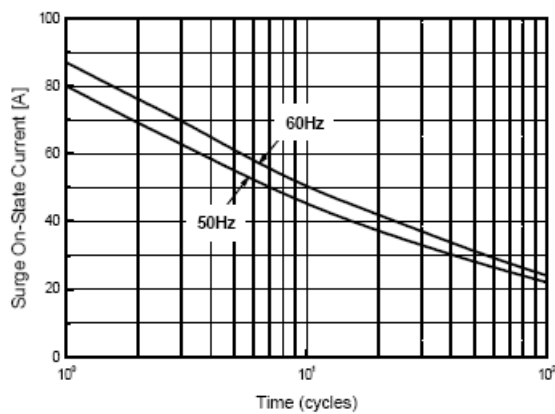


Fig.5 Surge On-State Current Rating (Non-Repetitive)

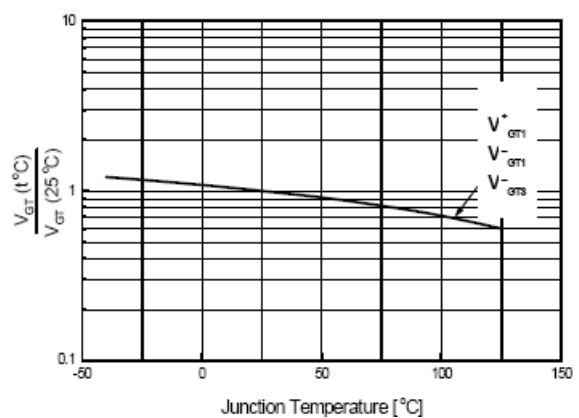


Fig.6 Gate Trigger Voltage vs. Junction Temperature

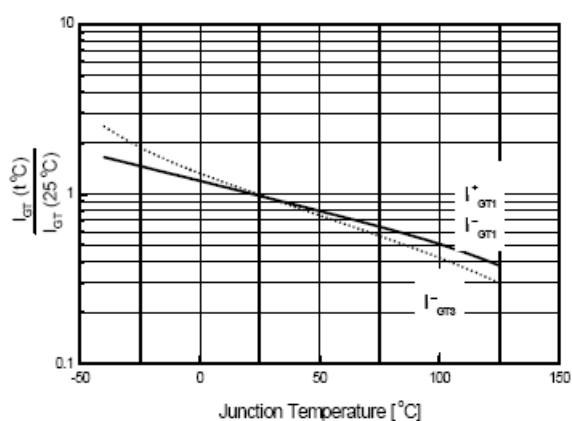


Fig.7 Gate Trigger Current vs. Junction Temperature

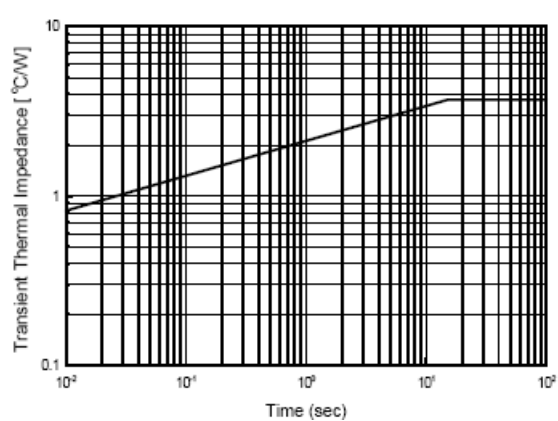


Fig.8 Transient Thermal Impedance

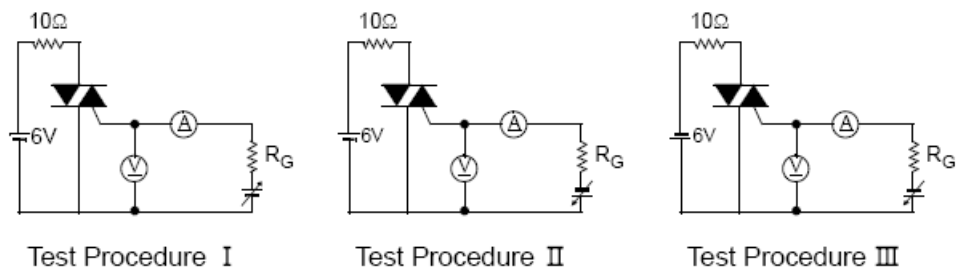


Fig.9 Gate Trigger Characteristics Test Circuit

TO-252 Package Dimension

Unit:mm

