

P-Channel MOSFET

### **General Description**

The WSD4018DN22 is the highest performance trench P-Channel MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD4018DN22 meet the RoHS and Green Product requirement with full function reliability approved.

### **Product Summery**

| BV <sub>DSS</sub> | R <sub>DSON</sub> | Ι <sub>D</sub> |
|-------------------|-------------------|----------------|
| -40V              | 26mΩ              | -18A           |

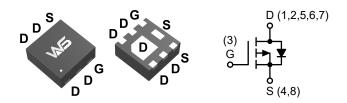
### Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

### **DFN2X2-6S Pin Configuration**



### **Absolute Maximum Ratings**

| Symbol                              | Parameter  | Rating     | Units |  |
|-------------------------------------|--|------------|-------|--|
| V <sub>DS</sub>                     | Drain-Source Voltage   | -40        | V     |  |
| V <sub>GS</sub>                     | Gate-Source Voltage  | ±20        | V     |  |
| I <sub>D</sub> @T₀=25℃              | Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>  | -18        | А     |  |
| I <sub>D</sub> @T <sub>c</sub> =70℃ | Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>  | -14.6      | А     |  |
| I <sub>DM</sub>                     | 300µS Pulsed Drain Current,V <sub>GS</sub> =-4.5V <sup>2</sup> | 54         | A     |  |
| P₀@T₀=25℃                           | Total Power Dissipation <sup>3</sup>                           | 19         | W     |  |
| T <sub>STG</sub>                    | Storage Temperature Range                                      | -55 to 150 | °C    |  |
| TJ                                  | Operating Junction Temperature Range                           | -55 to 150 | °C    |  |

### Thermal Data

| Symbol           | Parameter  | Тур. | Max. | Unit |
|------------------|--|------|------|------|
| R <sub>θJA</sub> | Thermal Resistance Junction-ambient <sup>1</sup> |      | 36   | °C/W |
| R <sub>θJC</sub> | Thermal Resistance Junction-Case <sup>1</sup>    |      | 6.5  | °C/W |



**P-Channel MOSFET** 

# Electrical Characteristics ÁQ/JMÁGÍ »ÔÁV} |^••ÁJc@\; ã^ÁP[ &åD

| Symbol                               | Parameter   | Conditions  | Min. | Тур.  | Max. | Unit |  |
|--------------------------------------|---|---|------|-------|------|------|--|
| BV <sub>DSS</sub>                    | Drain-Source Breakdown Voltage  | V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA                          | -40  |       |      | V    |  |
| $\triangle BV_{DSS} / \triangle T_J$ | BVDSS Temperature Coefficient   | Reference to 25 $^\circ\!\mathrm{C}$ , I_D=-1mA                       |      | -0.01 |      | V/℃  |  |
| Б                                    | Static Drain-Source On-Resistance <sup>2</sup>                          | V <sub>GS</sub> =-10V , I <sub>D</sub> =-8.0A                         |      | 26    | 34   |      |  |
| R <sub>DS(ON)</sub>                  |   | V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-6.0A                        |      | 31    | 42   | mΩ   |  |
| V <sub>GS(th)</sub>                  | Gate Threshold Voltage  |   | -1.0 | -1.5  | -3.0 | V    |  |
| $	riangle V_{GS(th)}$                | V <sub>GS(th)</sub> Temperature Coefficient                             | $V_{GS} = V_{DS}$ , $I_D = -2300A$                                    |      | 3.13  |      | mV/℃ |  |
|                                      | Drain-Source Leakage Current  | $V_{DS}$ =-40V , $V_{GS}$ =0V , T <sub>J</sub> =25 $^{\circ}$ C       |      |       | -1   |      |  |
| I <sub>DSS</sub>                     |   | $V_{DS}$ =-40V , $V_{GS}$ =0V , T <sub>J</sub> =55 $^{\circ}$ C       |      |       | -5   | uA   |  |
| I <sub>GSS</sub>                     | Gate-Source Leakage Current   | $V_{GS}$ = $\pm20V$ , $V_{DS}$ = $0V$                                 |      |       | ±100 | nA   |  |
| Qg                                   | Total Gate Charge (-4.5V)   |   |      | 27    |      |      |  |
| Q <sub>gs</sub>                      | Gate-Source Charge  | V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-1.5A |      | 2.5   |      | nC   |  |
| Q <sub>gd</sub>                      | Gate-Drain Charge   |   |      | 6.7   |      |      |  |
| T <sub>d(on)</sub>                   | Turn-On Delay Time  |   |      | 9.8   |      |      |  |
| Tr                                   | Rise Time   | V <sub>DD</sub> =-20V , V <sub>GS</sub> =-10V ,                       |      | 11    |      |      |  |
| T <sub>d(off)</sub>                  | Turn-Off Delay Time $R_G=3\Omega$ , RL=10 $\Omega$                      |   |      | 54    |      | ns   |  |
| T <sub>f</sub>                       | Fall Time   |   |      | 7.1   |      |      |  |
| C <sub>iss</sub>                     | Input Capacitance   |   |      | 1560  |      |      |  |
| C <sub>oss</sub>                     | Output Capacitance V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , f=1MHz |   |      | 116   |      | pF   |  |
| Crss                                 | Reverse Transfer Capacitance  |   |      | 97    |      |      |  |

### **Diode Characteristics**

| Symbol          | Parameter                                | Conditions   | Min. | Тур. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| Is              | Continuous Source Current <sup>1,4</sup> | $V_G=V_D=0V$ , Force Current                           |      |      | -18  | А    |
| V <sub>SD</sub> | Diode Forward Voltage <sup>2</sup>       | $V_{GS}$ =0V , $I_{S}$ =-1A , $T_{J}$ =25 $^{\circ}$ C |      |      | -1.2 | V    |

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, t $\leq$ 10sec.

2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%

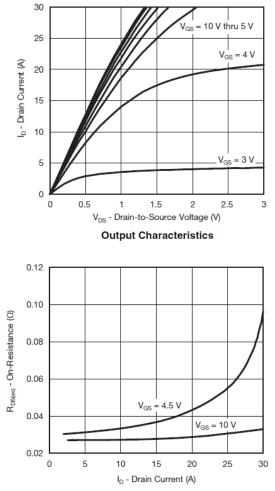
3. The power dissipation is limited by 150  $^\circ\!\mathrm{C}$  junction temperature

4. The data is theoretically the same as  $I_{\text{D}}$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

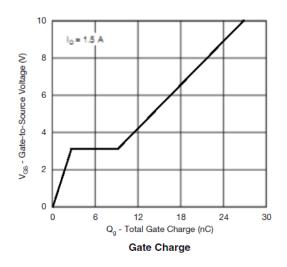


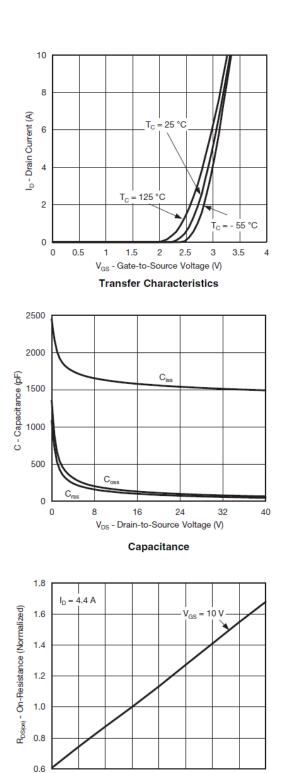
#### **P-Channel MOSFET**

## **Typical Characteristics**



On-Resistance vs. Drain Current and Gate Voltage





- 25

- 50

0

25

50

T<sub>J</sub> - Junction Temperature (°C)

**On-Resistance vs. Junction Temperature** 

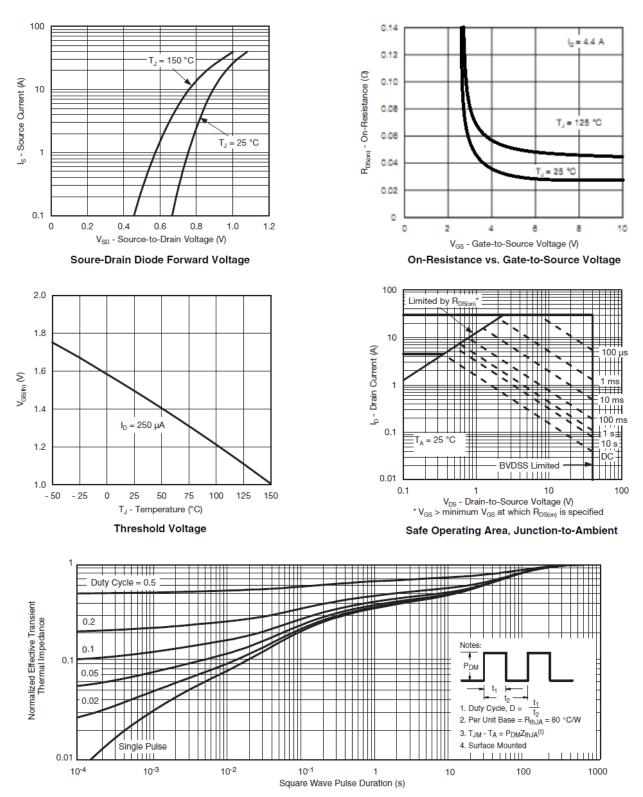
75 100

150

125



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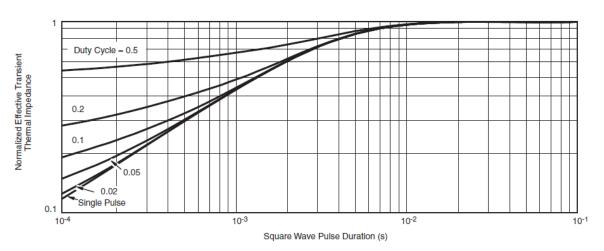


Normalized Thermal Transient Impedance, Junction-to-Ambient





**P-Channel MOSFET** 

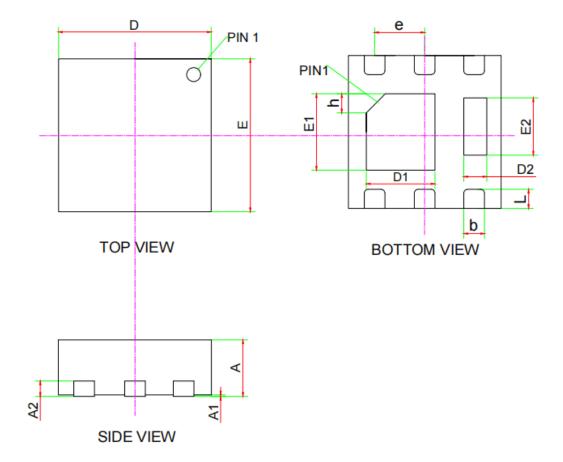


Normalized Thermal Transient Impedance, Junction-to-Case



P-Channel MOSFET

# Packaging information



| SYMBOL | MIN      | NOM  | MAX  |  |
|--------|----------|------|------|--|
| A      | 0.70     | 0.75 | 0.80 |  |
| A1     | 0.00     | 0.02 | 0.05 |  |
| A2     | 0.18     | 0.20 | 0.25 |  |
| b      | 0.20     | 0.27 | 0.34 |  |
| D      | 1.95     | 2.00 | 2.05 |  |
| E      | 1.95     | 2.00 | 2.05 |  |
| D1     | 0.80     | 0.90 | 1.00 |  |
| E1     | 0.90     | 1.00 | 1.10 |  |
| D2     | 0.20     | 0.30 | 0.40 |  |
| E2     | 0.65     | 0.75 | 0.85 |  |
| L      | 0.20     | 0.25 | 0.35 |  |
| h      | 0.20     | 0.25 | 0.30 |  |
| e      | 0.65 BSC |      |      |  |



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