

P-Channel MOSFET

General Description

The WSD4018DN22 is the highest performance trench P-Channel MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD4018DN22 meet the RoHS and Green Product requirement with full function reliability approved.

Product Summery

BV _{DSS}	R _{DSON}	Ι _D
-40V	26mΩ	-18A

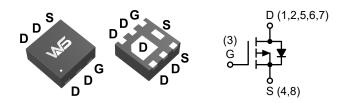
Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

DFN2X2-6S Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V _{DS}	Drain-Source Voltage	-40	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D @T₀=25℃	Continuous Drain Current, V _{GS} @ -10V ¹	-18	А	
I _D @T _c =70℃	Continuous Drain Current, V _{GS} @ -10V ¹	-14.6	А	
I _{DM}	300µS Pulsed Drain Current,V _{GS} =-4.5V ²	54	A	
P₀@T₀=25℃	Total Power Dissipation ³	19	W	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹		36	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹		6.5	°C/W



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Electrical Characteristics ÁQ/JMÁGÍ »ÔÁV} |^••ÁJc@\; ã^ÁP[&åD

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage V _{GS} =0V , I _D =-250uA		-40			V	
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\mathrm{C}$, I_D=-1mA		-0.01		V/℃	
Б	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-8.0A		26	34	mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V , I _D =-6.0A		31	42		
V _{GS(th)}	Gate Threshold Voltage		-1.0	-1.5	-3.0	V	
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D = -250 uA$		3.13		mV/℃	
	Drain Source Lookage Current	V_{DS} =-40V , V_{GS} =0V , T _J =25 $^{\circ}$ C			-1		
I _{DSS}	Drain-Source Leakage Current	V_{DS} =-40V , V_{GS} =0V , T _J =55 $^{\circ}$ C			-5	uA	
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm20V$, $V_{DS}=0V$			±100	nA	
Qg	Total Gate Charge (-4.5V)			27			
Q _{gs}	Gate-Source Charge	V _{DS} =-20V , V _{GS} =-10V , I _D =-1.5A		2.5		nC	
Q _{gd}	Gate-Drain Charge			6.7			
T _{d(on)}	Turn-On Delay Time			9.8			
Tr	Rise Time	V _{DD} =-20V , V _{GS} =-10V ,		11			
T _{d(off)}	Turn-Off Delay Time $R_G=3\Omega$, RL=10 Ω			54		ns	
T _f	Fall Time			7.1			
C _{iss}	Input Capacitance			1560			
C _{oss}	Output Capacitance V _{DS} =-20V , V _{GS} =0V , f=1MHz			116		pF	
C _{rss}	Reverse Transfer Capacitance			97			

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current			-18	А
V _{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =-1A , T_{J} =25 $^{\circ}$ C			-1.2	V

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t \leq 10sec.

2.The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%

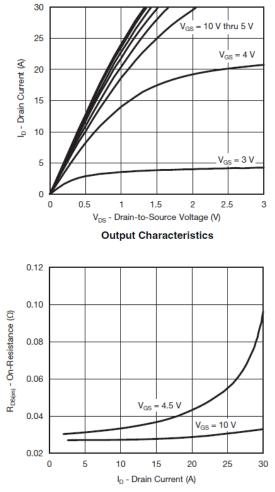
3. The power dissipation is limited by 150 $^\circ\!\mathrm{C}$ junction temperature

4. The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.

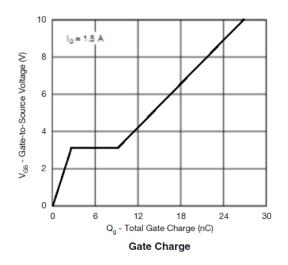


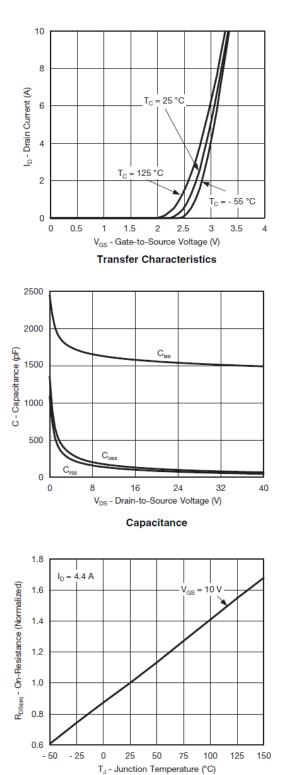
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Typical Characteristics



On-Resistance vs. Drain Current and Gate Voltage

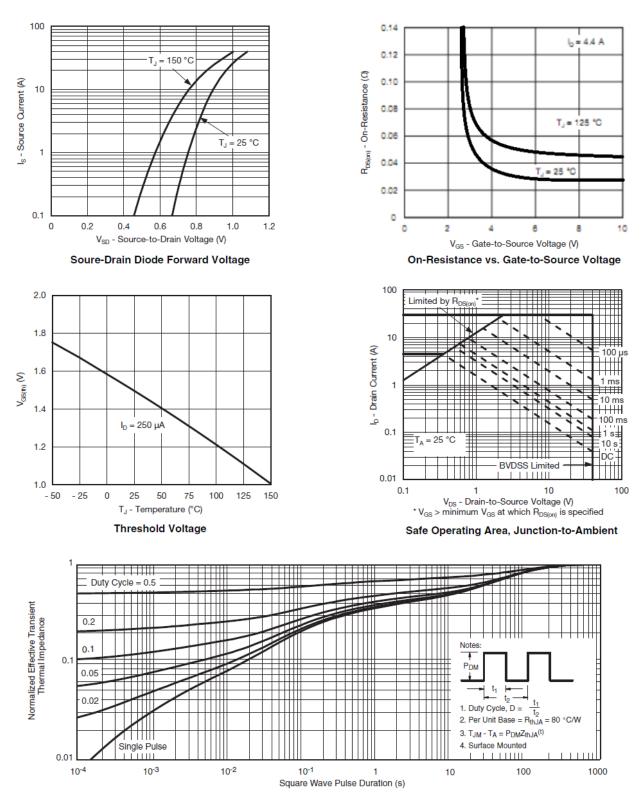




On-Resistance vs. Junction Temperature



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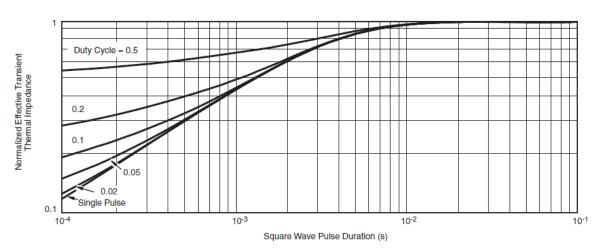


Normalized Thermal Transient Impedance, Junction-to-Ambient





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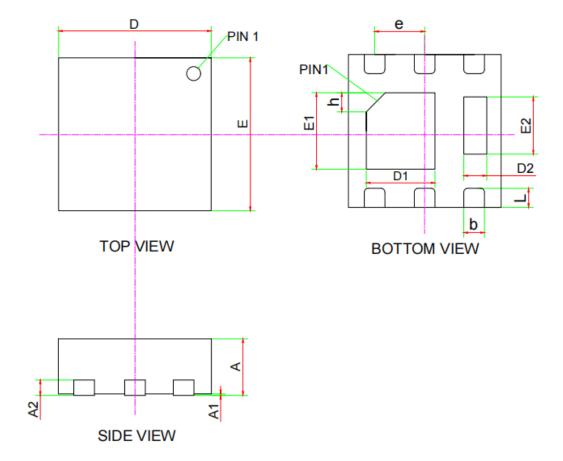


Normalized Thermal Transient Impedance, Junction-to-Case



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Packaging information



SYMBOL	MIN	NOM	MAX	
A	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
A2	0.18	0.20	0.25	
b	0.20	0.27	0.34	
D	1.95	2.00	2.05	
E	1.95	2.00	2.05	
D1	0.80	0.90	1.00	
E1	0.90	1.00	1.10	
D2	0.20	0.30	0.40	
E2	0.65	0.75	0.85	
L	0.20	0.25	0.35	
h	0.20	0.25	0.30	
е	0.65 BSC			



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