

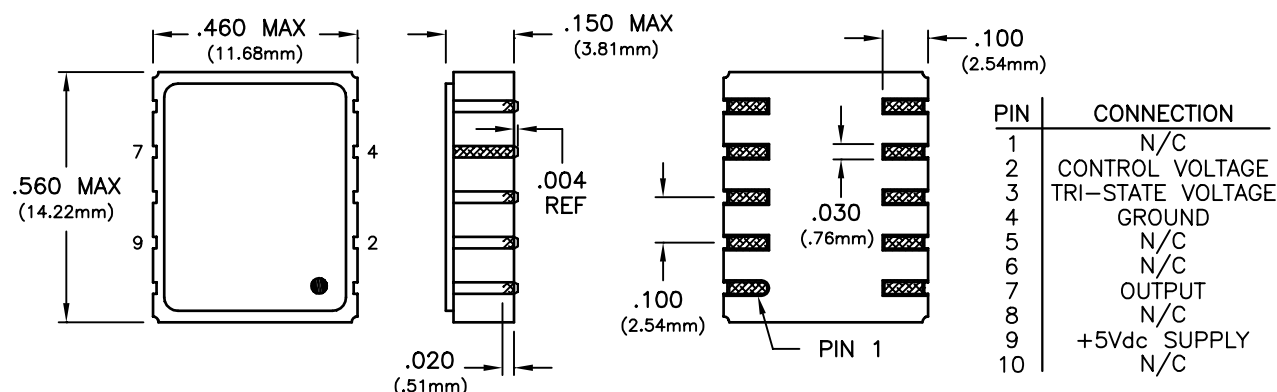


CRYSTAL CONTROLLED OSCILLATORS

AURORA, IL 60505
PHONE (630) 851-4722
FAX (630) 851-5040
www.conwin.com

SURFACE MOUNT TRI-STATE VCXO

SPECIFICATIONS		VST510**		VST520**	VST530**	VST540**
Frequency Range		2MHz to 80MHz				
Frequency Stability		±25ppm		±50ppm	±100ppm	±20ppm
Temperature Range		0°C to +70°C				
Output	Waveform	CMOS/TTL Squarewave				
	Load	50pF or 10TTL Loads				
	Voltage	Voh	4.50V Minimum			
		Vol	0.40V Maximum			
	Current	Ioh	−16mA			
		Iol	16mA			
	Duty Cycle	45/55 Maximum Measured @ 2.5Vdc				
	Rise/Fall Time	6nS Maximum				
	Jitter (RMS)	5pS Maximum				
Phase Noise (typical)		−65dBc/Hz @ 10Hz , −130dBc/Hz @ 10KHz				
Frequency Control						
	Pullability	15	150 (±75ppm Minimum)			
	(** add code to model)	22	200 (±100ppm Minimum)			
Control Voltage Range		0.5Vdc to 4.5Vdc				
Slope		Positive				
Control Voltage for Center Frequency		2.5Vdc ±0.5Vdc				
Monotonic Linearity		< ±10%				
Input Impedance		>50K ohm Minimum				
Modulation Bandwidth		≥15KHz				
Tri-State Input Control	Output E/D Time	100nS Typical				
	Enable (Vih)	2.2V Minimum				
	Disable (Vil)	0.8V Maximum				
		Oscillator output is enabled with no connection on pad 3				
Supply Voltage		+5Vdc ±5%				
Supply Current		60mA Maximum				
Package		Hermetically sealed, leadless ceramic package				



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REV: 05
DATE: 9/22/99

Specifications subject to change without notice. C-W © 1999

ISSUED BY: _____

Dimensional Tolerance: ±.02"
±.005"



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EXAMPLE MODEL NUMBER:

V S T 5 1 0 1 5 / 1 6 . 3 8 4 M H z

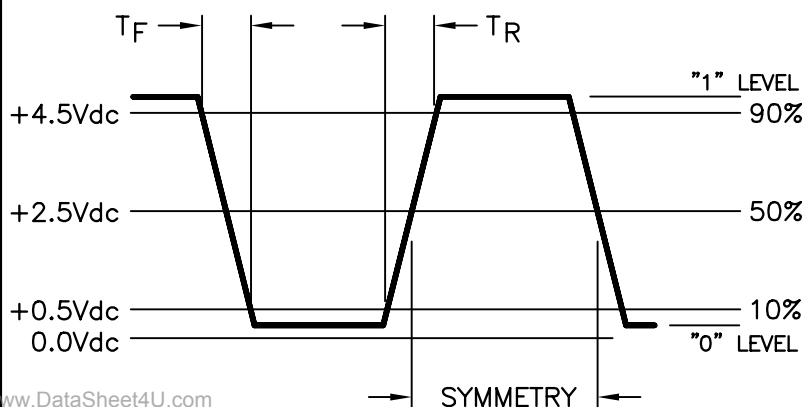
SURFACE MOUNT
VCXO SERIES

CENTER FREQUENCY

CONTROL VOLTAGE
0.5 to 4.5Vdc

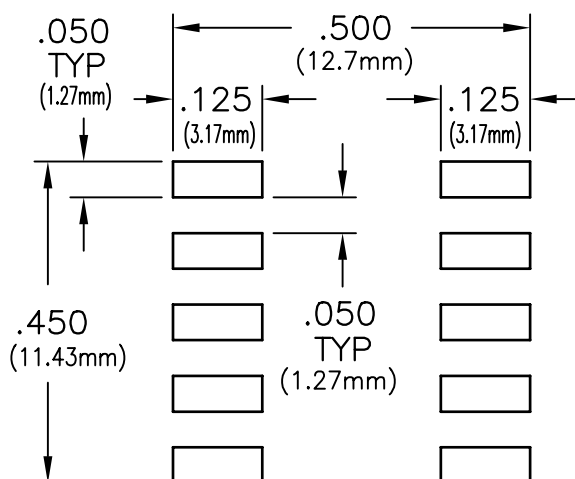
MINIMUM DEVIATION OF
 $\pm 75\text{ppm}$ OVER
CONTROL VOLTAGE RANGE

OUTPUT WAVEFORM

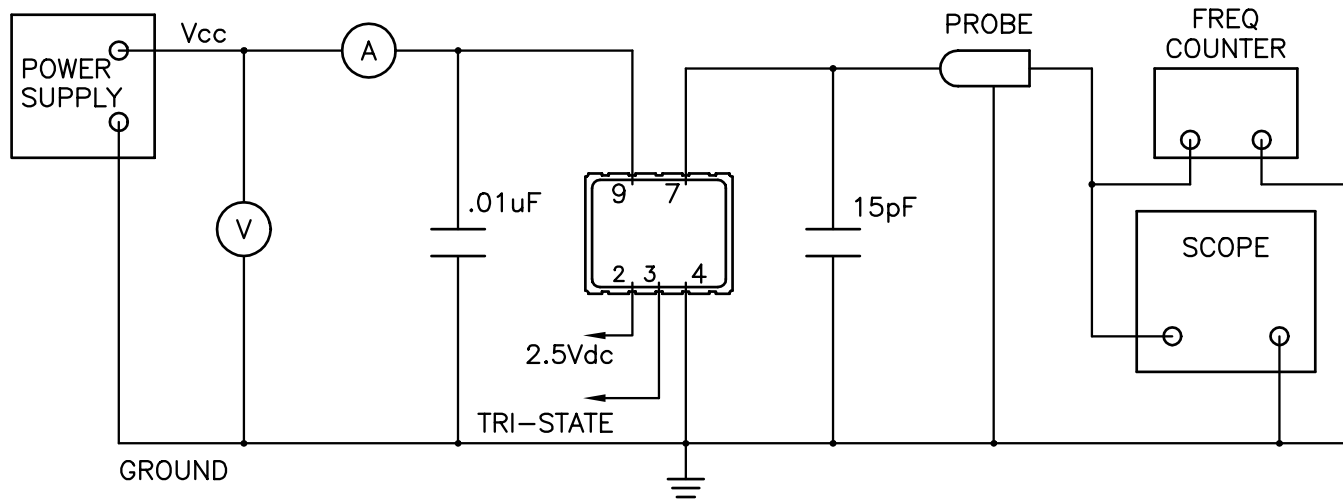


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SUGGESTED PAD LAYOUT



TEST CIRCUIT



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 $\pm .005"$