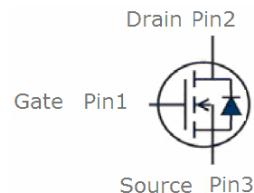


Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Low on-resistance @ $V_{GS}=4.5$ V
- Fast Switching
- Pb-free lead plating; RoHS compliant

V_{DS}	100	V
$R_{DS(on),typ}@VGS=10V$	70	mΩ
$R_{DS(on),typ}@VGS=4.5V$	75	mΩ
I_D	5	A

SOT89



Part ID	Package Type	Marking	Tape and reel information
VSR090N10MS	SOT89	090N10	3000pcs/reel

Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	100	V
V_{GS}	Gate-Source voltage	±20	V
I_D	Continuous drain current@ $V_{GS}=10V$	$T_c=25^\circ C$	A
		$T_A=70^\circ C$	A
I_{DM}	Pulse drain current tested ①	$T_c=25^\circ C$	A
P_D	Maximum power dissipation	$T_c=25^\circ C$	W
I_S	Diode Continuous Forward Current	$T_c=25^\circ C$	A
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage and operating temperature range	-55 to 175	°C

Thermal characteristics

$R_{\theta JA}$	Thermal Resistance Junction-Ambient	100	°C/W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	15	°C/W

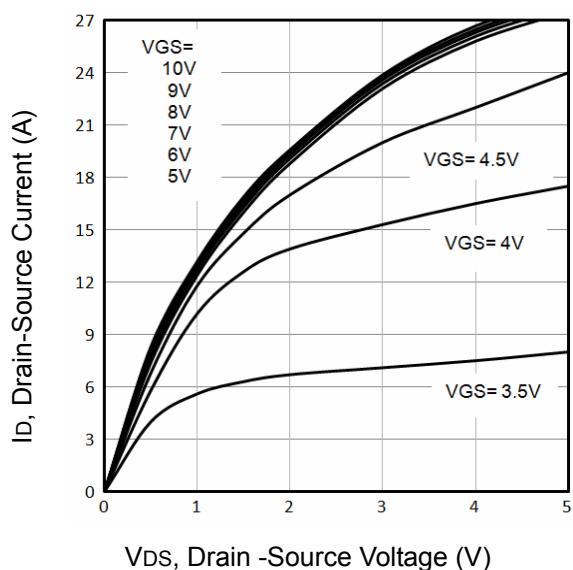
Typical Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.8	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$	--	70	90	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=2\text{A}$	--	75	100	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	435	--	pF
C_{oss}	Output Capacitance		--	45	--	pF
C_{rss}	Reverse Transfer Capacitance		--	30	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=1\text{A}, V_{\text{GS}}=10\text{V}$	--	9	--	nC
Q_{gs}	Gate-Source Charge		--	1.7	--	nC
Q_{gd}	Gate-Drain Charge		--	1.6	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=1\text{A}, R_{\text{G}}=6.8\Omega, V_{\text{GS}}=4.5\text{V}$	--	6	--	nS
t_r	Turn-on Rise Time		--	15	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	16	--	nS
t_f	Turn-Off Fall Time		--	10	--	nS
Source- Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=5\text{A}, V_{\text{GS}}=0\text{V}$	--	0.83	1.20	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{sd}}=5\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	--	43	--	nS
Q_{rr}	Reverse Recovery Charge			185		nC

NOTE:

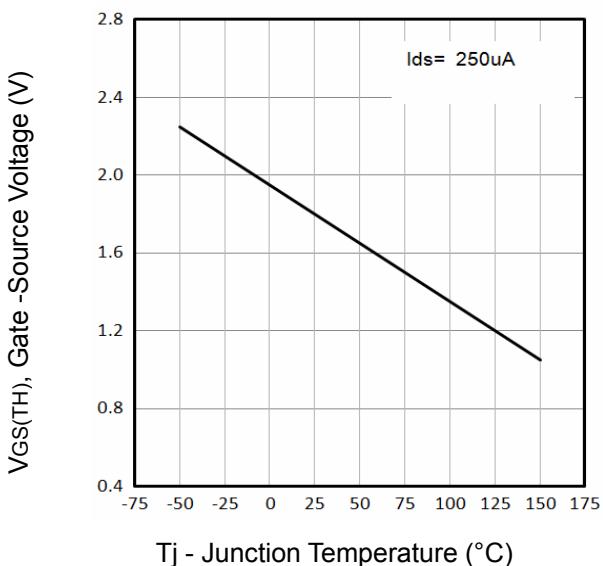
- ① Repetitive rating; pulse width limited by max. junction temperature
- ② Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics



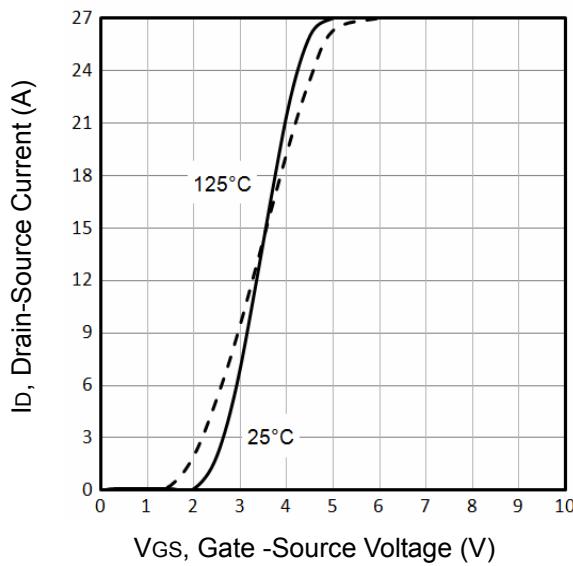
V_{DS}, Drain -Source Voltage (V)

Fig1. Typical Output Characteristics



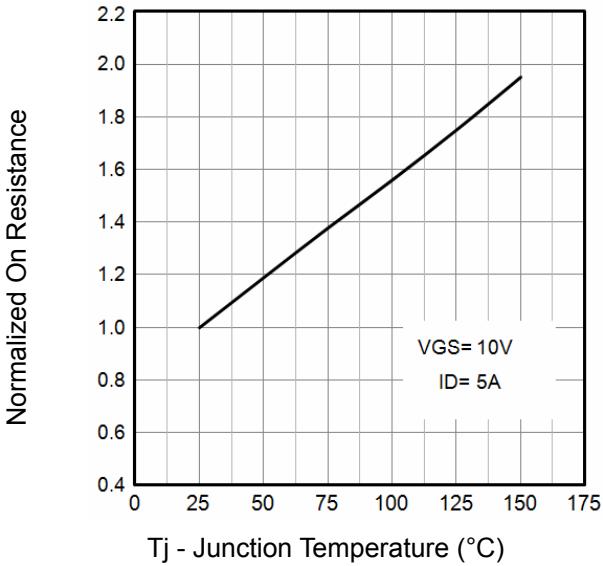
T_j - Junction Temperature (°C)

Fig2. Threshold Voltage Vs. Temperature



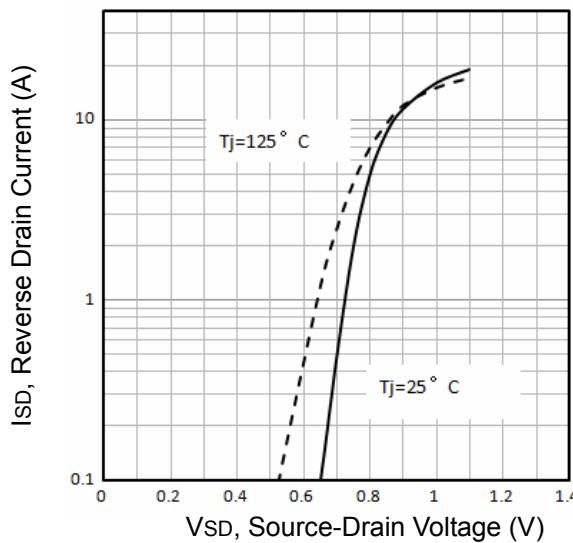
V_{GS}, Gate -Source Voltage (V)

Fig3. Typical Transfer Characteristics



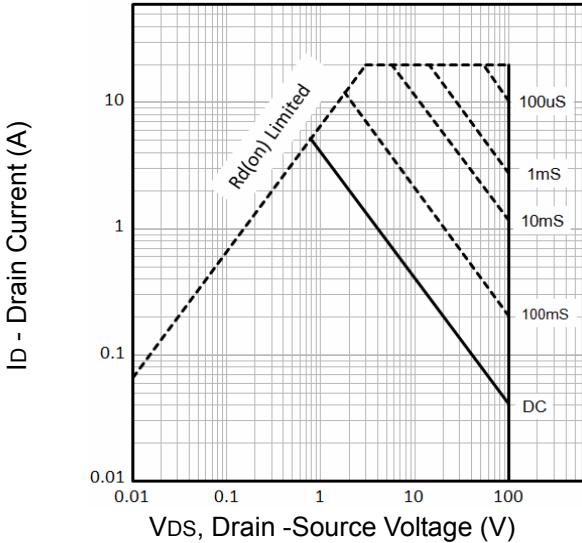
T_j - Junction Temperature (°C)

Fig4. Normalized On-Resistance Vs. Temperature



V_{SD}, Source-Drain Voltage (V)

Fig5. Typical Source-Drain Diode Forward Voltage



V_{DS}, Drain -Source Voltage (V)

Fig6. Maximum Safe Operating Area

Typical Characteristics

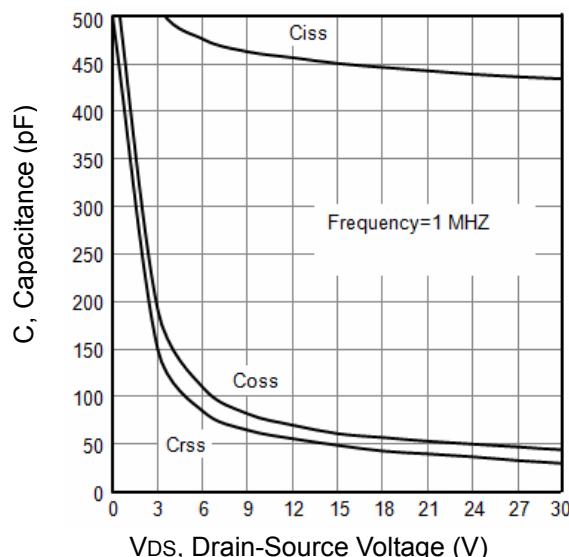


Fig7. Typical Capacitance Vs. Drain-Source

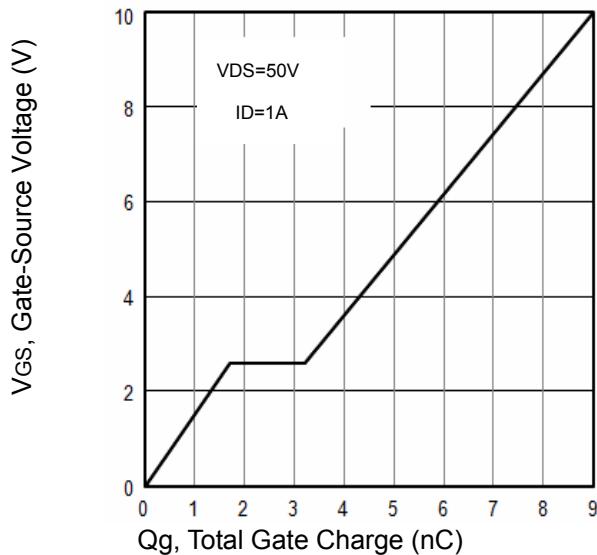


Fig8. Typical Gate Charge Vs. Gate-Source

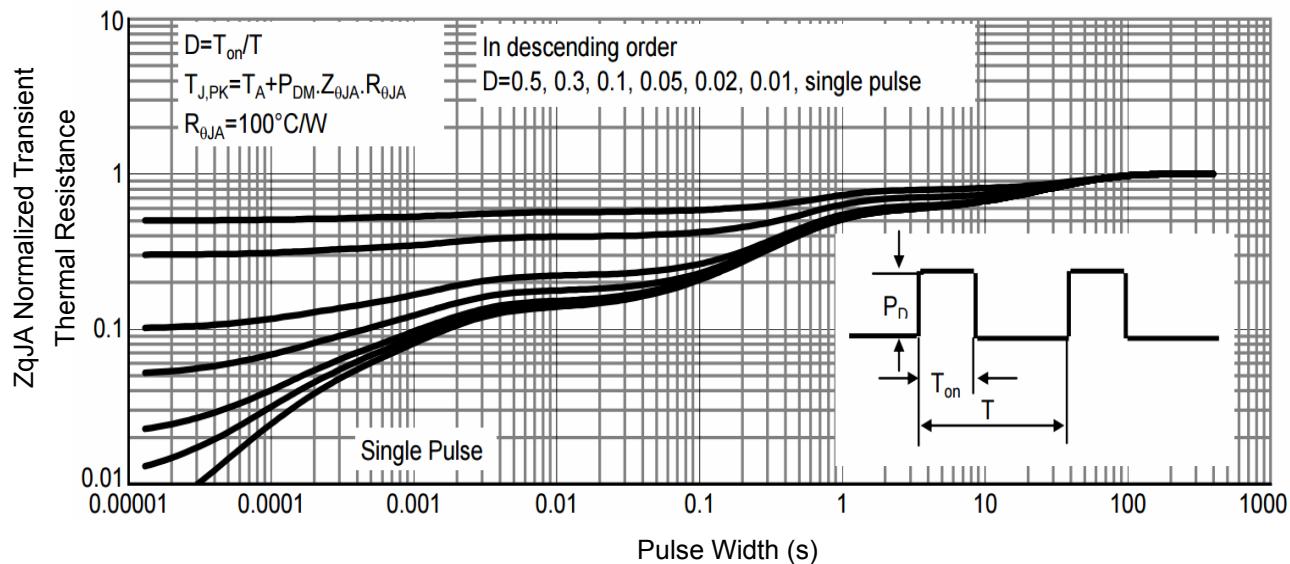


Fig9. Normalized Maximum Transient Thermal

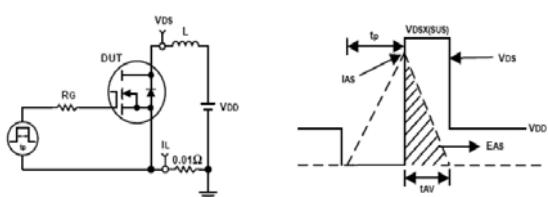


Fig10. Unclamped Inductive Test Circuit and waveforms

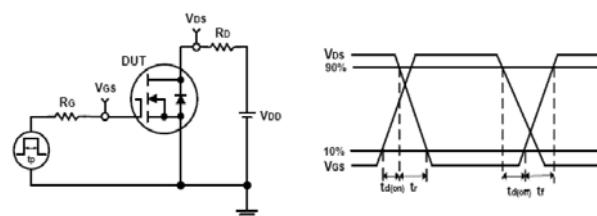
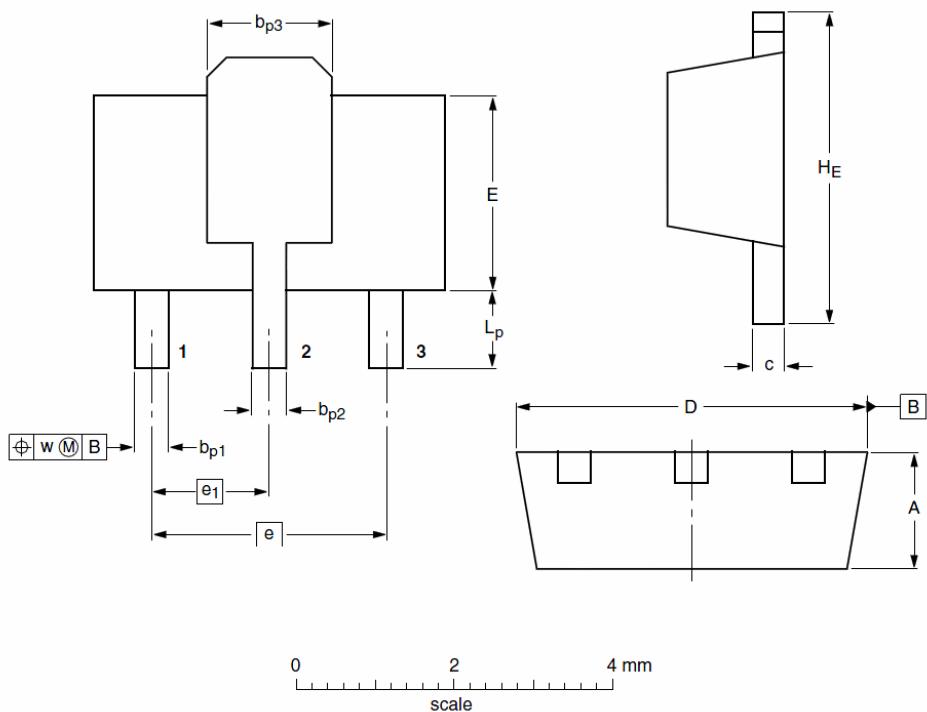


Fig11. Switching Time Test Circuit and waveforms

SOT89 Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	1.40	1.50	1.60	b_{p1}	0.35	0.43	0.48
b_{p2}	0.40	0.47	0.53	b_{p3}	1.40	1.68	1.80
c	0.23	0.35	0.44	D	4.40	4.48	4.60
E	2.40	2.51	2.60	e	--	3.00	--
e_1	--	1.50	--	H_e	3.75	4.08	4.25
L_p	0.80	0.90	1.20	w	--	0.13	--

Customer Service

Sales and Service:

sales@vgsemi.com

Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: www.vgsemi.com