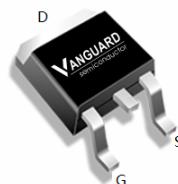


Features

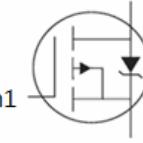
- P-Channel, -5V Logic Level Control
- Very low on-resistance RDS(on) @ $V_{GS}=-4.5$ V
- Fast Switching
- Enhancement mode
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant

V_{DS}	-30	V
$R_{DS(on),TYP}$ @ $V_{GS}=-10$ V	4.2	mΩ
$R_{DS(on),TYP}$ @ $V_{GS}=-4.5$ V	6.0	mΩ
I_D	-80	A

TO-252



Drain Pin2



Part ID	Package Type	Marking	Tape and reel information
VSD004P03MS	TO-252	004P03M	2500pcs/reel

Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	±20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	-30	V	
T_J	Maximum Junction Temperature	175	°C	
T_{STG}	Storage Temperature Range①	-55 to 175	°C	
I_S	Diode Continuous Forward Current	$T_c=25$ °C	-80	A
Mounted on Large Heat Sink				
I_D	Continuous Drain current @ $V_{GS}=-10$ V	$T_c=25$ °C	-80	A
		$T_c=100$ °C	-51	A
I_{DM}	Pulse Drain Current Tested ②	$T_c=25$ °C	-320	A
P_D	Maximum Power Dissipation	$T_c=25$ °C	83	W
R_{JJC}	Thermal Resistance-Junction to Case	1.8	°C/W	
R_{JJA}	Thermal Resistance Junction-Ambient	50	°C/W	
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed ③	93.7	mJ	

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.6	-2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ②	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-30\text{A}$	--	4.2	6.0	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ②	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-10\text{A}$	--	6.0	8.0	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	5960	--	pF
C_{oss}	Output Capacitance		--	585	--	pF
C_{rss}	Reverse Transfer Capacitance		--	415	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-10\text{A}, V_{\text{GS}}=-10\text{V}$	--	85	--	nC
Q_{gs}	Gate-Source Charge		--	18	--	nC
Q_{gd}	Gate-Drain Charge		--	28	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-5\text{A}, R_{\text{G}}=6.8\Omega, V_{\text{GS}}=-10\text{V}$	--	18	--	nS
t_r	Turn-on Rise Time		--	22	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	86	--	nS
t_f	Turn-Off Fall Time		--	45	--	nS
Source- Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{sd}}=-30\text{A}, V_{\text{GS}}=0\text{V}$	--	-0.86	-1.3	V
t_{rr}	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_{\text{sd}}=-10\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=-100\text{A}/\mu\text{s}$	--	42	--	nS
Q_{rr}	Reverse Recovery Charge			33		nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

③ Limited by $T_{J\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.3\text{mH}$, $R_G = 25\Omega$, $I_{AS} = -25\text{A}$, $V_{GS} = -10\text{V}$. Part not recommended for use above this value

Typical Characteristics

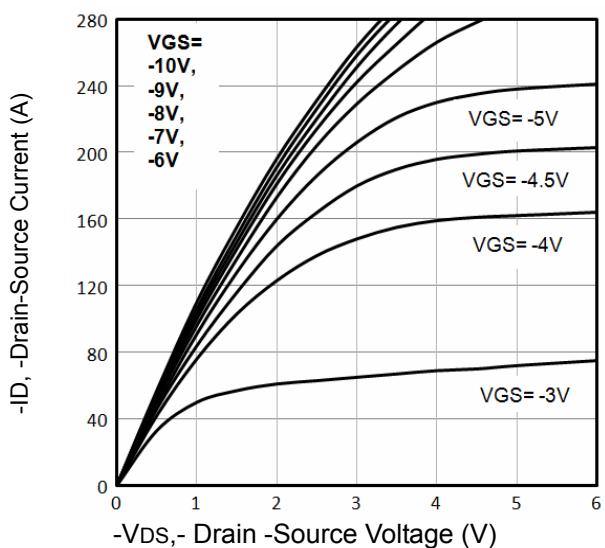


Fig1. Typical Output Characteristics

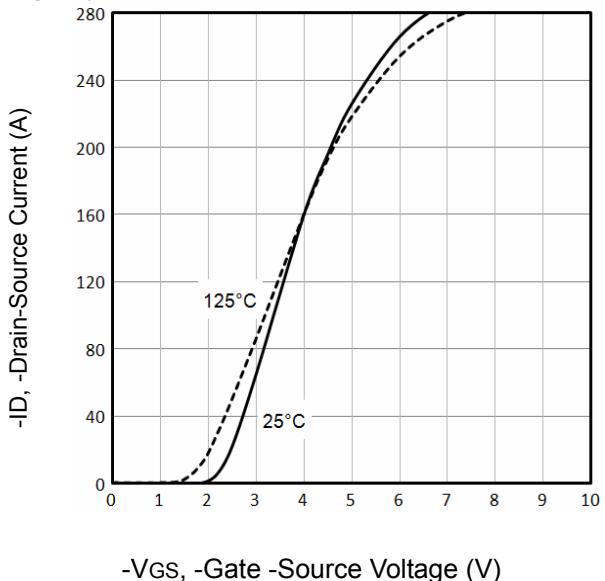


Fig3. Typical Transfer Characteristics

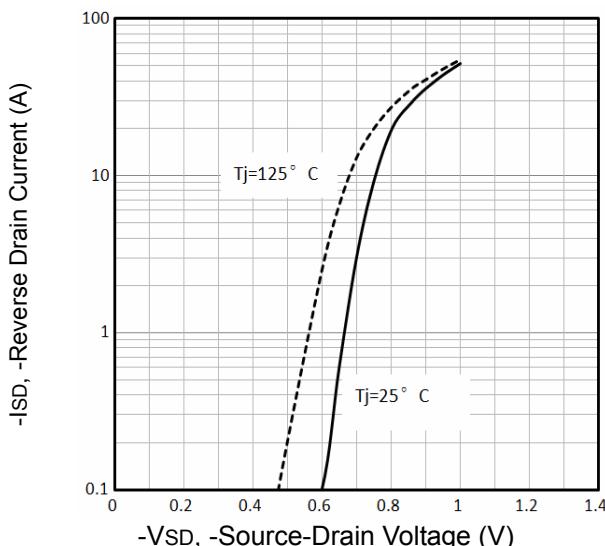


Fig5. Typical Source-Drain Diode Forward Voltage

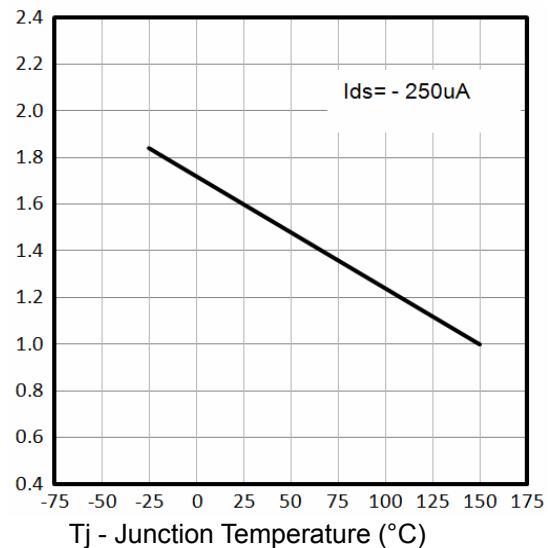


Fig2. Threshold Voltage Vs. Temperature

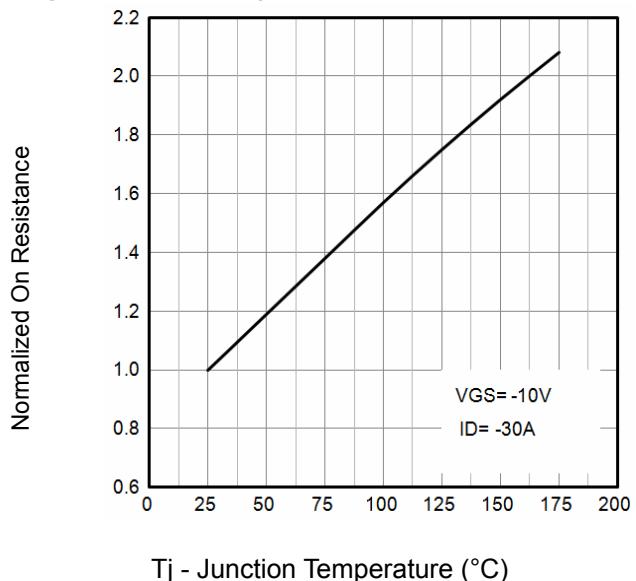


Fig4. Normalized On-Resistance Vs. Temperature

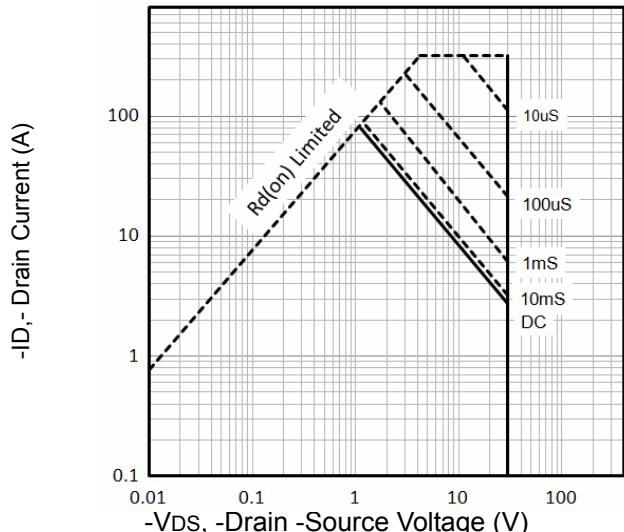


Fig6. Maximum Safe Operating Area

Typical Characteristics

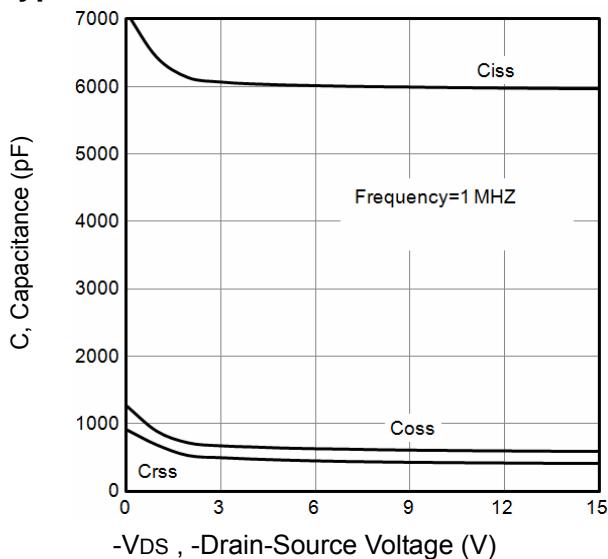


Fig7. Typical Capacitance Vs.Drain-Source Voltage

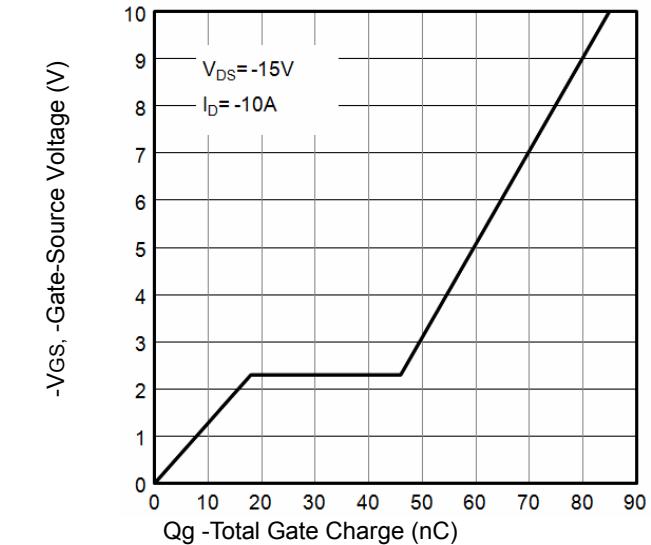


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

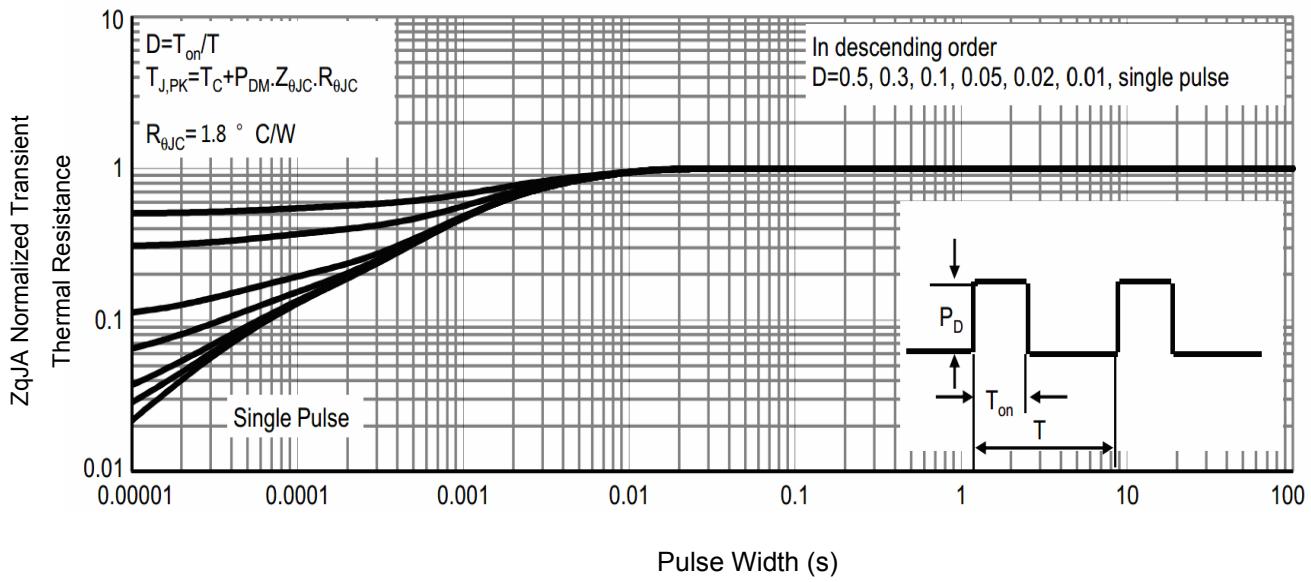


Fig9. Normalized Maximum Transient Thermal Impedance T_j -Junction

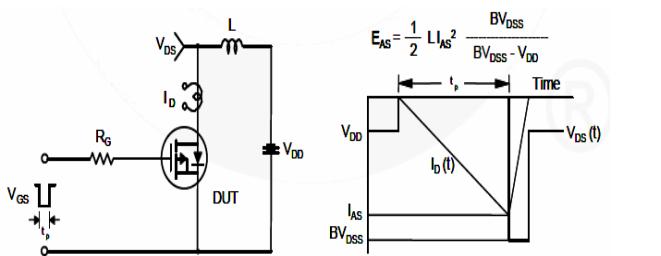


Fig10. Unclamped Inductive Test Circuit and Waveforms

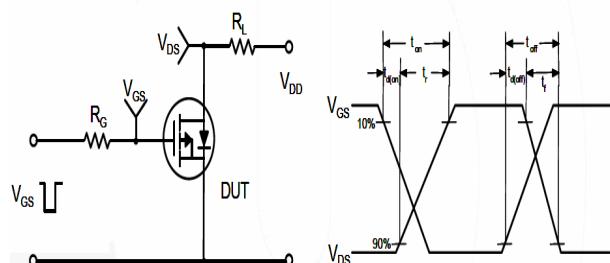
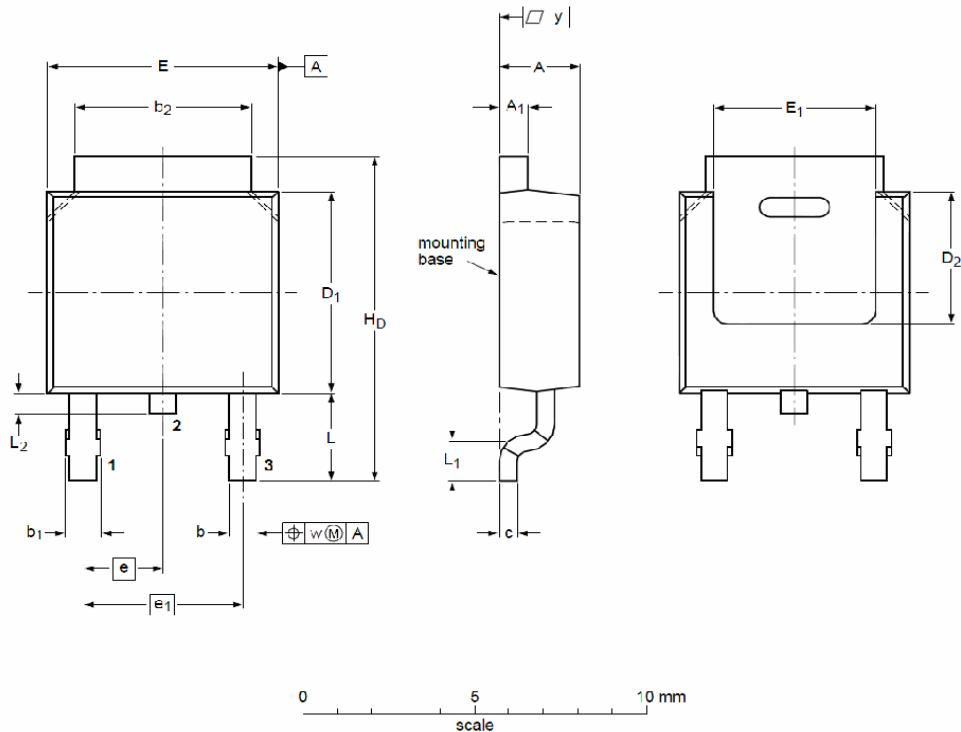


Fig11. Switching Time Test Circuit and waveforms

TO-252 Package Outline



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.22	2.30	2.38	A ₁	0.46	0.58	0.93
b	0.71	0.79	0.89	b ₁	0.90	0.98	1.10
b ₂	5.00	5.30	5.46	c	0.20	0.40	0.56
D ₁	5.98	6.05	6.22	D ₂	--	4.00	--
E	6.47	6.60	6.73	E ₁	5.10	5.28	5.45
e	--	2.28	--	e ₁	--	4.57	--
H _D	9.60	10.08	10.40	L	2.75	2.95	3.05
L ₁	--	0.50	--	L ₂	0.80	0.90	1.10
w	--	0.20	--	y	0.20	--	--

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