

### Vishay Semiconductors

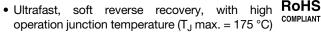
## **Insulated Ultrafast Rectifier Module, 80 A**



PRIMARY CHARACTERISTICS					
$V_{R}$	600 V				
I <sub>F(AV)</sub> per module at T <sub>C</sub> = 115 °C	80 A				
t <sub>rr</sub>	41 ns				
Type	Modules - diode FRED Pt®				
Package	SOT-227				

#### **FEATURES**

- Two fully independent diodes
- Fully insulated package





- Low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- · Industry standard outline
- UL approved file E78996



- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

The VS-UFL80FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V <sub>R</sub>		600	V			
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 85 °C	65	^			
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	300	A			
Maximum power dissipation per module	P <sub>D</sub>	T <sub>C</sub> = 85 °C	176	W			
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	V			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			

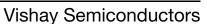


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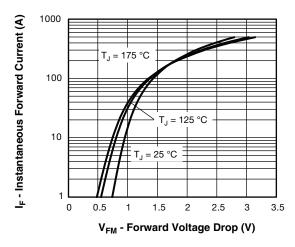
<b>ELECTRICAL SPECIFICATIONS PER DIODE</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	I <sub>R</sub> = 100 μA		600	-	=	
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 30 A		1	1.1	1.43	V
		I <sub>F</sub> = 60 A		ı	1.27	1.49	
Torward voltage		I <sub>F</sub> = 30 A	T <sub>.1</sub> = 125 °C	-	1.0	1.23	
		I <sub>F</sub> = 60 A	0 A	-	- 1.17 1.35		
Reverse leakage current I <sub>RM</sub>		$V_R = V_R$ rated		-	0.1	50	μA
neverse leakage current	I <sub>RM</sub>	$T_J = 175 ^{\circ}\text{C},  V_R = V_R  \text{rated}$		1	0.2	1.0	mA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V		-	30	=	pF

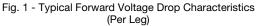
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 1 A; dI <sub>F</sub> /dt = 200 A/μs; V <sub>R</sub> = 30 V	-	41	ı	ns
		T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	115	-	A nC
		T <sub>J</sub> = 125 °C		-	200	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	11	=	
		T <sub>J</sub> = 125 °C		-	20	=	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	600	=	
		T <sub>J</sub> = 125 °C		-	1900	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	D		-	-	1.02	
Junction to case, both leg conducting	$R_{thJC}$		-	-	0.51	°C/W
Case to heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Wounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				;	SOT-227	









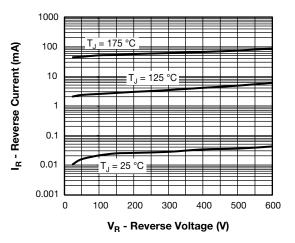


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

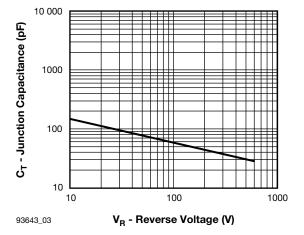


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

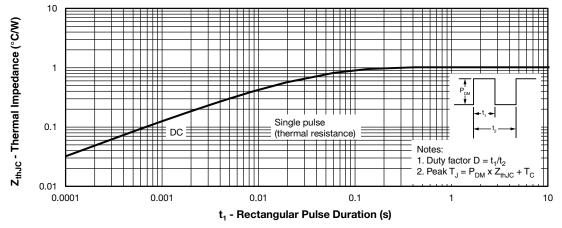


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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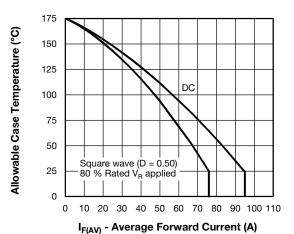
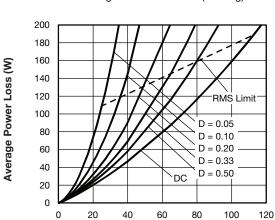


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)



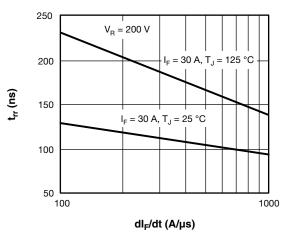


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

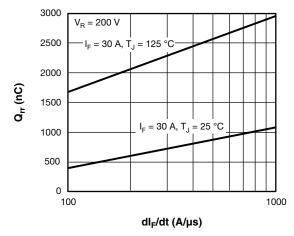


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

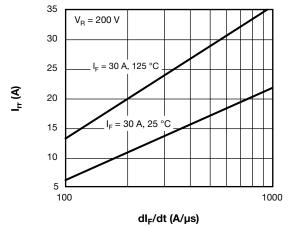


Fig. 9 - Typical Stored Current vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = 80 \ \% \ \text{rated} \ V_R \\ \end{array}$ 

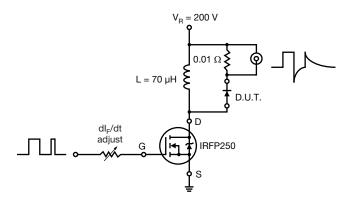
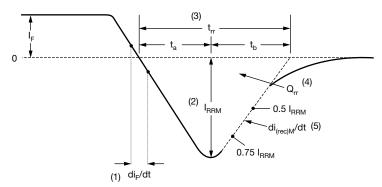


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$  and  $I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

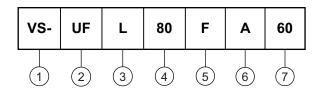
Fig. 11 - Reverse Recovery Waveform and Definitions



### Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Ultrafast rectifier

Ultrafast Pt diffused, Low V<sub>F</sub>

4 - Current rating (80 = 80 A)

5 - Circuit configuration (two separate diodes, parallel pin-out)

6 - Package indicator (SOT-227 standard insulated base)

7 - Voltage rating (60 = 600 V)

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two separate diodes, parallel pin-out	F	Lead Assignment  4				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				



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Vishay

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