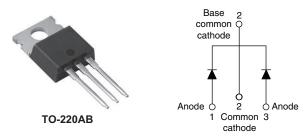


VS-STPS40L15CTPbF, VS-STPS40L15CT-N3

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Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY						
Package	TO-220AB					
I _{F(AV)}	2 x 20 A					
V _R	15 V					
V _F at I _F	See Electrical table					
I _{RM} max.	600 mA at 100 °C					
T _J max.	125 °C					
Diode variation	Common cathode					
E _{AS}	10 mJ					

FEATURES

- 125 °C T_{.1} operation ($V_B < 5 V$)
- Optimized for OR-ing applications
- · Ultra low forward voltage drop
- · High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability



RoHS

COMPLIANT

FREE

- High purity, high temperature epoxy HALOGEN for encapsulation enhanced mechanical strength and moisture resistance
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL CHARACTERISTICS VALUES UN						
I _{F(AV)}	Rectangular waveform	40	А			
V _{RRM}		15	V			
I _{FSM}	$t_p = 5 \ \mu s \ sine$	700	А			
V _F	19 A_{pk} , T_J = 125 °C (per leg, typical)	0.25	V			
TJ		- 55 to 125	°C			

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-STPS40L15CTPbF	VS-STPS40L15CT-N3	UNITS			
Maximum DC reverse voltage	V _R	15	15	V			
Maximum working peak reverse voltage	V _{RWM}	15	15	v			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDI	TIONS	VALUES	UNITS	
Maximum average	per leg		50 % duty cycle at T _C = 85 °C, rectangular waveform		20		
forward current See fig. 5	per device	I _{F(AV)}			40		
Maximum peak one cycle non-re surge current per leg	epetitive	1	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated		700	А	
See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	330		
Repetitive avalanche current per	rleg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2		
Non-repetitive avalanche energy	v per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6 mH		10	mJ	

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS		
		19 A	T _{.1} = 25 °C	-	0.41		
Forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=23 0	-	0.52	v	
See fig. 1	V FM (*)	19 A	T _J = 125 °C	0.25	0.33		
		40 A	1j=125 0	0.37	0.50		
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	-	10	mA	
See fig. 2	'RM \''	T _J = 100 °C	VR - Haleu VR	-	600	mA	
Threshold voltage	V _{F(TO)}			0.1	182	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		1j = 1j maximum 7.6		.6	mΩ
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8	-	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	e T _J		- 55 to 125	°C		
Maximum storage temperature range	T _{Stg}		- 55 to 150	U		
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	1.5			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (only for TO-220)	0.50	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (for D ² PAK and TO-262)	40			
Approvimete weight			2	g		
Approximate weight			0.07	oz.		
Mounting torque	n	Non-lubricated threads	6 (5)	kgf ⋅ cm		
Mounting torque maximu	n	non-iublicateu tiffeaus	12 (10)	(lbf ⋅ in)		
Marking device		Case style TO-220AB	STPS40	STPS40L15CT		



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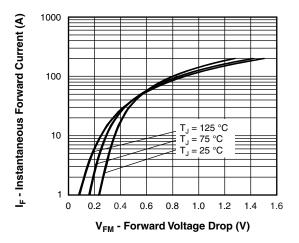


Fig. 1 - Maximum Forward Voltage Drop Characteristics

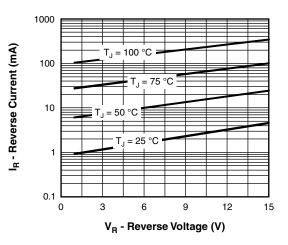


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

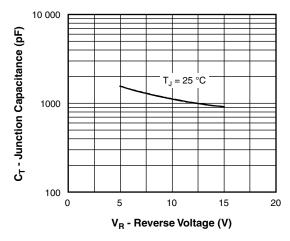
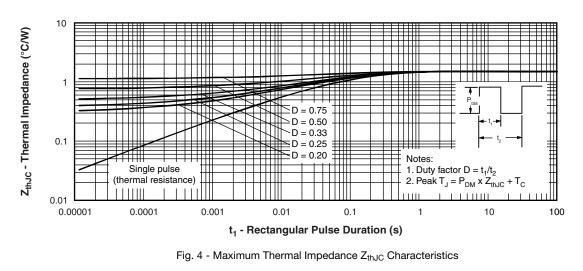


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

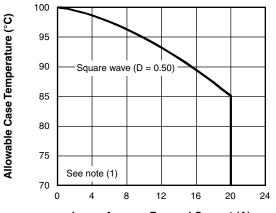


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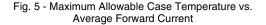


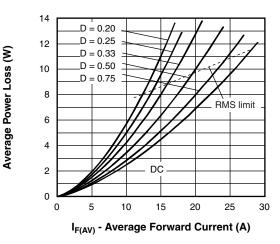
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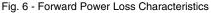
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I_{F(AV)} - Average Forward Current (A)







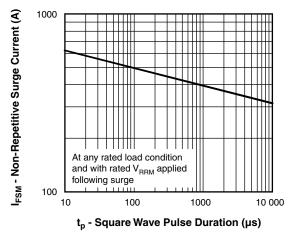


Fig. 7 - Maximum Non-Repetitive Surge Current

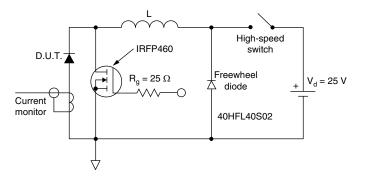


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

Device code	VS-	STPS	40	L	15	СТ	PbF							
	1	2	3	4	5	6	7							
	 Vishay Semiconductors product Schottky STPS series Current rating (40 = 40 A) L = Low voltage drop 													
	 5 - Voltage rating (15 = 15 V) 6 - CT = Essential part number 													
	7			0		d RoHS								

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-STPS40L15CTPbF	50	1000	Antistatic plastic tube			
VS-STPS40L15CT-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Dant manification information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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