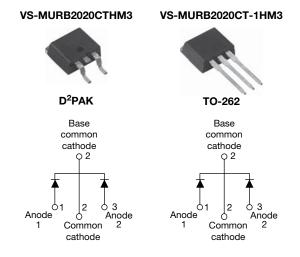
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VS-MURB2020CTHM3, VS-MURB2020CT-1HM3

Vishay Semiconductors

Ultrafast Rectifier, 2 x 10 A FRED Pt[®]



PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK), TO-262AA
I _{F(AV)}	2 x 10 A
V _R	200 V
V _F at I _F	0.85
t _{rr} typ.	19 ns
T _J max.	175 °C
Diode variation	Common cathode

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C HALOGEN
- AEC-Q101 qualified
- Meets JESD 201 class 1 whisker test
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance. ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Peak repetitive reverse voltage		V _{RRM}		200	V			
Average restified forward surrant	per leg	I _{F(AV)}		10				
Average rectified forward current	total device		Rated V _R , T _C = 145 °C	20	٨			
Non-repetitive peak surge current per leg		I _{FSM}		100	A			
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, $T_C = 145 \text{ °C}$	20				
Operating junction and storage tem	peratures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-			
Forward voltage		I _F = 8 A, T _J = 125 °C	-	-	0.85	v		
	V _F	I _F = 16 A	-	-	1.15			
		I _F = 16 A, T _J = 125 °C	-	-	1.05			
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	15			
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA		
Junction capacitance	CT	V _R = 200 V	-	55	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

Revision: 16-Jun-15 Document Number: 94807 1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000

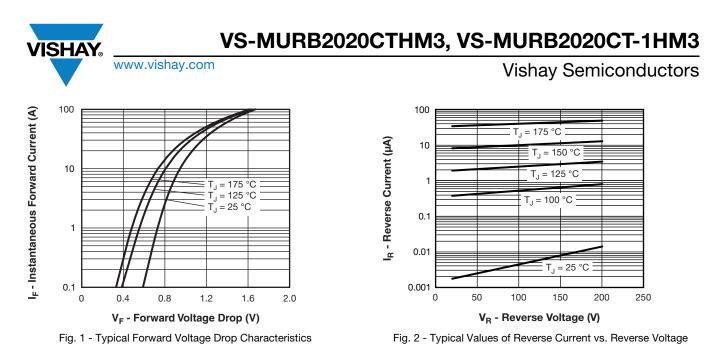


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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A}/\mu \text{s}, V_R = 30 \text{ V}$			-			
	t _{rr}	T _J = 25 °C		-	21	-	ns		
		T _J = 125 °C	I _F = 10 A dI _F /dt = 200 A/μs V _R = 160 V	-	35	-			
Peak recovery current	I _{RRM}	T _J = 25 °C		-	1.9	-	A		
		T _J = 125 °C		-	4.8	-			
Poverse receven charge	Q _{rr}	T _J = 25 °C		-	25	-	nC		
Reverse recovery charge		T _J = 125 °C		-	78	-			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C		
Thermal resistance, junction to case per leg	R _{thJC}		-	-	2.5			
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-			
Weight			-	2.0	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking dayling		Case style D ² PAK	MURB2020CTH					
Marking device		Case style TO-262		MURB2020CT-1H				



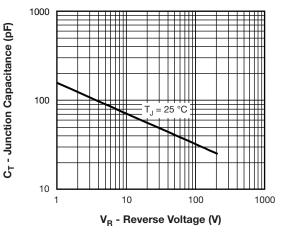


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

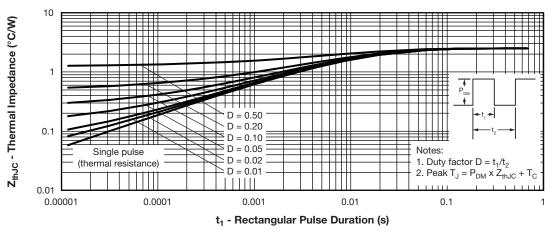
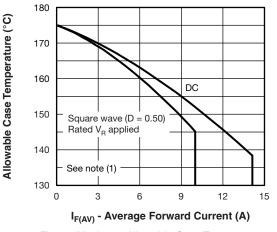
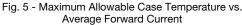


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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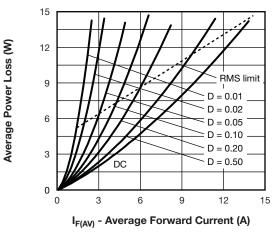
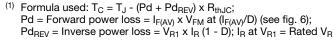
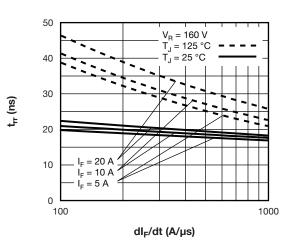


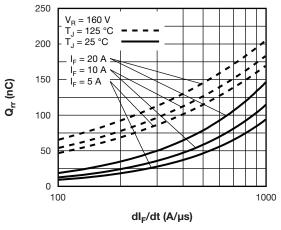
Fig. 6 - Forward Power Loss Characteristics

Note













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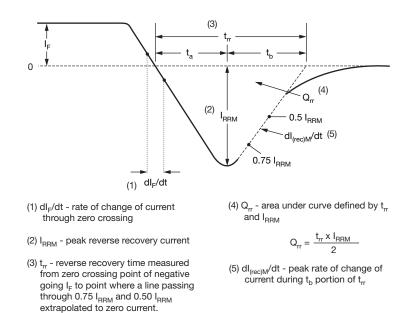


Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	MUR	В	20	20	СТ	-1	L	н	М3		
		2	3	4	5	6	7	8	9	10		
	1	1 - Vishay Semiconductors product										
	2	- Ultra	afast MI	JR serie	es							
	3	- B =	D ² PAK	/TO-262								
	4	- Cur	rent rati	ng (20 =	: 20 A)							
	5	- Volt	age rati	ng (20 =	= 200 V)							
	6	- CT	= Cente	r tap (du	ual) TO-	220/D ² I	PAK/TC	-262				
	7	- •-1	= TO-2	62								
		• No	one = D	² PAK								
	8	- • No	one									
	 L = Tape and reel (left oriented, for D²PAK package) 											
	• R = Tape and reel (right oriented, for D ² PAK package)											
	9	- H=	AEC-Q	101 qua	lified							
	10	- M3	= Halog	en-free,	RoHS-	complia	nt, and	termina	tions le	ad (Pb)-f		

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ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MURB2020CTHM3	50	1000	Antistatic plastic tube				
VS-MURB2020CT-1HM3	50	1000	Antistatic plastic tube				
VS-MURB2020CTLHM3	800	800	13" diameter reel				
VS-MURB2020CTRHM3	800	800	13" diameter reel				

	LINKS TO RELATED DOCUMENTS						
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046					
	TO-262AA	www.vishay.com/doc?95419					
	TO-263AB (D ² PAK)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032					
SPICE model		www.vishay.com/doc?95622					

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES		MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

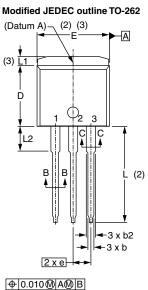


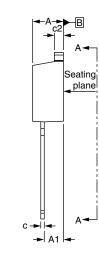
Outline Dimensions

Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

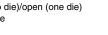


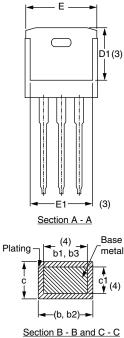


Lead assignments



Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

SYMBOL	MILLIM	ETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

Revision: 04-Oct-10

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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