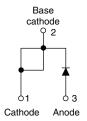


VS-MBR7...PbF Series, VS-MBR7...-N3 Series

Vishay Semiconductors

Schottky Rectifier, 7.5 A

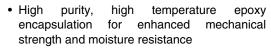




PRODUCT SUMMARY					
Package	TO-220AC				
I _{F(AV)}	7.5 A				
V_{R}	35 V, 45 V				
V _F at I _F	0.57 V				
I _{RM} max.	15 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Single die				
E _{AS}	7 mJ				

FEATURES

- 150 °C T_J operation
- · High frequency operation
- · Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-MBR7... Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	7.5	A			
V _{RRM}		35/45	V			
I _{FSM}	t _p = 5 μs sine	690	Α			
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V			
TJ	Range	- 65 to 150	°C			

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-MBR735PbF	VS-MBR735-N3	VS-MBR745PbF	VS-MBR745-N3	UNITS	
Maximum DC reverse voltage	V_{R}						
Maximum working peak reverse voltage	V _{RWM}	35	35	45	45	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	T _C = 131 °C, rated V _R	T _C = 131 °C, rated V _R		Α	
Non-repetitive peak surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	690	А	
		Surge applied at rated load condition half wave single phase 60 Hz		150		
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 3.5 \text{mH}$		7	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		2	Α	



VS-MBR7...PbF Series, VS-MBR7...-N3 Series

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.84		
Maximum forward voltage drop		7.5 A	T 105 %C	0.57	V	
		15 A	T _J = 125 °C	0.72		
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Dated DC valtage	0.1	mA	
Maximum instantaneous reverse current		T _J = 125 °C	Rated DC voltage	15		
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		400	pF	
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		1000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS		
Maximum junction temperature range	TJ		- 65 to 150	°C		
Maximum storage temperature range	T _{Stg}		- 65 to 175			
Maximum thermal resistance, junction to case	R _{thJC}	DC operation		00AM		
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W		
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
Mounting torque minimum			6 (5)	kgf ⋅ cm		
Mounting torque maximum			12 (10)	(lbf · in)		
Marking device		Casa style TO 220AC	MBR735			
Marking device		Case style TO-220AC	MBR745			

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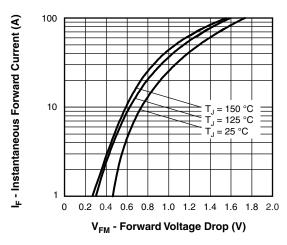


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

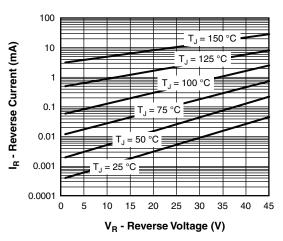


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

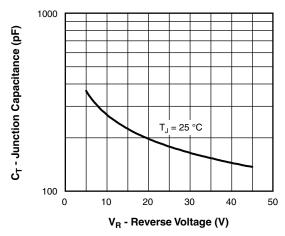


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

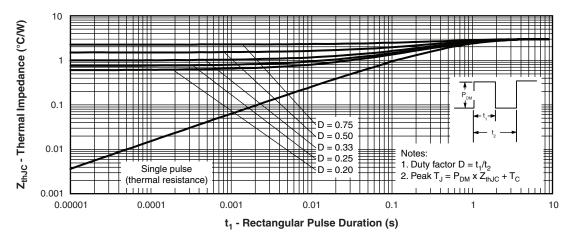


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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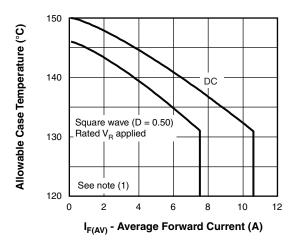


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

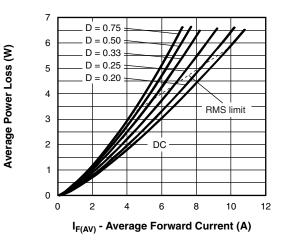


Fig. 6 - Forward Power Loss Characteristics

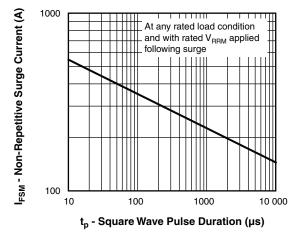


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

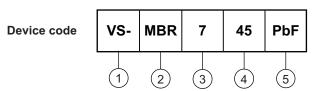
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

VS-MBR7...PbF Series, VS-MBR7...-N3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE



1 - Vishay Semiconductors product

2 - Schottky MBR series

3 - Current rating (7.5 A) 35 = 35 V 4 - Voltage ratings 45 = 45 V

5 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR735PbF	50	1000	Antistatic plastic tube				
VS-MBR735-N3	50	1000	Antistatic plastic tube				
VS-MBR745PbF	50	1000	Antistatic plastic tube				
VS-MBR745-N3	50	1000	Antistatic plastic tube				

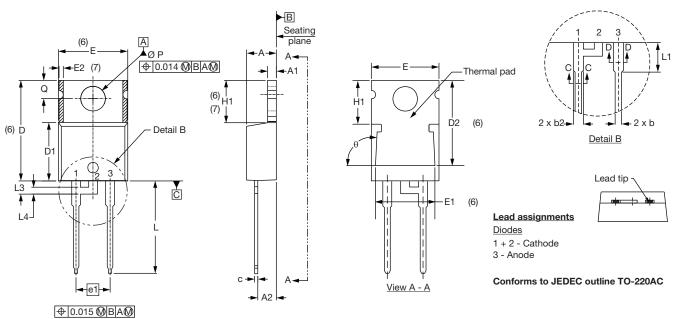
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95221</u>					
Dest enading information	TO-220AC PbF	www.vishay.com/doc?95224			
Part marking information	TO-220AC -N3	www.vishay.com/doc?95068			
SPICE model		www.vishay.com/doc?95298			



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	ERS INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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