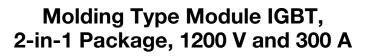
Vishay Semiconductors





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PRODUCT SUMMARY				
V _{CES}	1200 V			
$I_{\rm C}$ at $T_{\rm C}$ = 80 °C	300 A			
V _{CE(on)} (typical) at I _C = 300 A, 25 °C	3.10 V			
Package	Double INT-A-PAK			
Circuit	Half bridge			

FEATURES

- 10 µs short circuit capability
- $V_{CE(on)}$ with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low switching losses
- Rugged with ultrafast performance
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Switching mode power supplies
- Inductive heating
- Electronic welder

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as electronic welder and inductive heating.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V_{GES}		± 20	v	
Collector current			530		
Collector current	IC	$I_{\rm C}$ T _C = 80 °C			
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	600	А	
Diode continuous forward current	I _F	T _C = 80 °C	300		
Diode maximum forward current I _{FM}		t _p = 1 ms	600		
Maximum power dissipation	PD	T _J = 150 °C	2119	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: Pulse width limited by maximum junction temperature.

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COMPLIANT



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IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$T_J = 25 \ ^{\circ}C$	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I _C = 300 A, T _J = 25 °C	-	3.10	3.60	v
		V_{GE} = 15 V, I _C = 300 A, T _J = 125 °C	-	3.45	-	v
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = $V_{GE},~I_{C}$ = 3.0 mA, T_{J} = 25 $^{\circ}C$	4.4	5.2	6.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_J = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 V, T_J = 25 \ ^{\circ}C$	-	-	400	nA

SWITCHING CHARACTERISTICS	S					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	662	-	
Rise time	t _r		-	142	-	ns
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 300 \text{ A}, \text{ R}_{g} = 3.3 \Omega,$	-	633	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	117	-	
Turn-on switching loss	E _{on}	7	-	19.7	-	mJ
Turn-off switching loss	E _{off}	7	-	22.4	-	
Turn-on delay time	t _{d(on)}		-	660	-	
Rise time	tr	7	-	143	-	- ns
Turn-off delay time	t _{d(off)}		-	665	-	
Fall time	t _f		-	137	-	
Turn-on switching loss	E _{on}	7	-	24.9	-	1
Turn-off switching loss	E _{off}	7	-	28.4	-	mJ
Input capacitance	C _{ies}		-	25.3	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 30 \text{ V}, f = 1.0 \text{ MHz}$	-	2.25	-	nF
Reverse transfer capacitance	C _{res}	7	-	0.91	-	
SC data	I _{SC}	$\label{eq:tsc} \begin{array}{l} t_{sc} \leq 10 \; \mu s, \; V_{GE} = 15 \; V, \; T_{J} = 125 \; ^{\circ}C, \\ V_{CC} = 600 \; V, \; V_{CEM} \leq 1200 \; V \end{array}$	-	2550	-	А
Internal gate resistance	R _{gint}		-	1.2	-	Ω
Stray inductance	L _{CE}		-	-	18	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.32	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_C = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. M		MAX.	UNITS		
Diode forward voltage	V _F	I _F = 300 A	T _J = 25 °C	-	1.82	2.25	v
Diode forward voltage	۷F		T _J = 125 °C	-	1.95	-	
D'ale and a second second	Q _{rr}		T _J = 25 °C	-	21.5	-	μC
Diode reverse recovery charge			T _J = 125 °C	-	32.4	-	
Diede zoels reverse recevers every	I _{rr} dl/dt = - 2125 A/μs,	$I_F = 300 \text{ A}, V_R = 600 \text{ V},$	T _J = 25 °C	-	178	-	•
Diode peak reverse recovery current		$V_{GE} = -15 V$	T _J = 125 °C	-	225	-	A
Diada management	E _{rec}		T _J = 25 °C	-	10.4	-	ml
Diode reverse recovery energy			T _J = 125 °C	-	16.6	-	mJ

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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Operating junction temperature range	TJ		-	-	150	°C	
Storage temperature range	T _{STG}		- 40	-	125		
Junction to case	Р		-	-	0.059		
Diode	R _{thJC}		-	-	0.107	K/W	
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-		
Mounting torque		Power terminal screw: M6	2.5 to 5.0		Nm		
Mounting torque		Mounting screw: M6	:	3.0 to 6.0)		
Weight				300		g	

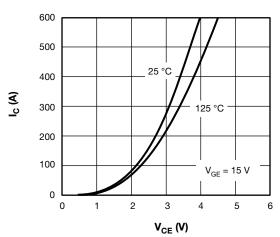


Fig. 1 - IGBT Typical Output Characteristics

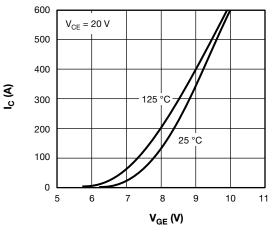
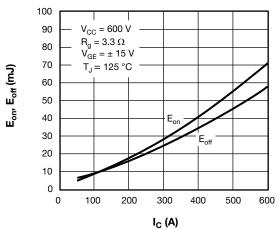
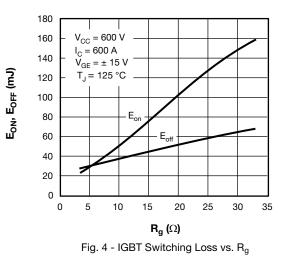


Fig. 2 - IGBT Typical Transfer Characteristics









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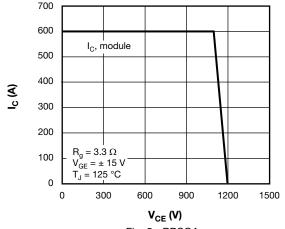
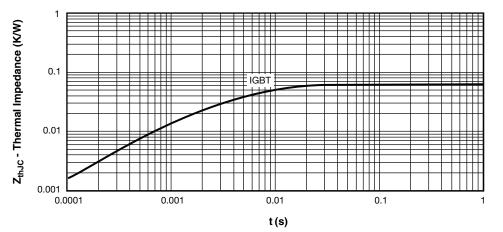
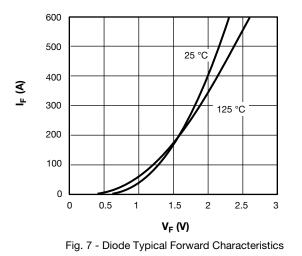


Fig. 5 - RBSOA







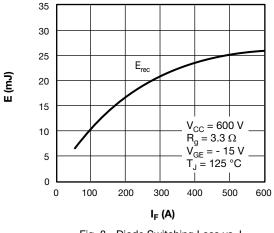


Fig. 8 - Diode Switching Loss vs. I_F

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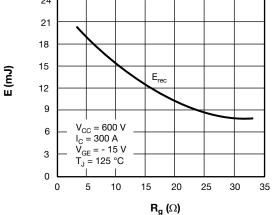
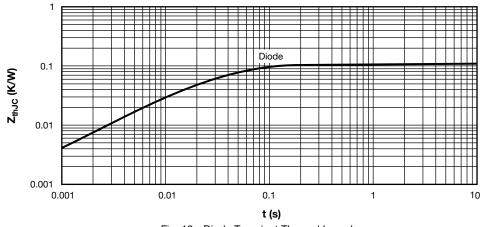
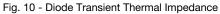
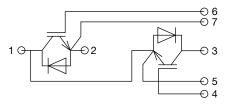


Fig. 9 - Diode Switching Loss vs. Gate Resistance





CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95525	



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