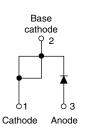


Schottky Rectifier, 6 A





PRODUCT SUMMARY							
Package	TO-220AC						
I _{F(AV)}	6 A						
V _R	35 V, 40 V, 45 V						
V _F at I _F	0.53 V						
I _{RM} max.	7 mA at 125 °C						
T _J max.	175 °C						
Diode variation	Single die						
E _{AS}	8 mJ						

FEATURES

- 175 °C T_J operation
- · High frequency operation
- · Low forward voltage drop
- High purity, high temperature ероху encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES						
I _{F(AV)}	Rectangular waveform	6	A					
V _{RRM}	Range	35 to 45	V					
I _{FSM}	$t_p = 5 \ \mu s \ sine$	690	A					
V _F	6 A _{pk} , T _J = 125 °C	0.53	V					
TJ	Range	- 55 to 175	°C					

VOLTAGE RATINGS											
PARAMETER	SYMBOL	VS- 6TQ035PbF	VS- 6TQ035-N3	VS- 6TQ040PbF	VS- 6TQ040-N3	VS- 6TQ045PbF	VS- 6TQ045-N3	UNITS			
Maximum DC reverse voltage	V _R	35	35	40	40	45	45	v			
Maximum working peak reverse voltage	V _{RWM}										

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS						
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 164 °C	6	А					
Maximum peak one cycle non-repetitive surge current	150.4	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	690	А				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	140	7				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1.20 \text{ A}, L = 11$	8	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T_J maxim	1.20	А					

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1



ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
Maximum forward voltage drop See fig. 1		6 A	T.I = 25 °C	0.60					
	V _{FM} ⁽¹⁾	12 A	1j=25 0	0.73	v				
	VFM (")	6 A	T.I = 125 °C	0.53					
		12 A	1j = 125 C	0.64					
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.8	mA				
See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7					
Threshold voltage	V _{F(TO)}			0.35	V				
Forward slope resistance	r _t	$T_{J} = T_{J}$ maximum		18.23	mΩ				
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range	400	pF					
Typical series inductance	L _S	Measured lead to lead 5 m	8	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C				
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.2	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV				
Approvimate weight				2	g				
Approximate weight				0.07	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
				6TQ	035				
Marking device	Marking device		Case style TO-220AC	6TQ040					
				6TQ045					



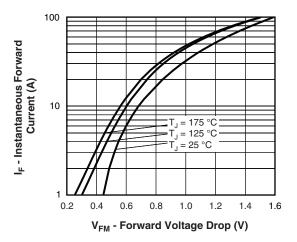


Fig. 1 - Maximum Forward Voltage Drop Characteristics

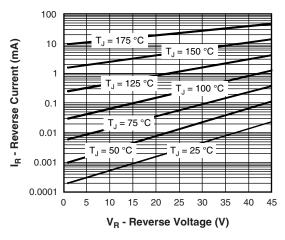


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

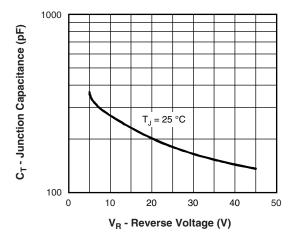
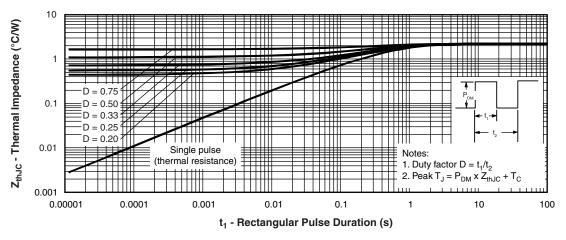
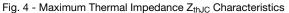


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



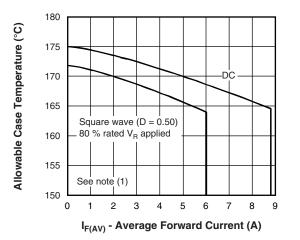


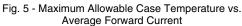
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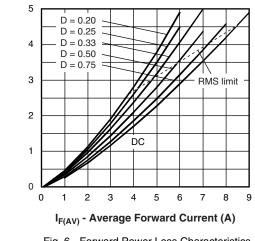


Average Power Loss (W)

Vishay Semiconductors









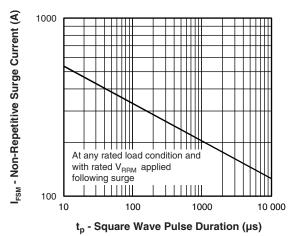


Fig. 7 - Maximum Non-Repetitive Surge Current

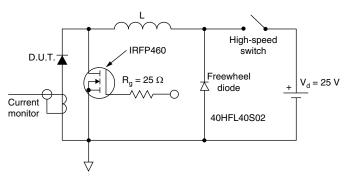


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6);

 Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; $I_R \text{ at } V_{R1}$ = 80 % rated V_R

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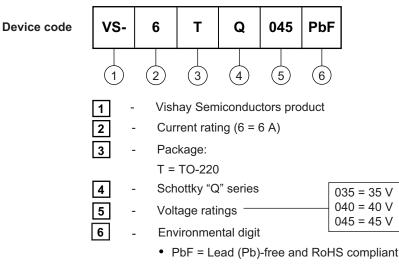
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ORDERING INFORMATION TABLE



• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-6TQ035PbF	50	1000	Antistatic plastic tube							
VS-6TQ035-N3	50	1000	Antistatic plastic tube							
VS-6TQ040PbF	50	1000	Antistatic plastic tube							
VS-6TQ040-N3	50	1000	Antistatic plastic tube							
VS-6TQ045PbF	50	1000	Antistatic plastic tube							
VS-6TQ045-N3	50	1000	Antistatic plastic tube							

LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95221				
	TO-220AC PbF	www.vishay.com/doc?95224				
Part marking information	TO-220AC -N3	www.vishay.com/doc?95068				



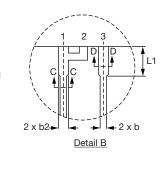
TO-220AC

plane

DIMENSIONS in millimeters and inches









Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183		E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055		E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115		е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040		e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4	H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068		L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4	L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024		L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4	L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3	ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355		Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6	θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6						

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches
- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1
- ⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- ⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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