

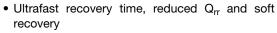
Ultrafast Rectifier, 3 A FRED Pt®





PRODUCT SUMMARY				
Package	SMB (DO-214AA)			
I _{F(AV)}	3 A			
V_{R}	600 V			
V _F at I _F	0.99 V			
t _{rr} typ.	41 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES





RoHS

COMPLIANT **HALOGEN**

FREE

- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- · Low forward voltage drop
- Low leakage current
- per J-STD-020, Meets MSL level LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 2 whisker test
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _L = 110 °C ⁽¹⁾	3	V
Non-repetitive peak surge current per leg	I _{FSM}	T _J = 25 °C, 6 ms square pulse	55	А
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

Note

⁽¹⁾ Mounted on PCB with minimum pad size

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL TEST CONDITIONS MIN. TYP. MA		MAX.	UNITS		
Breakdown voltage, blocking voltage	V_{BR} , V_{R} $I_{R} = 100 \ \mu A$		600	-	-	
Forward valtage	and valte re	I _F = 3 A	-	1.15	1.35	V
Forward voltage V _F	VF	I _F = 3 A, T _J = 150 °C	-	0.99	1.2	
Reverse leakage current	I _R	V _R = V _R rated	-	-	3	
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	100	μA
Junction capacitance	C _T	V _R = 600 V	-	3.9	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I _F = 1.0 A, dI _F /dt =	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		41	-	
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	52	-	
Reverse recovery time	t _{rr}	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	65	ns
		T _J = 25 °C		-	38	-	
	T _J = 125 °C		-	52	-		
Peak recovery current	Dark management I	T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/μs	-	5.6	-	Α
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{R} = 390 \text{ V}$	-	7.3	-	^	
Dayaraa raaayan, aharaa	Reverse recovery charge Q _{rr}	T _J = 25 °C		-	108	-	nC
neverse recovery charge		T _J = 125 °C		-	193	=	I IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, junction to case	R _{thJC} ⁽¹⁾		-	-	18	°C/W
Thermal resistance, junction to ambient	R _{thJA} ⁽¹⁾		-	-	90	C/W
Approximate Weight				0.1		g
Approximate weight				0.003		oz.
Marking device		Case style SMB (DO-214AA)		3U	6H	

Note

⁽¹⁾ Mounted on PCB with minimum pad size

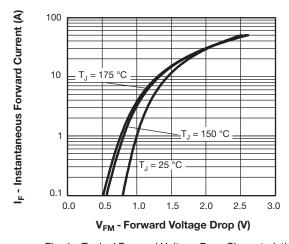


Fig. 1 - Typical Forward Voltage Drop Characteristics

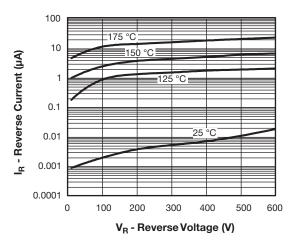


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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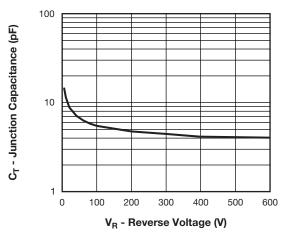


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

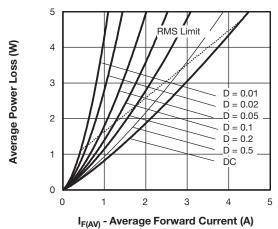


Fig. 5 - Forward Power Loss Characteristics

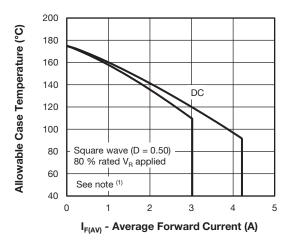


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

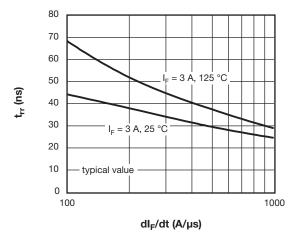


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

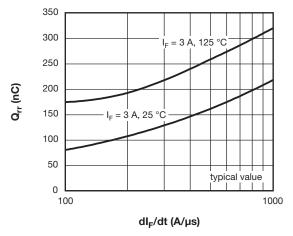
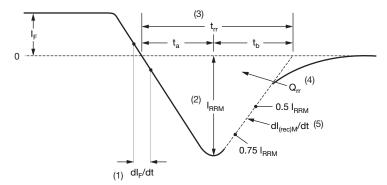


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

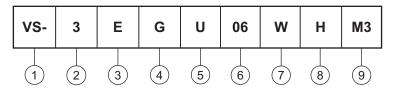
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (3 = 3 A)
- 3 Circuit configuration:

E = single diode

4 - G = SMB package

5 - Process type,

U = ultrafast recovery

Voltage code (06 = 600 V)

7 - W = special

8 - H = AEC-Q101 qualified

9 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-3EGU06WHM3/5BT	5BT	3200	13"diameter plastic tape and reel		

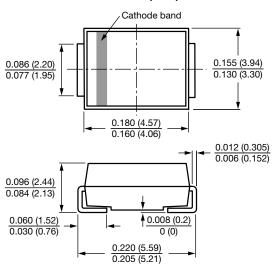
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95401			
Part marking information	www.vishay.com/doc?95624			
Packaging information	www.vishay.com/doc?95404			



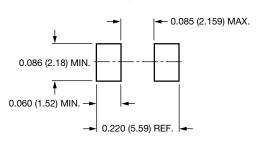
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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