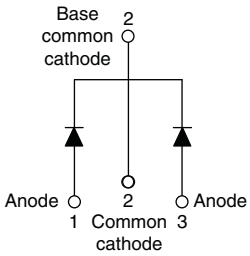
Schottky Rectifier, 2 x 15 A

TO-220AB



FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

Pb-free

e3

RoHS COMPLIANT

HALOGEN FREE Available

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRODUCT SUMMARY	
Package	TO-220AB
$I_{F(AV)}$	2 x 15 A
V_R	50 V, 60 V
V_F at I_F	0.56 V
I_{RM} max.	45 mA at 125 °C
T_J max.	150 °C
Diode variation	Common cathode
E_{AS}	13 mJ

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	30	A
V_{RRM}		50/60	V
I_{FSM}	t_p = 5 µs sine	1000	A
V_F	15 A_{pk}, T_J = 125 °C (per leg)	0.56	V
T_J	Range	- 55 to 150	°C

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30CTQ050PbF	VS-30CTQ050-N3	VS-30CTQ060PbF	VS-30CTQ060-N3	UNITS
Maximum DC reverse voltage	V_R	50	50	60	60	V
Maximum working peak reverse voltage	V_{RWM}					

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	per device	$I_{F(AV)}$	50 % duty cycle at T_C = 105 °C, rectangular waveform		30	A
	per leg				15	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I_{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V_{RRM} applied	1000	
			10 ms sine or 6 ms rect. pulse		260	
Non-repetitive avalanche energy per leg		E_{AS}	T_J = 25 °C, I_{AS} = 1.50 A, L = 11.5 mH		13	mJ
Repetitive avalanche current per leg		I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1.50	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V_{FM} (1)	15 A	T_J = 25 °C	0.62	V
		30 A		0.82	
		15 A	T_J = 125 °C	0.56	
		30 A		0.71	
Maximum reverse leakage current per leg See fig. 2	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	0.80	mA
		T_J = 125 °C		45	
Threshold voltage	$V_{F(TO)}$	T_J = T_J maximum		0.39	V
Forward slope resistance	r_t			8.47	mΩ
Maximum junction capacitance per leg	C_T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		720	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/µs

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J, T_{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		R_{thJC}	DC operation	3.25	°C/W
Maximum thermal resistance, junction to case per package				1.63	
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device			Case style TO-220AB	30CTQ050	
				30CTQ060	

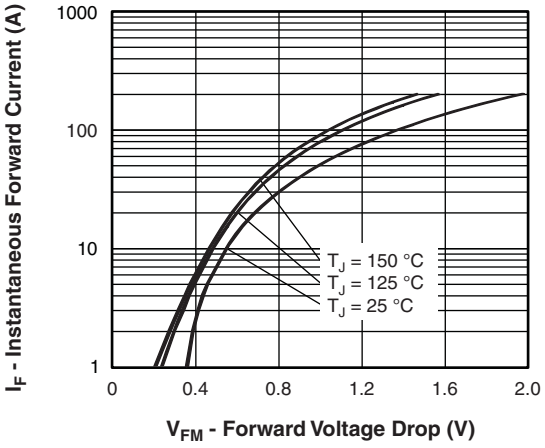


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

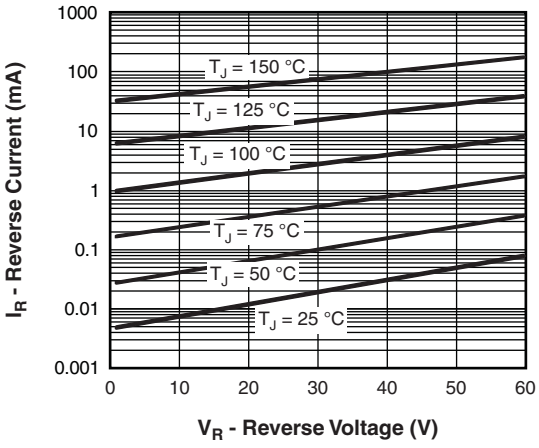


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

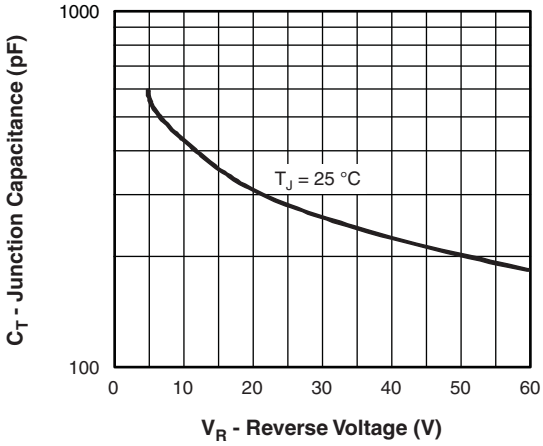


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

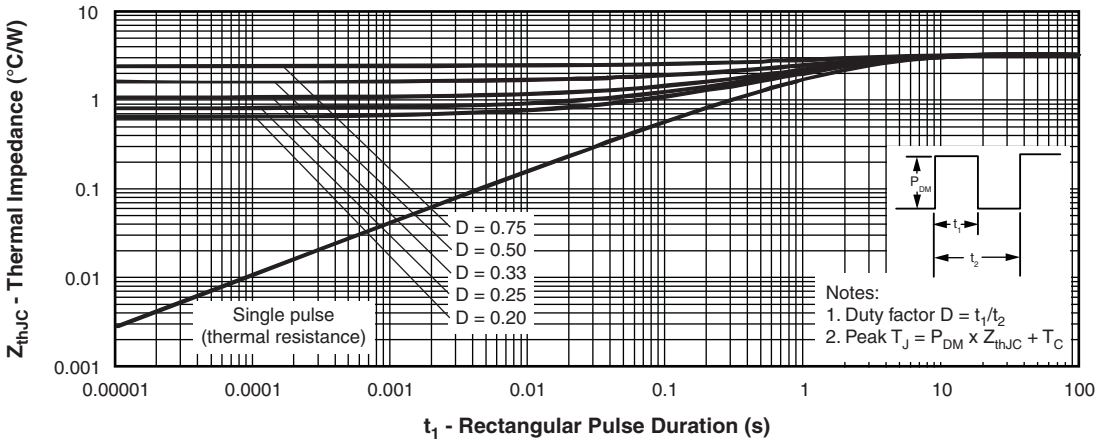


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

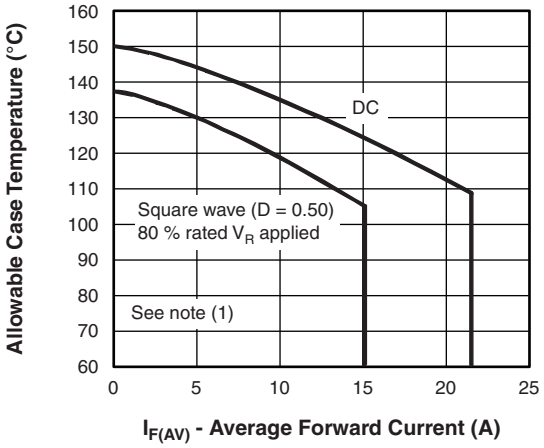


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

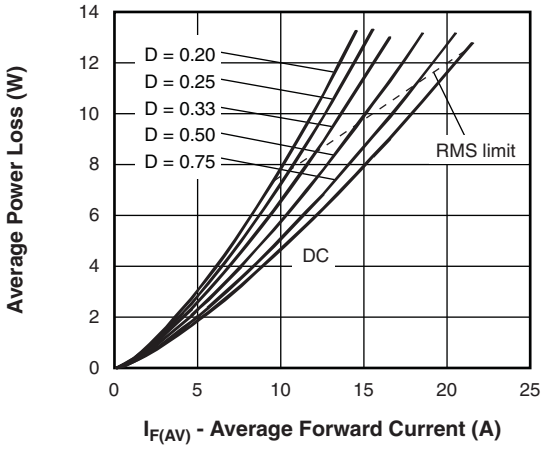


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

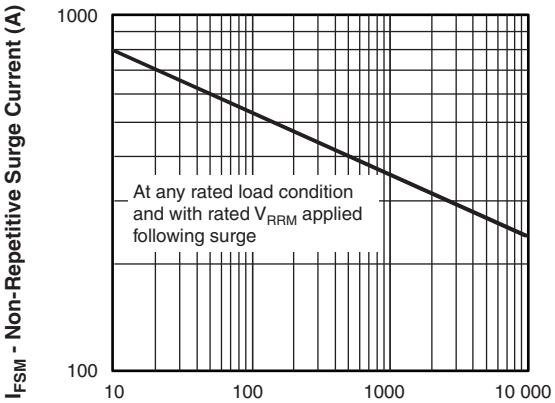


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

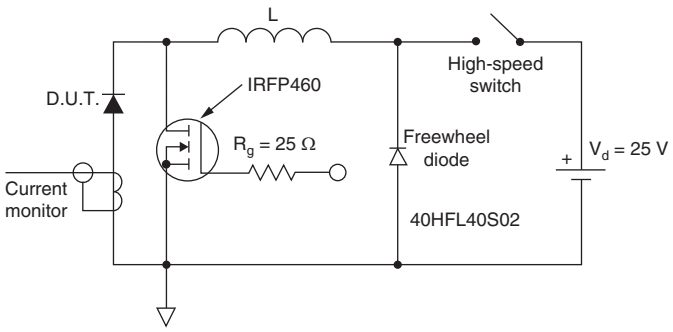


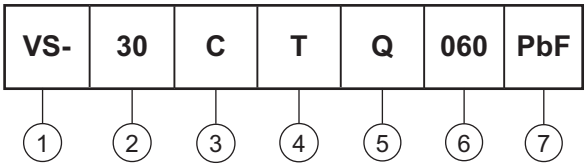
Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 10 V

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product
2 - Current rating (30 = 30 A)
3 - Circuit configuration:
C = Common cathode
4 - Package:
T = TO-220
5 - Schottky "Q" series
6 - Voltage ratings — 050 = 50 V, 060 = 60 V
7 - Environmental digit
- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**QUANTITY PER T/R**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-30CTQ050PbF	50	1000	Antistatic plastic tube
VS-30CTQ050-N3	50	1000	Antistatic plastic tube
VS-30CTQ060PbF	50	1000	Antistatic plastic tube
VS-30CTQ060-N3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS		
Dimensions		www.vishay.com/doc?95222
Part marking information	TO-220ABPbF	www.vishay.com/doc?95225
	TO-220AB-N3	www.vishay.com/doc?95028

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