### **Vishay Semiconductors**





www.vishay.com

**PRODUCT SUMMARY** Package DO-214AC (SMA) 1 A I<sub>F(AV)</sub> 200 V  $V_R$ 0.68 V V<sub>F</sub> at I<sub>F</sub> t<sub>rr</sub> 25 ns T<sub>J</sub> max. 175 °C **Diode variation** Single die

#### **FEATURES**

- Hyperfast recovery time, reduced Qrr, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers, and freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V <sub>RRM</sub>		200	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 158 °C	1	Δ			
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$ , 6 ms square pulse	50	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}$ , $V_{R}$	I <sub>R</sub> = 100 μA	200	-	-		
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 1 A	-	0.82	0.90	V	
		I <sub>F</sub> = 1 A, T <sub>J</sub> = 125 °C	-	0.68	0.76		
Reverse leakage current, per diode	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	2		
		$T_J = 125 \ ^\circ C, V_R = V_R \text{ rated}$	-	1	8	μA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 100 V	-	8	-	pF	

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RoHS

COMPLIANT HALOGEN

FREE



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt =	-	24	-			
Reverse recovery time		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A	-	-	25			
		T <sub>J</sub> = 25 °C		-	15.2	-	A nC	
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 1 A, dI <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 200 V	-	21	-		
Deck recovery ourrent		T <sub>J</sub> = 25 °C		-	1.38	-		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	2	-		
Reverse recovery charge	0	T <sub>J</sub> = 25 °C	]	-	10.6	-		
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C	1	-	21	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C	
Thermal resistance, junction to case	R <sub>thJC</sub>	Device mounted on PCB with 2 x 3.5 mm soldering lands	-	11	21	°C/W	
Approximate weight			0.07			g	
Approximate weight		0.002		0.002		oz.	
Marking device		Case style SMA (DO-214AC)		11	H2		

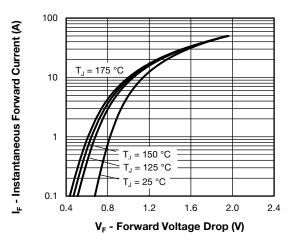


Fig. 1 - Typical Forward Voltage Drop Characteristics

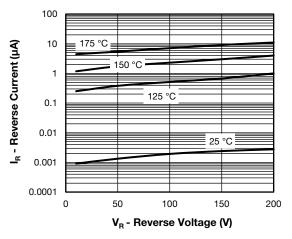
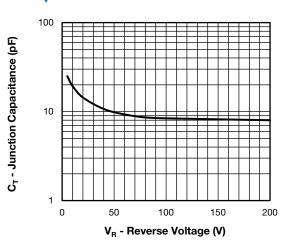


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

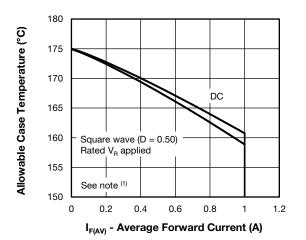
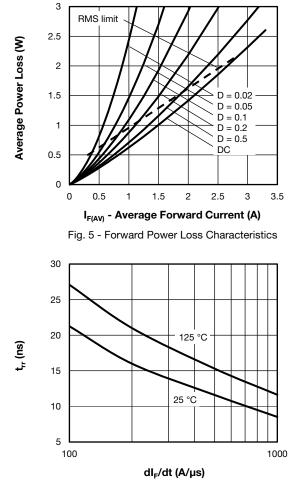


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current





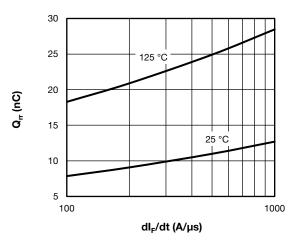


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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# VS-1EMH02-M3

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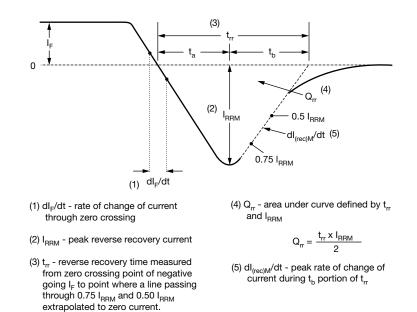


Fig. 8 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

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Device code	vs-	1	Е	м	н	02	-M3
Device code	V3-			IVI		02	-1413
	1	2	3	4	5	6	7
	1	- Visl	nay Sen	nicondu	ctors pro	oduct	
	2	- Cur	rent rati	ng (1 =	1 A)		
	3	- Circ	uit conf	iguratio	n:		
		E =	single c	liode			
	4	- M =	SMA p	ackage			
	5	- Pro	cess typ	e,			
	_	H =	hyperfa	st reco	/ery		
	6	- Voli	age coo	le (02 =	200 V)		
	7	M3	= halog	gen-free	, RoHS	-complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	REFERRED P/N QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTIO						
VS-1EMH02-M3/5AT	7500	7500	13"diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95400					
Part marking information	www.vishay.com/doc?95472					
Packaging information	www.vishay.com/doc?95404					

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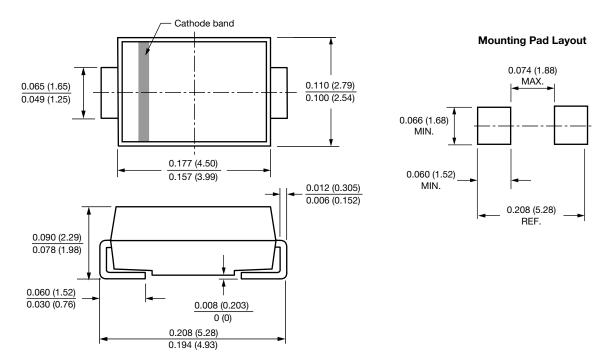
# **Outline Dimensions**

### **Vishay Semiconductors**

SMA

#### **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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