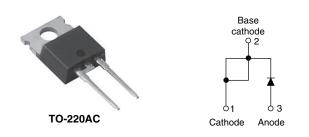
Vishay Semiconductors

High Performance Schottky Rectifier, 15 A



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PRODUCT SUMMARY							
Package	TO-220AC						
I _{F(AV)}	15 A						
V _R	60 V						
V _F at I _F	0.56 V						
I _{RM} max.	45 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Single die						
E _{AS}	6 mJ						

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC[®]-JESD47
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-15TQ060... Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES							
I _{F(AV)}	Rectangular waveform	15	А						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	1000	А						
V _F	15 A _{pk} , T _J = 125 °C	0.56	V						
TJ	Range	-55 to 150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-15TQ060PbF	VS-15TQ060-N3	UNITS					
Maximum DC reverse voltage	V _R	60	60	V					
Maximum working peak reverse voltage	V _{RWM}	00	00	v					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 104 °C	15					
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and	1000	А			
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	260				
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11	6	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxim	1.50	А				

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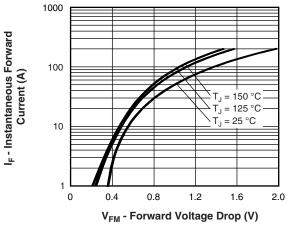
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		15 A	T.I = 25 °C	0.62				
Maximum forward voltage drop	V _{EM} ⁽¹⁾	30 A	1j=25 C	0.82	v			
See fig. 1	VFM ("	15 A	T.I = 125 °C	0.56				
		30 A	1j = 125 C	0.71				
Maximum reverse leakage curent	I _{RM} (1)	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.80	mA			
See fig. 2		T _J = 125 °C	VR = naleu VR	45				
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		720	pF			
Typical series inductance	L _S	Measured lead to lead 5 n	8	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

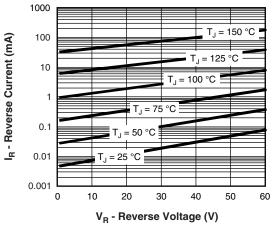
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to 150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.25	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	0/14				
Approximate weight			2	g				
			0.07	oz.				
Mounting torque			6 (5)	kgf · cm				
Mounting torque maximum			12 (10)	(lbf ⋅ in)				
Marking device	Case style TO-220AC (JEDEC)		15TC	2060				

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Fig. 1 - Maximum Forward Voltage Drop Characteristics





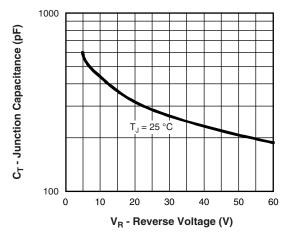


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

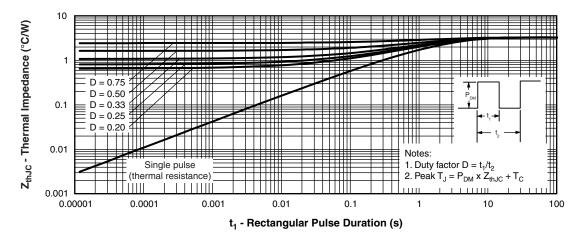
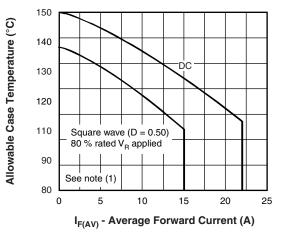


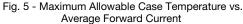
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

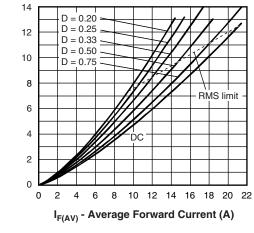
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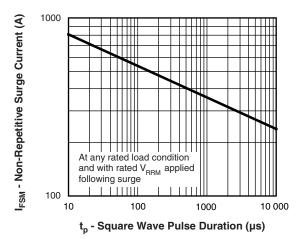


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Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current

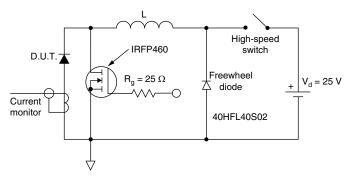


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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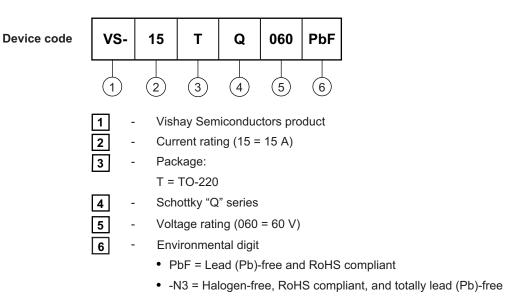
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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-15TQ060PbF	50	1000	Antistatic plastic tube						
VS-15TQ060-N3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95221				
Part marking information	TO-220ACPbF	www.vishay.com/doc?95224				
	TO-220AC-N3	www.vishay.com/doc?95068				
SPICE model		www.vishay.com/doc?95600				



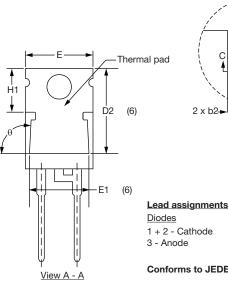
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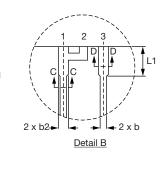
TO-220AC

plane

DIMENSIONS in millimeters and inches









Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL -	MILLIMETERS		INCHES		NOTES
STMBUL	MIN.	MAX.	MIN.	MAX.	NOTES			MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches
- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1
- ⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- ⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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