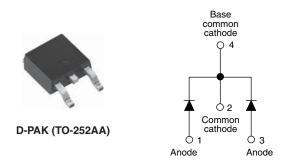


Schottky Rectifier, 2 x 6 A



PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I _{F(AV)}	2 x 6 A				
V _R	40 V				
V _F at I _F	0.48 V				
I _{RM}	40 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	9 mJ				

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-12CWQ04FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNIT						
I _{F(AV)}	Rectangular waveform	12	А					
V _{RRM}		40	V					
I _{FSM}	t _p = 5 μs sine	550	А					
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.48	V					
TJ	Range	- 55 to 150	°C					

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-12CWQ04FNHM3	UNITS			
Maximum DC reverse voltage	V _R	40	V			
Maximum working peak reverse voltage	V _{RWM}	40	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg	I =	$I_{F(AV)}$ 50 % duty cycle at T _C = 134 °C, rectangular waveform		50.94 duty cyclo at $T_{\rm c} = 124.$ °C, rectangular waveform		6	А
See fig. 5	per device			, rectangular wavelonn	12	~		
Maximum peak one cycle non-repetitive surge current		I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	550	A		
See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	90			
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 1.5 \text{ A}, L = 8 \text{ mH}$		4	9	mJ				
Repetitive avalanche curren	t per leg	I _{AR}	$ \begin{array}{c} \mbox{Current decaying linearly to zero in 1 } \mu s \\ \mbox{Frequency limited by } T_J \mbox{ maximum } V_A = 1.5 \ x \ V_R \ typical \end{array} $		1.2	А		

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FREE



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		6 A	T.I = 25 °C	0.53	V	
Maximum forward	V _{EM} ⁽¹⁾	12 A	1j = 23 0	0.68		
voltage drop per leg See fig. 1	VFM (')	6 A	T.I = 125 °C	0.48		
		12 A	$-1_{\rm J} = 125$ C	0.64		
Maximum reverse	I _{BM} ⁽¹⁾	T _J = 25 °C		3	mA	
leakage current per leg See fig. 2	IRM (")	T _J = 125 °C	V _R = Rated V _R	40		
Threshold voltage	V _{F(TO)}	T T mavimum	0.28	V		
Forward slope resistance	r _t	T _J = T _J maximum 25.58 n				
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C 405 pF			pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 nH			nH	

Note

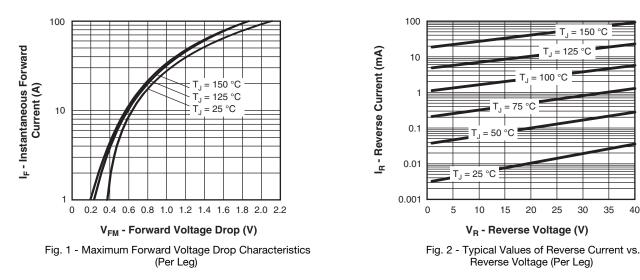
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and srorage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	P	DC operation	3.0	°C/W
junction to case	per device	R _{thJC}	See fig. 4	1.5	0/11
Approvimeto weight				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style D-PAK	12CWQ	04FNH

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink





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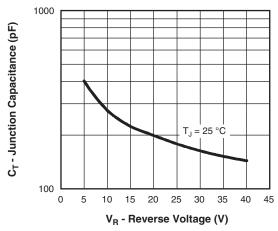


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

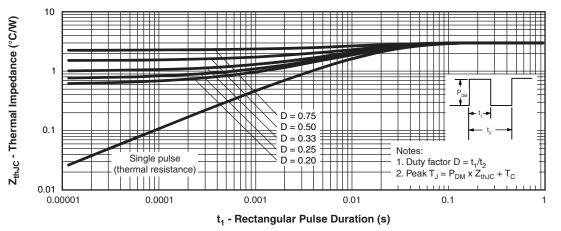
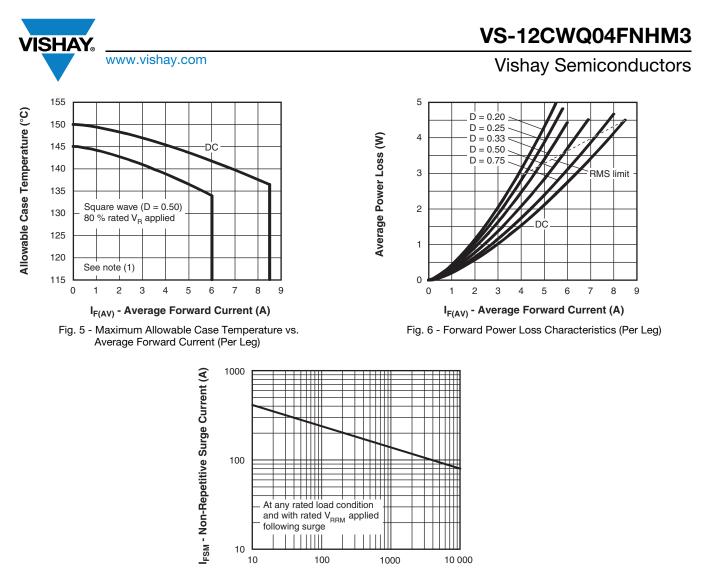


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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tp - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} =$ Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} =$ 80 % rated V_R



ORDERING INFORMATION TABLE

www.vishay.com

ISHAY

Device code	VS-	12	С	w	Q	04	FN	TRL	Н	М3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)
				Ŭ	C		\bigcirc	0	\bigcirc	0
			hay Sen			Jauci				
	2		rrent rati							
	3		nter tap	-	ation					
	4	- Pa	ckage id	entifier:						
		W	= D-PAk	ζ.						
	5	- Sc	Schottky "Q" series							
	6	- Vo	Voltage rating (04 = 40 V)							
	7	- FN	FN = TO-252AA							
	8	- • N	• None = Tube							
		• T	R = Tap	e and re	el					
			• TRL = Tape and reel (left oriented)							
			• TRR = Tape and reel (right oriented)							
	9		= AEC-G	-						
				·						
	10		vironme	-						
		M3	= Halog	jen-free,	, RoHS-	complia	int, and	termina	tions le	ad (Pb)-

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-12CWQ04FNHM3	75	3000	Antistatic plastic tube				
VS-12CWQ04FNTRHM3	2000	2000	13" diameter reel				
VS-12CWQ04FNTRRHM3	3000	3000	13" diameter reel				
VS-12CWQ04FNTRLHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

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