

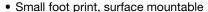
# **High Performance Schottky Rectifier, 1 A**

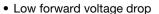


DO-214AC (SMA)

PRODUCT SUMMARY				
Package	DO-214AC (SMA			
I <sub>F(AV)</sub>	1 A			
$V_R$	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.78 V			
I <sub>RM</sub>	1 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	1.0 mJ			

#### **FEATURES**







- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-10MQ100NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES UNI				
I <sub>F(AV)</sub>	Rectangular waveform	1	Α		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	120	Α		
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.68	V		
T <sub>J</sub>	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-10MQ100NPbF	UNITS
Maximum DC reverse voltage	$V_R$	100 V	
Maximum working peak reverse voltage	$V_{RWM}$		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T <sub>L</sub> = 126 °C, On PC board 9 mm <sup>2</sup> island (0.013		1.5	А
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 135 °C, On PC board 9 mm <sup>2</sup> island (0.013	rectangular waveform 8 mm thick copper pad area)	1	A
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	120	
non-repetitive surge current, $T_J = 25$ °C See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	30	A
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C}$ , $I_{AS} = 0.5  \text{A}$ , $L = 8  \text{mH}$		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		0.5	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T <sub>.1</sub> = 25 °C	0.78	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	1J=25 C	0.85	
See fig. 1	V FM ('')	1 A	T <sub>J</sub> = 125 °C	0.63	
		1.5 A		0.68	
Maximum reverse leakage current	Maximum reverse leakage current (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.1	- mA
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C		1	
Threshold voltage	V <sub>F(TO)</sub>	$T_{J} = T_{J} \text{ maximum} $ $0.52$ $78.4$		0.52	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	C <sub>T</sub>	$V_R = 10 V_{DC}$ , $T_J = 25 °C$ , test signal = 1 MHz 38		38	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0 nl		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width  $<300~\mu s,$  duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (similar D-64)	1.	J

### Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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# Vishay Semiconductors

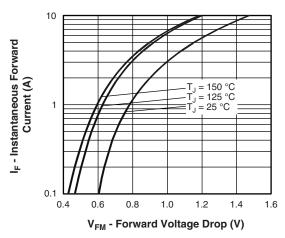


Fig. 1 - Maximum Forward Voltage Drop Characteristics

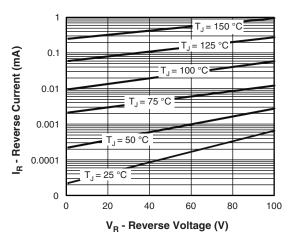


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

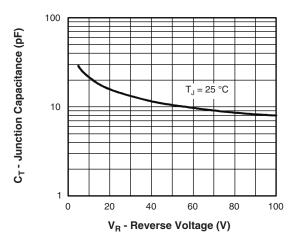
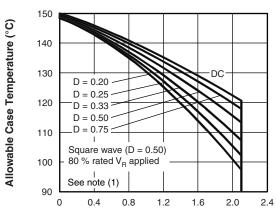


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature

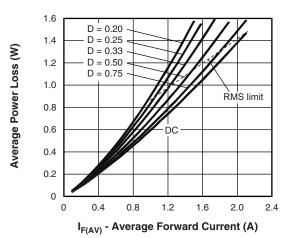
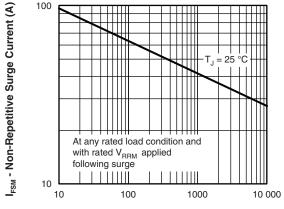


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t<sub>n</sub> - Square Wave Pulse Duration (µs)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

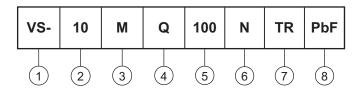
#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (10 = 1 A)

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - N = new SMA

7 - TR = tape and reel

PbF = lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-10MQ100NTRPbF	5AT	7500	13" diameter plastic tape and reel			

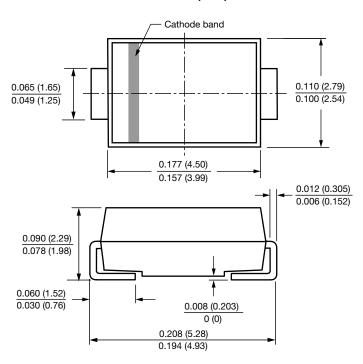
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95400</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95371			



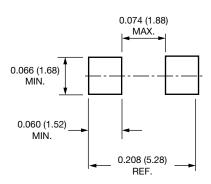
## **SMA**

### **DIMENSIONS** in inches (millimeters)

### **DO-214AC (SMA)**



### Mounting Pad Layout





### **Legal Disclaimer Notice**

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