High Performance Schottky Rectifier, 1.0 A

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

PRODUCT SUMMARY	
Package	SMB
$I_{F(AV)}$	1.0 A
V_R	15 V
V_F at I_F	0.21 V
I_{RM}	35 mA at 100 °C
T_J max.	125 °C
Diode variation	Single die
E_{AS}	1.0 mJ

DESCRIPTION

The VS-10BQ015HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	1.0	A
V_{RRM}		15	V
I_{FSM}	t_p = 5 µs sine	140	A
V_F	1.0 A_{pk}, T_J = 125 °C	0.21	V
T_J	Range	-55 to +125	°C

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-10BQ015HM3	UNITS
Maximum DC reverse voltage	V_R	15	V
Maximum working peak reverse voltage	V_{RWM}	25	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at T_L = 134 °C, rectangular waveform		1.0	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I_{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V_{RRM} applied	140	A
		10 ms sine or 6 ms rect. pulse		40	
Non-repetitive avalanche energy	E_{AS}	T_J = 25 °C, I_{AS} = 1 A, L = 2 mH		1.0	mJ
Repetitive avalanche current	I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1.0	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V_{FM} (1)	1 A	T_J = 25 °C	0.33	V
		2 A		0.39	
		1 A	T_J = 125 °C	0.21	
		2 A		0.29	
Maximum reverse leakage current See fig. 2	I_{RM}	T_J = 25 °C	V_R = Rated V_R	0.5	mA
		T_J = 100 °C		35	
Threshold voltage	$V_{F(TO)}$	T_J = T_J maximum		-	V
Forward slope resistance	r_t			-	mΩ
Typical junction capacitance	C_T	V_R = 5 V_{DC}, (test signal range 100 kHz to 1 MHz), 25 °C		390	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/µs

Note

(1) Pulse width = 300 µs, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T_J (1)		-55 to +125	°C
Maximum storage temperature range	T_{Stg}		-55 to +150	
Maximum thermal resistance, junction to lead	R_{thJL} (2)	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R_{thJA}	DC operation	80	
Approximate weight			0.10	g
			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	1C	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB

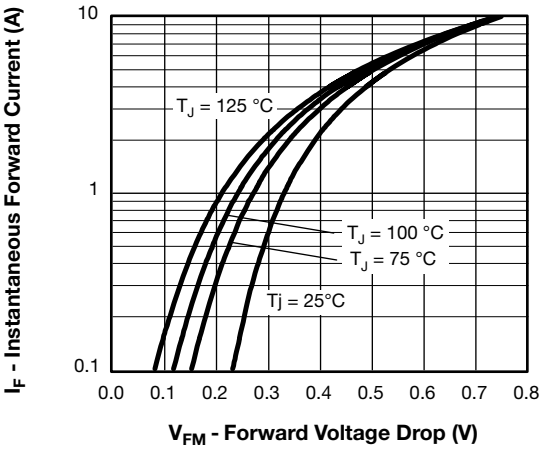


Fig. 1 - Maximum Forward Voltage Drop Characteristics

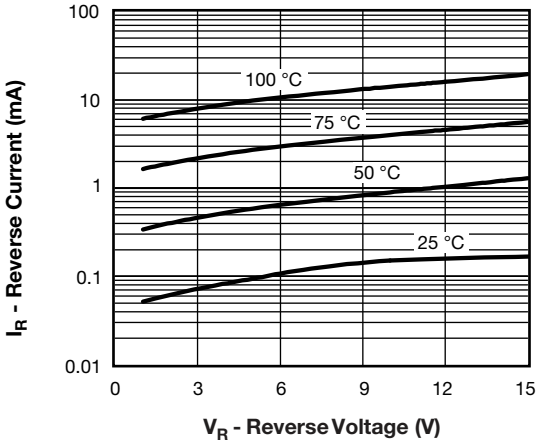


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

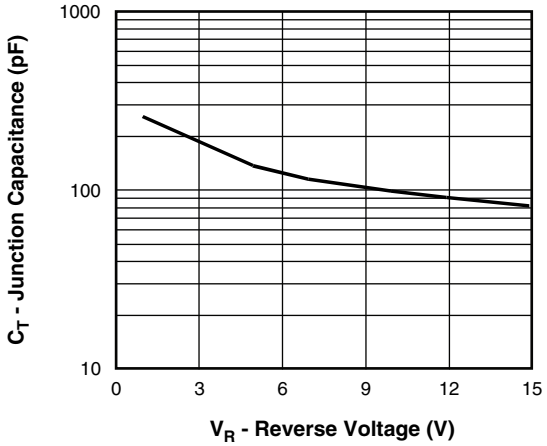


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

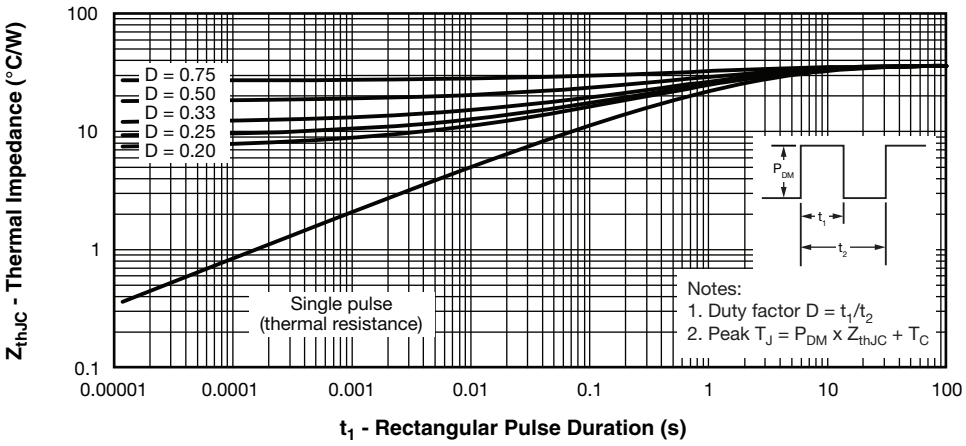


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

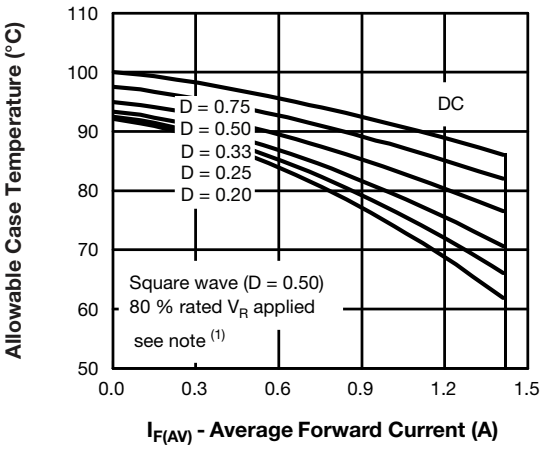


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

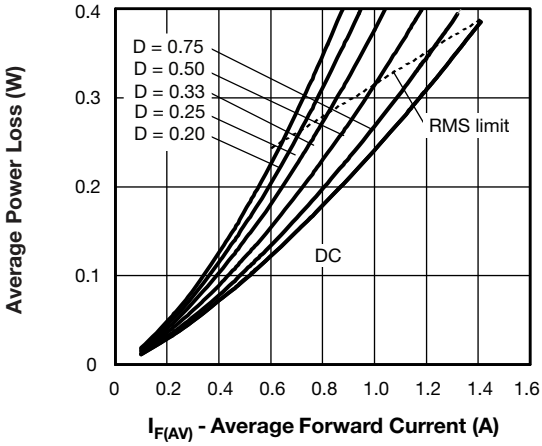


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

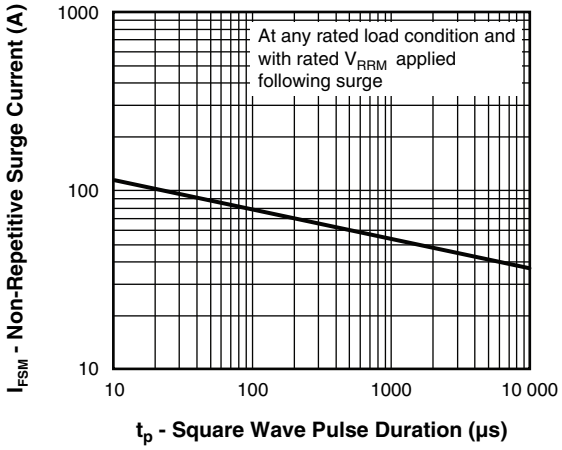


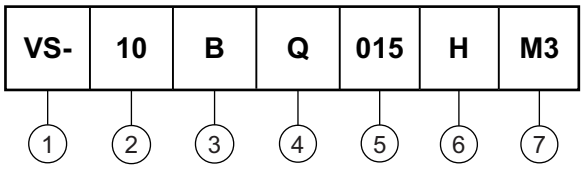
Fig. 7 - Maximum Non-Repetitive Surge Current

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product
2 - Current rating
3 - B = SMB
4 - Q = Schottky “Q” series
5 - Voltage rating (015 = 15 V)
6 - H = AEC-Q101 qualified
7 - Environmental digit:
M3 = halogen-free, RoHS compliant and terminations lead (Pb)-free

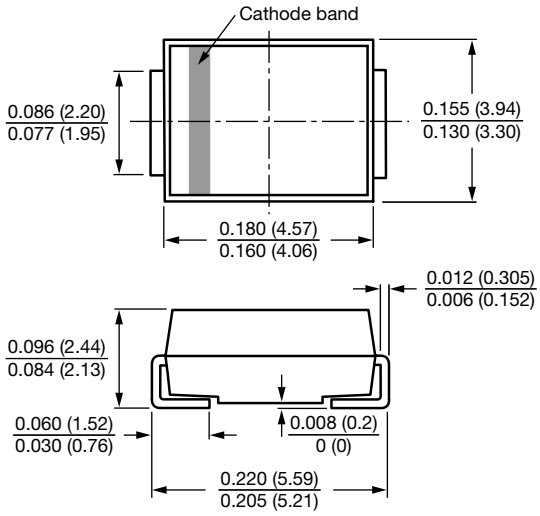
ORDERING INFORMATION (Example)			
PREFERRED P/N	**PREFERRED PACKAGE CODE**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-10BQ015HM3/5BT	5BT	3200	13" diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95401
Part marking information	www.vishay.com/doc?95403
Packaging information	www.vishay.com/doc?95404
SPICE model	www.vishay.com/doc?95666

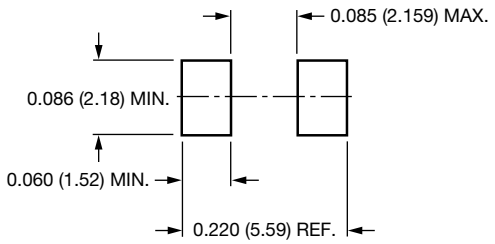
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout



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