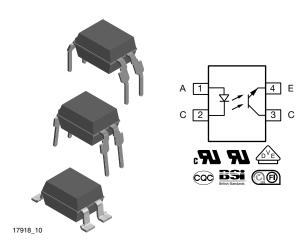


## Vishay Semiconductors

# **Optocoupler, Low Input Current, Phototransistor Output**



### **DESCRIPTION**

The VO610A consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic dual inline package.

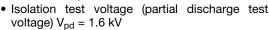
#### AGENCY APPROVALS

(All parts are certified under base model VO610A)

- BSI: EN 60065:2002, EN 60950:2000
- DIN EN 60747-5-5, available with option 1
- FIMKO EN 60065, EN 60335-1, EN 60950-1
- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- CQC GB4943.1-2011, GB8898-2011

#### **FEATURES**

- Temperature range 55 °C to + 110 °C
- Rated impulse voltage (transient overvoltage)
   V<sub>IOTM</sub> = 6 kV<sub>peak</sub>





voltage) V<sub>pd</sub> = 1.6 kV
 Rated isolation voltage (RMS includes DC)

RoHS COMPLIANT

- V<sub>IOWM</sub> = 600 V<sub>RMS</sub>
   Rated recurring peak voltage (repetitive) V<sub>IORM</sub> = 850 V<sub>peak</sub>
- Thickness through insulation ≥ 0.4 mm
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **APPLICATIONS**

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- for appl. class I IV at mains voltage ≤ 300 V
- for appl. class I IV at mains voltage ≤ 600 V according to table 1 of IEC 60664-1, suitable for:
  - Switch-mode power supplies
  - Line receiver
  - Computer peripheral interface
  - Microprocessor system interface

ORDERING INFORMATION		
V O 6 1 0 A  PART NUMBER	- # X 0 # #  CTR PACKAGE OPTION BIN	TAPE AND REEL  10.16 mm
		Option 8 Option 9

AGENCY CERTIFIED/PACKAGE	CTR (%)					
BSI, FIMKO, UL, cUL	40 to 80	63 to 125	100 to 200	160 to 320		
DIP-4	VO610A-1	VO610A-2	VO610A-3	-		
SMD-4, option 7	-	-	VO610A-3X007T	-		
SMD-4, option 8	=	-	VO610A-3X008T	VO610A-4X008T		
SMD-4, option 9	-	-	VO610A-3X009T	-		
VDE, BSI, FIMKO, UL, cUL	40 to 80	63 to 125	100 to 200	160 to 320		
DIP-4	-	-	VO610A-3X001	=		
DIP-4, 400 mil, option 6	-	-	VO610A-3X016	-		
SMD-4, option 7	=	-	-	VO610A-4X017T		
SMD-4, option 8	=	-	VO610A-3X018T	=		
SMD-4, option 9	VO610A-1X019T	-	VO610A-3X019T	VO610A-4X019T		

### Note

Rev. 1.7, 18-Feb-13

Additional options may be possible, please contact sales office



## Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION SYMBOL VALUE U						
INPUT							
Reverse voltage		$V_R$	6	V			
Forward current		I <sub>F</sub>	60	mA			
Forward surge current	$t_p \le 10 \; \mu s$	I <sub>FSM</sub>	1.5	Α			
LED power dissipation	at 25 °C	P <sub>diss</sub>	100	mW			
OUTPUT							
Collector emitter voltage		V <sub>CEO</sub>	70	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		I <sub>C</sub>	50	mA			
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA			
Output power dissipation	at 25 °C	P <sub>diss</sub>	150	mW			
COUPLER							
Isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub>	5000	$V_{RMS}$			
Operating ambient temperature range		T <sub>amb</sub>	- 55 to + 110	°C			
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C			
Soldering temperature (1)	2 mm from case, ≤ 10 s	T <sub>sld</sub>	260	°C			

#### Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted parts (SMD), and wave profile for soldering conditions for through hole parts (DIP), please go to "Assembly Instructions" (<a href="https://www.vishay.com/doc?80054">www.vishay.com/doc?80054</a>).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = 50 \text{ mA}$	V <sub>F</sub>		1.25	1.6	V
Reverse current	$V_R = 6 V$	I <sub>R</sub>			100	μA
Junction capacitance	V <sub>R</sub> = 0, f = 1 MHz	C <sub>j</sub>		50		pF
OUTPUT						
Collector emitter voltage	$I_C = 1 \text{ mA}$	$V_{CEO}$	70			V
Emitter collector voltage	$I_E = 100 \mu A$	V <sub>ECO</sub>	7			V
Collector emitter cut-off current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>		10	100	nA
COUPLER						
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>			0.3	V
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>		110		kHz
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.6		pF

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



# Vishay Semiconductors

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	TEST CONDITION PART SYMBOL MIN. TYP. MAX.						
		VO610A-1	CTR	13	30		%	
	$V_{CF} = 5 \text{ V}, I_{F} = 1 \text{ mA}$	VO610A-2	CTR	22	45		%	
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 IIIA	VO610A-3	CTR	34	70		%	
1 //		VO610A-4	CTR	56	90		%	
$I_{C}/I_{F}$ $V_{CE} = 5 \text{ V, } I_{F} = 10$		VO610A-1	CTR	40		80	%	
	\\ - 5 \\ \ - 10 m\	VO610A-2	CTR	63		125	%	
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 IIIA	VO610A-3	CTR	100		200	%	
		VO610A-4	CTR	160		320	%	

SAFETY AND INSULATION RATED PARAMETERS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	$V_{pd}$	1.6			kV
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$ (see figure 2)	V <sub>pd</sub>	1.3			kV
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>			Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω
Rated impulse voltage		V <sub>IOTM</sub>			6	kV
Max. working voltages	Recurring peak voltage	V <sub>IORM</sub>	850			V <sub>peak</sub>
Forward current		I <sub>SI</sub>			130	mA
Power dissipation		P <sub>SO</sub>			265	mW
Safety temperature		T <sub>si</sub>			150	°C
Creepage distance			7.6			mm

### Note

• According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2 (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

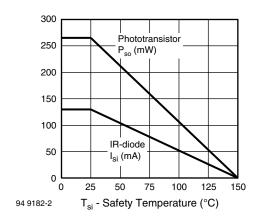


Fig. 1 - Derating Diagram

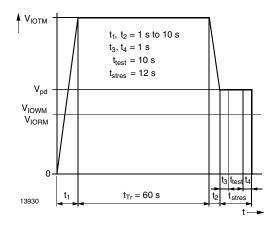


Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5 (VDE0884), IEC 60747

# Vishay Semiconductors

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5$ V, $I_C = 2$ mA, $R_L = 100$ $\Omega$ , (see figure 3)	t <sub>d</sub>		3		μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>r</sub>		3		μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>f</sub>		4.7		μs
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	ts		0.3		μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>on</sub>		6		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>off</sub>		5		μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 4)	t <sub>on</sub>		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 4)	t <sub>off</sub>		10		μs

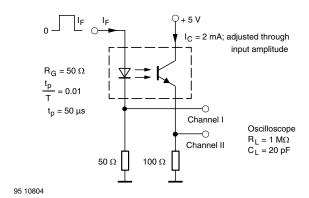


Fig. 3 - Test Circuit, Non-Saturated Operation

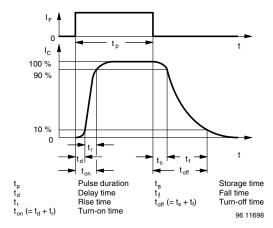


Fig. 5 - Switching Times

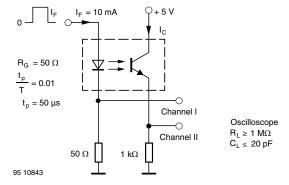
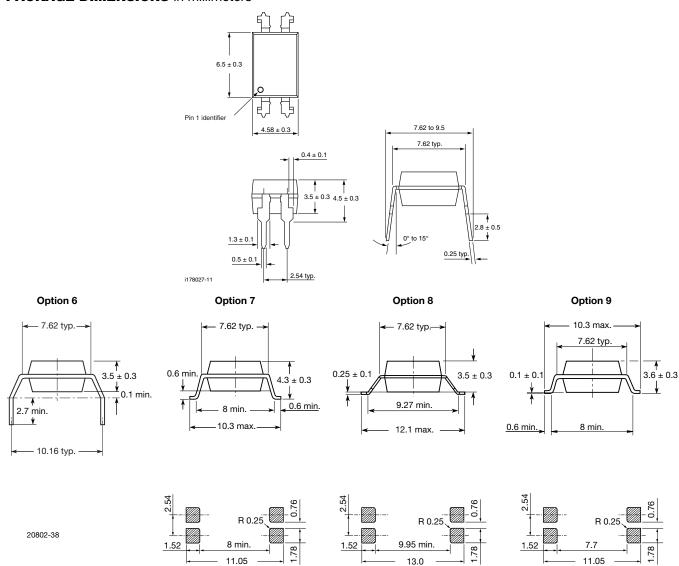


Fig. 4 - Test Circuit, Saturated Operation

# Vishay Semiconductors

### **PACKAGE DIMENSIONS** in millimeters



### PACKAGE MARKING (Example of VO617A-3X018T)



### Note

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE logo is only printed on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



# Vishay Semiconductors

#### **PACKING INFORMATION**

DEVICE PER TUBE							
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX				
DIP-4, standard and option 6	100	40	4000				

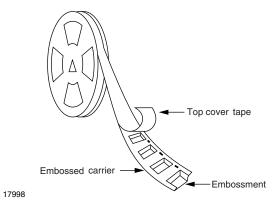


Fig. 6 - Tape and Reel Shipping Medium

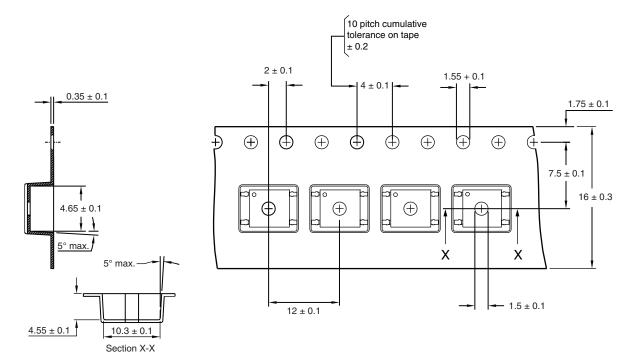


Fig. 7 - Tape and Reel Packing for Option 7 and Option 9 (1000 units per reel)

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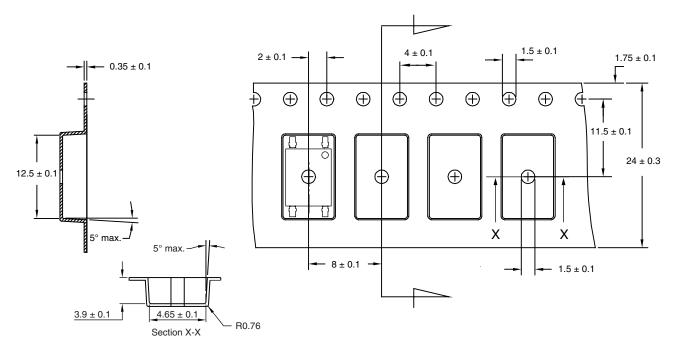


Fig. 8 - Tape and Reel Packing for Option 8 (2000 units per reel)



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Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000