

N-Channel Enhancement-Mode MOSFET

DESIGNED FOR:

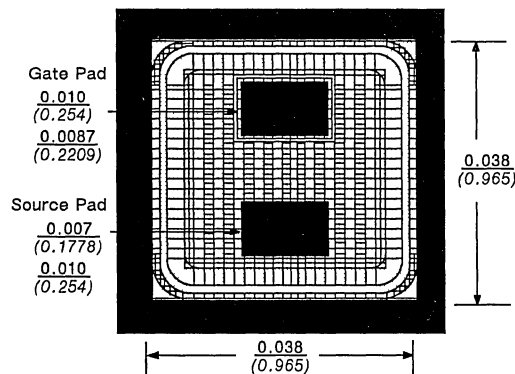
- Switching
- Amplification

FEATURES

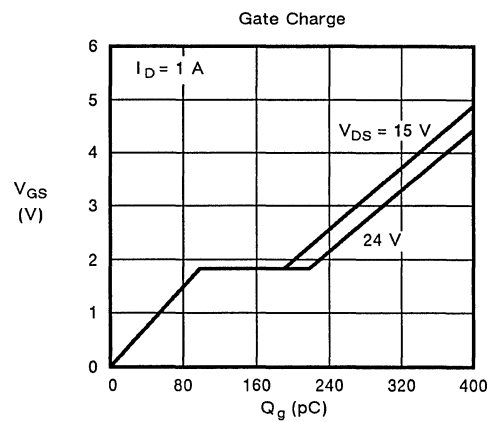
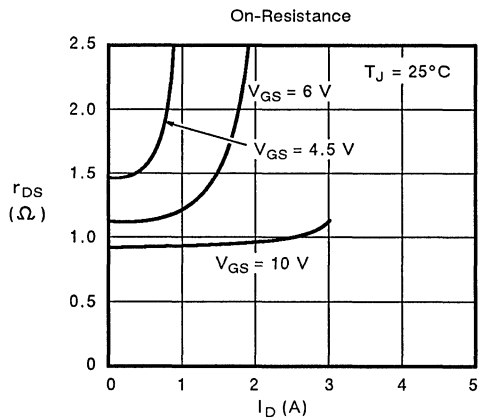
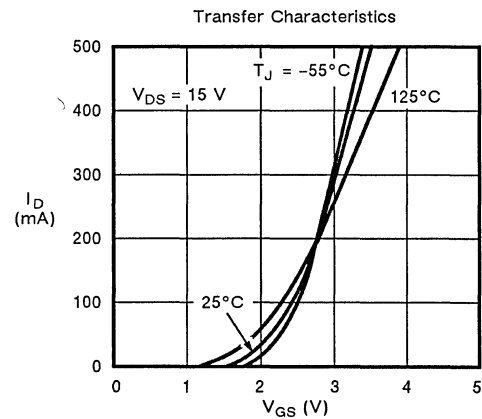
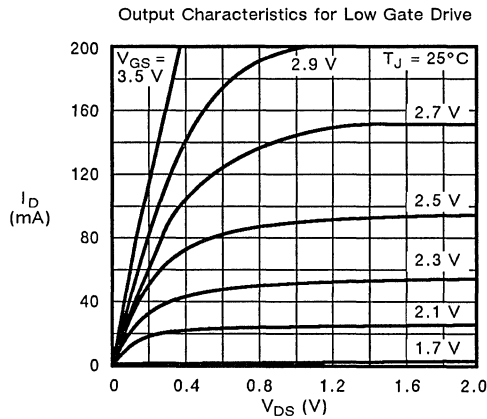
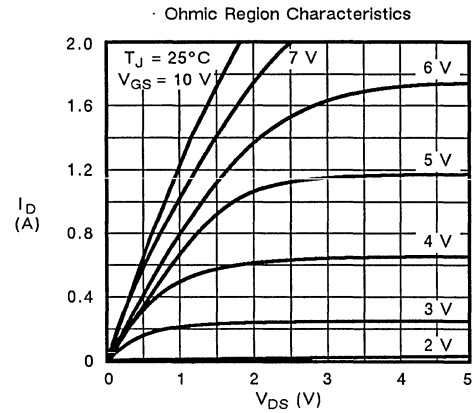
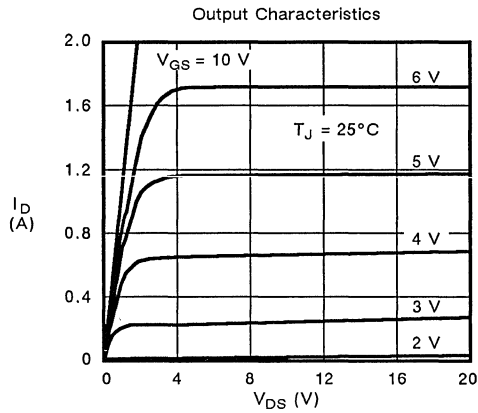
- Low $r_{DS(on)} < 3 \Omega$

TYPE	PACKAGE	DEVICE
Single	TO-205AD	• VN0300B
	TO-92	• VN0300L
	TO-237	• VN0300M
Quad	14-Pin Plastic	• VQ1001J
	14-Pin Dual-In-Line	• VQ1001P
	Chip	• Available as above specifications

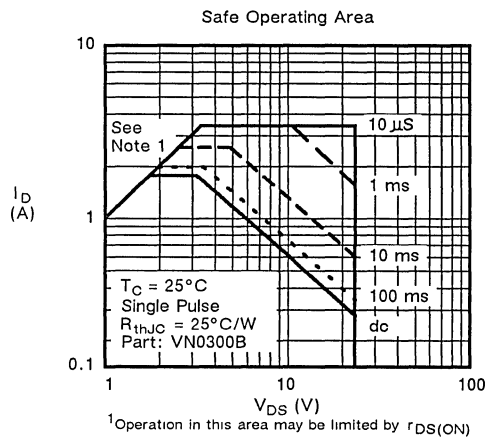
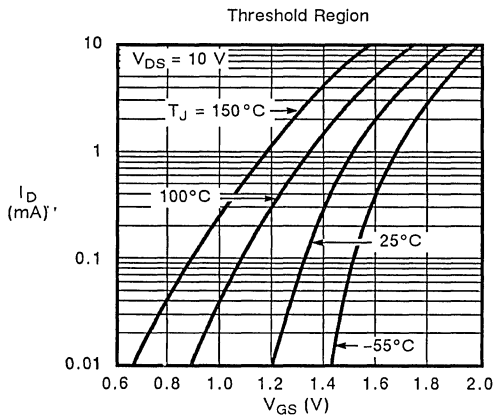
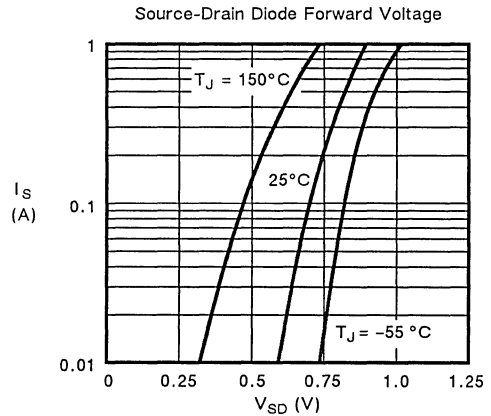
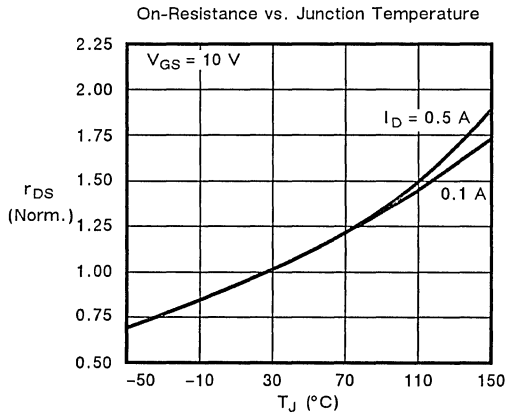
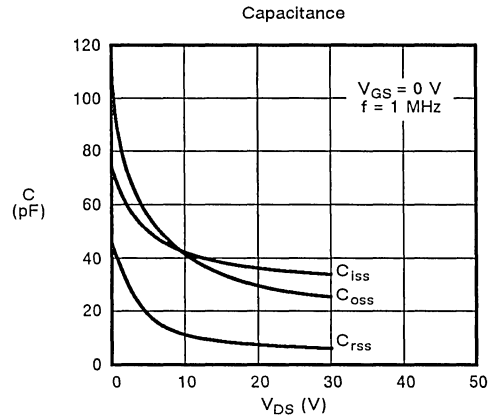
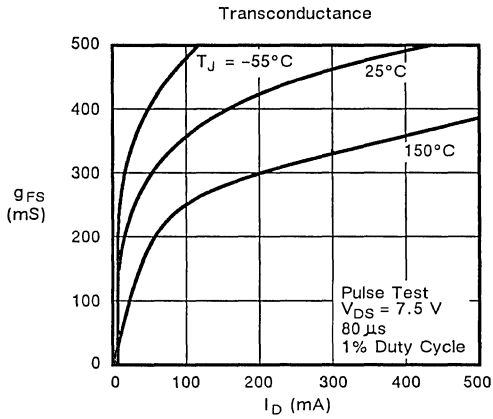
GEOMETRY DIAGRAM



TYPICAL CHARACTERISTICS

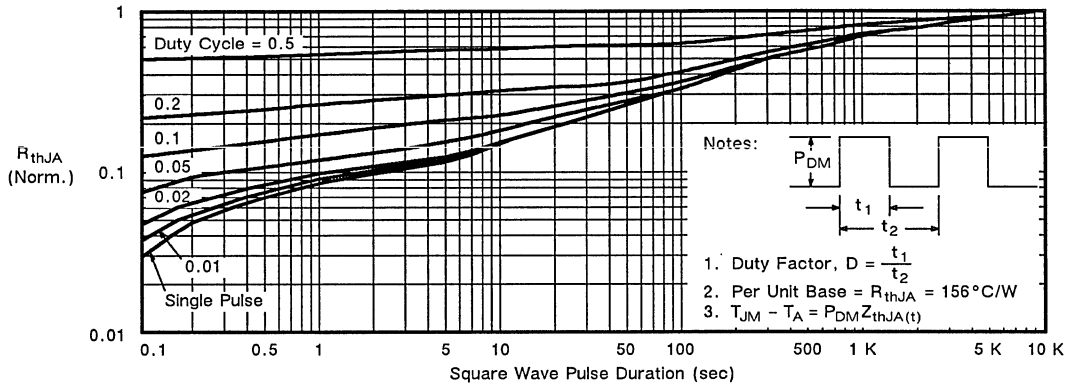


TYPICAL CHARACTERISTICS

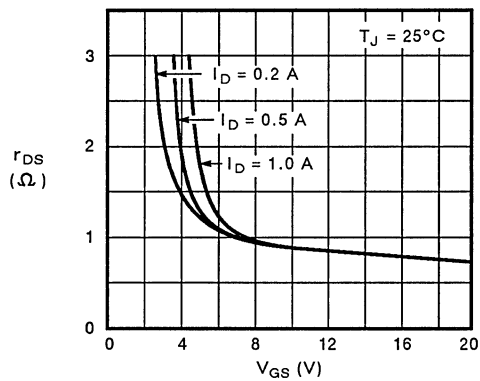


TYPICAL CHARACTERISTICS

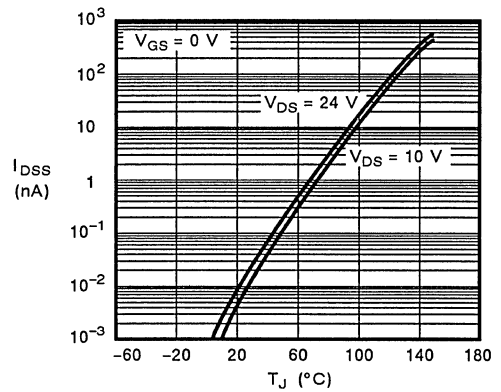
Normalized Effective Transient Thermal Impedance, Junction-to-Ambient (TO-92)



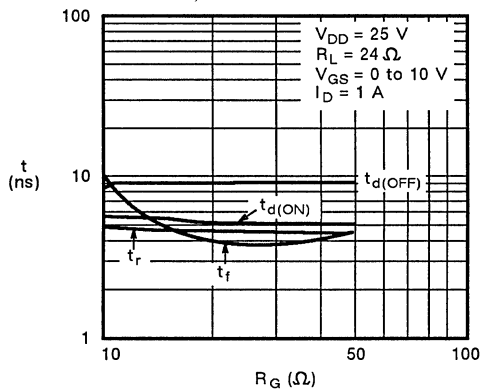
On-Resistance vs. Gate to Source Voltage



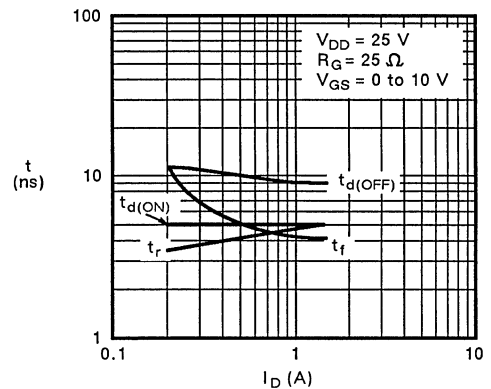
Off-State Current



Drive Resistance Effects on Switching



Load Condition Effects on Switching



TYPICAL CHARACTERISTICS

