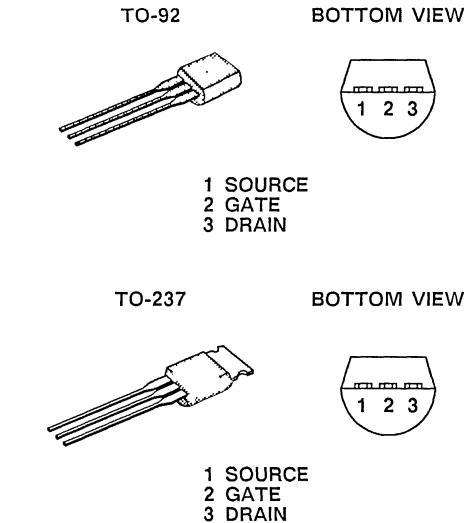


## PRODUCT SUMMARY

PART NUMBER	V <sub>(BR)DSS</sub> (V)	r <sub>DSON</sub> (Ω)	I <sub>D</sub> (A)	PACKAGE
VN1710L	170	10	0.17	TO-92
VN1710M	170	10	0.19	TO-237

Performance Curves: VNDB24 (See Section 7)



## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	VN1710L	VN1710M	UNITS
Drain-Source Voltage		V <sub>DS</sub>	170	170	V
Gate-Source Voltage		V <sub>GS</sub>	±30	±30	
Continuous Drain Current	T <sub>A</sub> = 25°C	I <sub>D</sub>	0.17	0.19	A
	T <sub>A</sub> = 100°C		0.11	0.12	
Pulsed Drain Current <sup>1</sup>		I <sub>DM</sub>	0.47	0.54	
Power Dissipation	T <sub>A</sub> = 25°C	P <sub>D</sub>	0.8	1.0	W
	T <sub>A</sub> = 100°C		0.32	0.4	
Operating Junction and Storage Temperature		T <sub>j</sub> , T <sub>stg</sub>	-55 to 150		°C
Lead Temperature (1/16" from case for 10 seconds)		T <sub>L</sub>	300		

## THERMAL RESISTANCE

THERMAL RESISTANCE		SYMBOL	VN1710L	VN1710M	UNITS
Junction-to-Ambient		R <sub>thJA</sub>	156	125	°C/W

<sup>1</sup>Pulse width limited by maximum junction temperature

# VN1710 SERIES

 Siliconix  
incorporated

ELECTRICAL CHARACTERISTICS <sup>1</sup>			LIMITS			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	VN1710		UNIT
				MIN	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}$ , $I_D = 100 \mu\text{A}$	230	170		V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 1 \text{ mA}$	1.4	0.8	2.0	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ $V_{GS} = \pm 15 \text{ V}$	$\pm 1$ $\pm 5$		$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 120 \text{ V}$ $V_{GS} = 0 \text{ V}$	0.01 1		10 500	
On-State Drain Current <sup>3</sup>	$I_{D(\text{ON})}$	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 10 \text{ V}$	1.2	1		A
Drain-Source On-Resistance <sup>3</sup>	$r_{DS(\text{ON})}$	$V_{GS} = 2.5 \text{ V}$ , $I_D = 0.1 \text{ A}$	8.5		10	$\Omega$
		$V_{GS} = 10 \text{ V}$ $I_D = 0.5 \text{ A}$	6.5 14		10 24.7	
		$T_J = 125^\circ\text{C}$				
Forward Transconductance <sup>3</sup>	$g_{FS}$	$V_{DS} = 10 \text{ V}$ , $I_D = 0.5 \text{ A}$	530	300		$\text{mS}$
Common Source Output Conductance <sup>3</sup>	$g_{OS}$	$V_{DS} = 7.5 \text{ V}$ , $I_D = 0.5 \text{ A}$	475			$\mu\text{s}$
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$	110		125	pF
Output Capacitance	$C_{oss}$		30		50	
Reverse Transfer Capacitance	$C_{rss}$		5		20	
<b>SWITCHING</b>						
Turn-On Time	$t_{d(\text{ON})}$	$V_{DD} = 60 \text{ V}$ , $R_L = 150 \Omega$ $I_D = 0.4 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ $R_G = 25 \Omega$ (Switching time is essentially independent of operating temperature)	3		8	ns
	$t_r$		2		8	
Turn-Off Time	$t_{d(\text{OFF})}$		13		23	
	$t_f$		9		34	

- NOTES: 1.  $T_A = 25^\circ\text{C}$  unless otherwise noted.  
 2. For design aid only, not subject to production testing.  
 3. Pulse test;  $PW = 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .