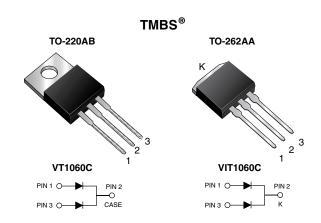




Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.39 \text{ V}$ at $I_F = 2.5 \text{ A}$



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 5.0 A				
V _{RRM}	60 V				
I _{FSM}	100 A				
V _F at I _F = 5.0 A	0.50 V				
T _J max.	150 °C				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses

High efficiency operation
 COMPLIANT HALOGEN

HAL

 Solder bath temperature 275 °C max. 10 s, per JESD 22-B106

- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VT1060C	VIT1060C	UNIT
Maximum repetitive peak reverse voltage		V_{RRM}	60		V
Maximum average forward rectified current (fig. 1)	per device		10		А
	per diode	I _{F(AV)}	5		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	100		А
Voltage rate of change (rated V _R)		dV/dt	10 000		V/µs
Operating junction and storage temperature ra	ange	T _J , T _{STG}	- 55 to	+ 150	°C

VT1060C, VIT1060C

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 2.5 A	T _A = 25 °C	V _F ⁽¹⁾	0.49	-	. V	
	I _F = 5.0 A			0.58	0.70		
	I _F = 2.5 A	T _A = 125 °C		0.39	-		
	I _F = 5.0 A			0.50	0.60		
Reverse current per diode	$V_R = 60 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	I _R ⁽²⁾	-	700	μΑ		
		T _A = 125 °C	'	6.6	25	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	VT1060C	VIT1060C	UNIT	
Typical thermal resistance	per diode	R _{θJC}	3.5		°C/W	
	per device		2.5			

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	VT1060C-M3/4W	1.87	4W	50/tube	Tube	
TO-262AA	VIT1060C-M3/4W	1.45	4W	50/tube	Tube	
TO-220AB	VT1060CHM3/4W (1)	1.87	4W	50/tube	Tube	
TO-262AA	VIT1060CHM3/4W (1)	1.45	4W	50/tube	Tube	

Note

RATINGS AND CHARACTERISTICS CURVES

 $(T_A = 25 \, ^{\circ}C \text{ unless otherwise noted})$

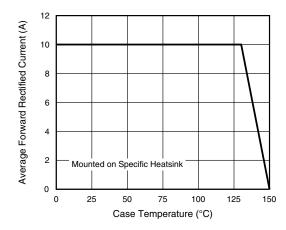


Fig. 1 - Maximum Forward Current Derating Curve

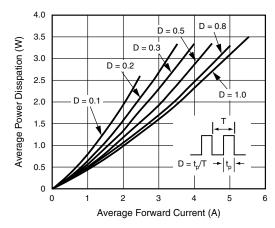


Fig. 2 - Forward Power Dissipation Characteristics

⁽¹⁾ AEC-Q101 qualified





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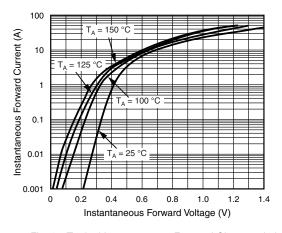


Fig. 3 - Typical Instantaneous Forward Characteristics

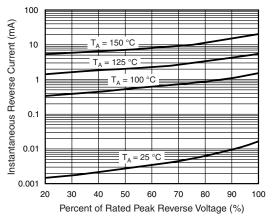


Fig. 4 - Typical Reverse Characteristics

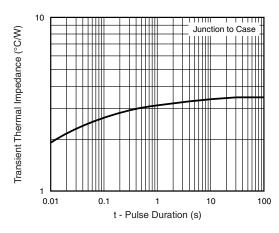


Fig. 5 - Typical Transient Thermal Impedance

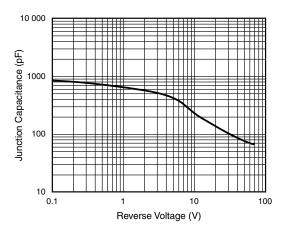


Fig. 6 - Typical Junction Capacitance

VT1060C, VIT1060C

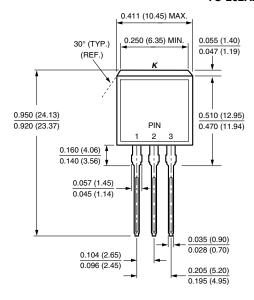
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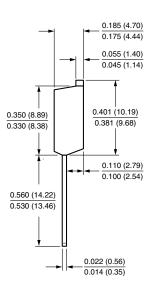


PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AB 0.415 (10.54) MAX. 0.185 (4.70) 0.370 (9.40) 0.154 (3.91) 0.175 (4.44) 0.360 (9.14) 0.148 (3.74) 0.055 (1.39) 0.113 (2.87) 0.045 (1.14) 0.103 (2.62) 0.145 (3.68) 0.135 (3.43) 0.603 (15.32) 0.635 (16.13) 0.625 (15.87) 0.573 (14.55) PIN 0.350 (8.89) 0.330 (8.38) 0.160 (4.06) 1.148 (29.16) 0.140 (3.56) 1.118 (28.40) 0.110 (2.79) 0.100 (2.54) 0.057 (1.45) 0.045 (1.14) 0.560 (14.22) 0.530 (13.46) 0.105 (2.67) 0.095 (2.41) 0.035 (0.90) 0.104 (2.65) 0.028 (0.70) 0.022 (0.56) 0.205 (5.20) 0.096 (2.45) 0.014 (0.36) 0.195 (4.95)

TO-262AA









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