**New Product** 

VFT3045CBP

Vishay General Semiconductor

# Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.30$  V at  $I_F = 5.0$  A

# **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- HALOGEN • Solder bath temperature 275 °C max. 10 s, per FREE JESD 22-B106
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

### **MECHANICAL DATA**

#### Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

### Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VFT3045CBP	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	per device	- I <sub>F(AV)</sub> <sup>(1)</sup>	30	A	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	200	A	
Isolation voltage from termal to heatsink, t = 1 min		V <sub>AC</sub>	1500	V	
Operating junction and storage temperature range		T <sub>OP</sub> , T <sub>STG</sub>	- 40 to + 150	°C	
Junction temperature in DC forward current without reverse bias, t $\leq$ 1 h		T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C	

Notes

<sup>(1)</sup> With heatsink

<sup>(2)</sup> Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

Document Number: 89369 For technical questions within your region, please contact one of the following: Revision: 27-Oct-10 DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com



RoHS COMPLIANT





**TMBS**<sup>®</sup>

ITO-220AB

# PIN 2

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	200 A			
$V_F$ at $I_F = 15$ A	0.39 V			
T <sub>OP</sub> max.	150 °C			

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.42	-	V	
	I <sub>F</sub> = 7.5 A			0.44	-		
	I <sub>F</sub> = 15 A			0.49	0.57		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.30	-		
	I <sub>F</sub> = 7.5 A			0.33	-		
	I <sub>F</sub> = 15 A			0.39	0.48		
Reverse current per diode	V <sub>B</sub> = 45 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	2000	μA	
	v <sub>R</sub> = 43 v	T <sub>A</sub> = 125 °C		17	50	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	SYMBOL VFT3045CBP		
Typical thermal resistance	per diode	· R <sub>θJC</sub>	6.0	°C/W	
	per device		4.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N UNIT WEIGHT (g) PACK		PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT3045CBP-M3/4W	1.76	4W	50/tube	Tube	

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

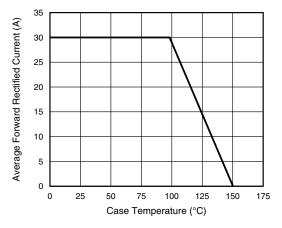


Fig. 1 - Maximum Forward Current Derating Curve

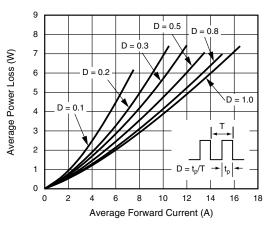


Fig. 2 - Forward Power Loss Characteristics Per Diode



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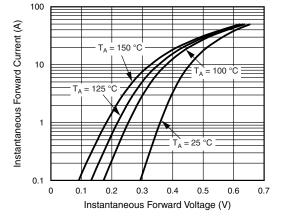


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

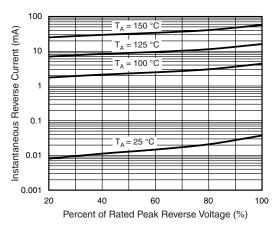


Fig. 4 - Typical Reverse Characteristics Per Diode

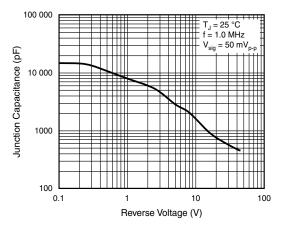


Fig. 5 - Typical Junction Capacitance Per Diode

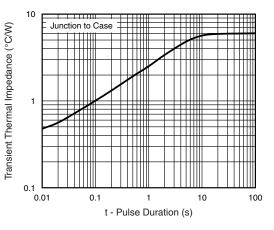
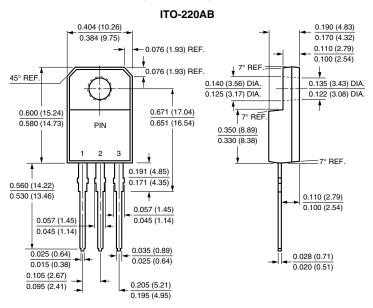


Fig. 6 - Typical Transient Thermal Impedance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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