



Vishay General Semiconductor

Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.33$ V at $I_F = 5.0$ A





PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 10 A				
V _{RRM}	45 V				
I _{FSM}	160 A				
V _F at I _F = 10 A	0.41 V				
T _{OP} max.	150 °C				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation

 Solder bath temperature 275 °C max. 10 s, per JESD 22-B106



- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	VFT2045CBP	UNIT		
Maximum repetitive peak reverse voltage		V_{RRM}	45	V		
Maximum average forward rectified current (fig. 1)	per device	- I _{F(AV)} ⁽¹⁾	20	А		
	per diode		10			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	160	А		
Isolation voltage from termal to heatsink, t = 1 min		V _{AC}	1500	V		
Operating junction and storage temperature range		T _{OP} , T _{STG}	- 40 to + 150	°C		
Junction temperature in DC forward current without reverse bias, $t \le 1 \text{ h}$		T _J ⁽²⁾	≤ 200	°C		

Notes

(1) With heatsink

(2) Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

VFT2045CBP

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.44	-	- V	
	I _F = 10 A			0.49	0.58		
	I _F = 5 A	- T _A = 125 °C		0.33	-		
	I _F = 10 A			0.41	0.52		
Reverse current per diode	$V_R = 45 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	T _A = 25 °C	I _R ⁽²⁾	-	2000	μΑ	
		IR ^(−)	10	30	mA		

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VFT2045CBP	UNIT		
Typical thermal resistance	per diode	$R_{ hetaJC}$	6.0	°C/W	
	per device		4.5		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT2045CBP-M3/4W	1.76	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

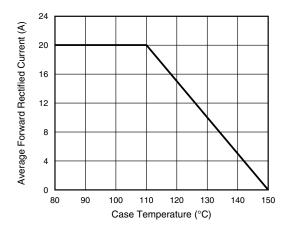


Fig. 1 - Maximum Forward Current Derating Curve

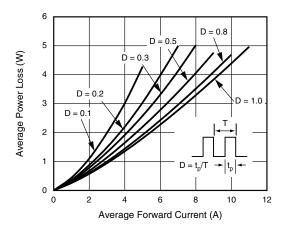


Fig. 2 - Forward Power Loss Characteristics Per Diode





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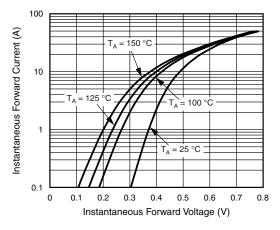


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

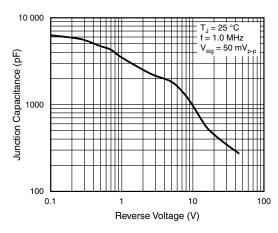


Fig. 5 - Typical Junction Capacitance Per Diode

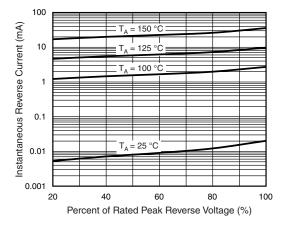


Fig. 4 - Typical Reverse Characteristics Per Diode

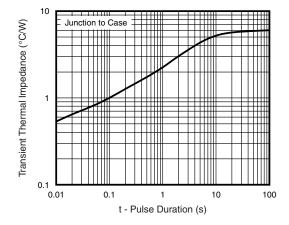
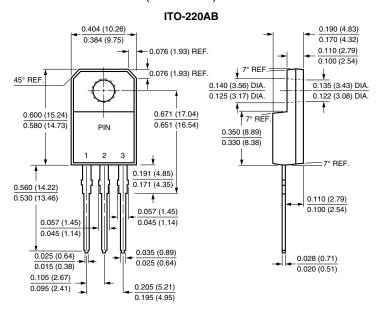


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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