

#### Characteristic:

Isolation measurement, isolation withstand voltage up to 3kv@50HZ, 1min can measure 1~60A AC and DC current

- ≻5V/3.3V power supply selection
- ≻SOIC8 package
- Extremely low current lead impedance: 0.8m
- Support Viout Vref differential output mode
- Zero voltage hysteresis close to 0
- ➢Response time as low as 5uS

➢Wide operating temperature range: -40~125°C/-55~ 125°C for selection

High accuracy: <1% accuracy error at normal temperature

Accuracy error <3% in the whole temperature range

Strong driving capability, load as low as 2.2k

Extremely easy-to-use peripheral circuits

Supports fully automatic wave soldering and tape and reel packaging

Not affected by wire magnetic fields, external magnetic fields, and geomagnetic fields. High power supply rejection ratio.

Independent research and development, no technology dependence

#### Product appearance picture:



#### Typical application diagram:



#### Overview:

VCS712 is a member of Wakeway's fully integrated Hall curren t sensor product line. The main differential output mode is conv enient for downstream applications. Its ultra-wide dynamic dete ction capability supports customers to detect measured current ranges as low as 1A and as high as 50A. It allows users to det ect load current conditions under the condition of insulation isol ation, and is suitable for replacing power resistors, linear optoc ouplers, transformers and other passive or discrete sensor det ection solutions.

The VCS712 series is an isolated current detection chip that w orks on the open-loop Hall sensor detection principle. By introd ucing the high-voltage side current wire into the package, base d on the magnetic effect of the current, the equal-proportional magnetic field generated around the measured wire is sensed by the magnetic sensor of the built-in chip and converted into a processable equal-proportional voltage signal. This voltage sig nal After built-in high-precision ADC read amplification, combin ed with digital calibration technology, environmental variables s uch as temperature, noise, hysteresis, nonlinearity, etc. are re moved, and the final output voltage value is close to the ideal tr ansformation ratio to the measured current value, achieving iso lation. current measurement.

VCS712 adopts fully automatic production and processing, whi ch can bring customers consistency, high quality, high reliabilit y and low cost that cannot be matched by discrete solutions. T he standard package design is very suitable for customers to c arry out batch automatic patch production. It is the best solutio n for power device current detection, power supply, load detect ion and other applications. We are committed to researching c ore chip technology to bring optimal current detection to custo mers. Solutions are the purpose.





## Selection table:

| Model          | Temp<br>Range(°C)  | Detect<br>current(A) | Sensitivity<br>(mV/A) | Zero<br>output(V) | Rated<br>output(V) | Special<br>Code | Voltage<br>Reference<br>(V) |
|----------------|--------------------|----------------------|-----------------------|-------------------|--------------------|-----------------|-----------------------------|
| VCS712I-010B5F |                    | ±10                  | 200                   | B(0.5Vcc)         |                    |                 |                             |
| VCS712I-020B5F |                    | ±20                  | 100                   |                   |                    |                 |                             |
| VCS712I-025B5F |                    | ±25                  | 80                    |                   | 0                  |                 |                             |
| VCS712I-030B5F |                    | ±30                  | 66                    |                   | 2                  |                 |                             |
| VCS712I-040B5F |                    | ±40                  | 50                    |                   |                    |                 |                             |
| VCS712I-050B5F |                    | ±50                  | 40                    |                   |                    | F               | NC                          |
| VCS712I-010U5F |                    | 10                   | 400                   |                   |                    |                 |                             |
| VCS712I-020U5F |                    | 20                   | 200                   |                   |                    |                 |                             |
| VCS712I-030U5F |                    | 30                   | 133                   | U(0.1Vcc)         | 4                  |                 |                             |
| VCS712I-040U5F |                    | 40                   | 100                   |                   |                    |                 |                             |
| VCS712I-050U5F |                    | 50                   | 80                    |                   |                    |                 |                             |
| VCS712I-010E5D |                    | ±10                  | 200                   |                   |                    |                 |                             |
| VCS712I-020E5D |                    | ±20                  | 100                   | 59                | 2                  | D               | 2.5                         |
| VCS712I-025E5D | <b>(-40~125℃</b> ) | ±25                  | 80                    |                   |                    |                 |                             |
| VCS712I-030E5D |                    | ±30                  | 66                    | E(2.5)            |                    |                 |                             |
| VCS712I-040E5D |                    | ±40                  | 50                    |                   |                    |                 |                             |
| VCS712I-050E5D |                    | ±50                  | 40                    |                   |                    |                 |                             |
| VCS712I-010B3F |                    | ±10                  | 132                   |                   |                    |                 |                             |
| VCS712I-020B3F |                    | ±20                  | 66                    |                   | 1.32               |                 |                             |
| VCS712I-030B3F |                    | ±30                  | 44                    | B(0.5Vcc)         |                    |                 |                             |
| VCS712I-040B3F |                    | ±40                  | 33                    |                   |                    |                 |                             |
| VCS712I-050B3F |                    | ±50                  | 26.4                  |                   |                    | F               | NO                          |
| VCS712I-010U3F |                    | 10                   | 264                   |                   |                    | F               | NC                          |
| VCS712I-020U3F |                    | 20                   | 132                   | U(0.1Vcc)         | 2.64               |                 |                             |
| VCS712I-030U3F |                    | 30                   | 88                    |                   |                    |                 |                             |
| VCS712I-040U3F |                    | 40                   | 66                    |                   |                    |                 |                             |
| VCS712I-050U3F |                    | 50                   | 52.8                  |                   |                    |                 |                             |



| E  | E When there is no current in the IP, VIOUT@0A=VREF=2.5V, which is suitable for bidirectio nal current detection. The zero point and sensitivity do not change with the VCC ratio. |  |  |  |  |  |
|--|--|--|--|--|--|--|
| В  | B When there is no current in the IP, VIOUT@0A=0.5VCC, which is suitable for bidirectional current detection. The zero point and sensitivity change with the VCC ratio.            |  |  |  |  |  |
| U  | When there is no current in the IP, VIOUT@0A=0.1VCC, which is suitable for unidirectional current detection. The zero point and sensitivity change with the VCC ratio.             |  |  |  |  |  |
| D Differential mode, the 6th pin of the chip is the VREF pin, which is suitable for typical circuit and differential circuit design. The differential circuit can effectively reduce environmental and temperature interference. |  |  |  |  |  |  |
| F Follow-up mode, the 6th pin of the chip is NC pin, no special definition, suitable for typical circuit design  |  |  |  |  |  |  |
| Remark 1: When selecting feature code F, the default 0A output is B/U. When selecting feature code D,  |  |  |  |  |  |  |
| the default 0A output is E.  |  |  |  |  |  |  |
| Remark 2: The sensitivity is equal to the rated output/rated current.  |  |  |  |  |  |  |

Naming rules:





Pin definition:



| Pin number | Pin name | Describe  |  |  |
|------------|----------|---|--|--|
| 1/2        | IP+      | Primary current input positive terminal, supports only connecting 1 or 2  |  |  |
| 3/4        | IP-      | The negative terminal of the primary current output supports only connecting 3 or 4                                 |  |  |
| 5          | GND      | Weak current GND isolated from primary current lines  |  |  |
| 6          | NC       | Undefined, left empty   |  |  |
| 6 VREF     |          | Reference terminal, supports input and output, VIOUT=Vref (when IP=0A), can be used for differential circuit design |  |  |
| 7          | VIOUT    | The output voltage is equal to the primary current,<br>and IP+VIOUT in the same direction=IP*sensitivity+VIOUT (0A) |  |  |
| 8          | VCC      | Chip supply voltage   |  |  |

## Limit parameters:

| Characteristic   | Symbol   | Max  | Unit | Remark  |
|--|----------|------|------|---|
| Voltage  | Vcc      | 6    | V    |   |
| Output voltage   | VIOUT    | 6    | V    |   |
| Max junction temperature   | TJ (max) | 165  | °C   |   |
| 1 minute isolation withstand<br>voltage test (50Hz)                                  | VISO     | 3000 | VRMS |   |
| Under ambient temperature<br>conditions,Continuously load the<br>maximum IP value    | IPmax    | 40   | A    | It is directly related to the heat dissipation<br>capacity of PCB. This data relies on WKW demo<br>test board                                     |
| Under ambient temperature<br>conditions,Transient Overload<br>IP Line End Capability | IPover   | 80   | А    | It is directly related to the heat dissipation capacity of<br>PCBdemo. This data relies on<br>Yu Weikewei's test board 1pulse100ms, 1% duty cycle |



### Electrical performance parameters:

| Characteristic                     | Symbol     | Min | Typical value | Max | Unit | Remark/Condition                                 |
|------------------------------------|------------|-----|---------------|-----|------|--|
| Supply voltage                     | Vcc        | 4.5 | 5             | 5.5 | V    |  |
| Supply current                     | ICC        |     | 20            |     | mA   |  |
| Primary current<br>impedance       | RP         |     | 0.8           |     | mΩ   |  |
| Reference voltage                  | VREF       |     | 2.5           |     | V    | Special CodeD                                    |
|                                    |            |     | 2.5           |     | V    | E,See selection table for details                |
| Zero output                        | Voq        |     | 0.5vcc        |     | V    | B,See selection table for details                |
|                                    |            |     | 0.1vcc        |     | V    | U,See selection table for details                |
|                                    |            |     | 2             |     | V    | The suffix isB5F/E5F                             |
| Rated output                       | VFS        |     | 4             |     | V    | The suffix isU5F,See selection table for details |
| (Viout–Vref)                       |            |     | 1.32          |     | V    | The suffix isB3F,See selection table for details |
| Sensitivity                        | Sens       |     | VFS/IPR       |     | mV/A | See selection table for details                  |
| Rated current                      | IPR        | 1   |               | 60  | A    | See selection table for details                  |
| Zero drift                         | YD         | -3  | ±1            | 3   | %    |  |
| Thermal zero drift                 | $\delta$ T | -1  | ±0.5          | 1   | %    |  |
| Ripple                             | Voq_pp     |     |               | 100 | mV   |  |
| Response time                      | tresponse  |     |               | 10  | uS   |  |
| Bandwidth                          | f          |     | 100           |     | kHz  |  |
| Linearity                          | ELIN       |     | ±1            |     | %    |  |
| Accuracy                           | ACC        |     | ±1            |     | %    | <b>25</b> ℃                                      |
| Full temperature<br>range accuracy | ACC        | -3  |               | 3   | %    | See selection table for full temperature range   |

## Application circuit:

Introduction: The design of Typical application circuit is simple and convenient, and is generally used in environments with small temperature changes and external electromagnetic interference; differential application circuits have good anti-interference properties and are suitable for environments with large temperature changes and external electromagnetic interference; Notice:

 The output capacitance at the VIOUT terminal can be adjusted according to frequency and ripple requirements (the larger the capacitance, the lower the ripple and frequency)
 When designing, it is necessary to consider whether the sensor Output voltage is within the ADC acquisition range.ÿ



• Typical application circuit



- Differential application circuit
- 1 Hardware differential acquisition circuit



2 Software differential acquisition circuit





# VCS712

Small size - wide temperature range current sensor

Package information:





|  | SIDE | VIEW |
|--|------|------|
|--|------|------|

| Symbol | Dimensions I | n Millimeters | Dimensions In Inches |       |  |
|--------|--------------|---------------|----------------------|-------|--|
|        | Min          | Max           | Min                  | Max   |  |
| A      | 1.350        | 1.750         | 0.053                | 0.069 |  |
| A1     | 0.100        | 0.250         | 0.004                | 0.010 |  |
| A2     | 1.350        | 1.550         | 0.053                | 0.061 |  |
| D      | 4.700        | 5.100         | 0.185                | 0.201 |  |
| E1     | 3.800        | 4.000         | 0.150                | 0.157 |  |
| E      | 5.800        | 6.200         | 0.228                | 0.244 |  |
| b      | 0.330        | 0.510         | 0.013                | 0.020 |  |
| С      | 0.170        | 0.250         | 0.007                | 0.010 |  |
| е      | 1.270(BSC)   |               | 0.050                | (BSC) |  |
| L      | 0.400        | 1.270         | 0.016                | 0.050 |  |
| θ      | 0            | 8             | 0                    | 8     |  |



Wiring reference:

The VCS712 model current sensor is packaged in SOIP-8 and has the characteristics of small size and large measurement range, so its PCB wiring design is particularly important. The following suggestions are

| Continuous<br>current | Cabling recommendations  | Arrangement |
|-----------------------|--|-------------|
| < 10A                 | <ul> <li>Standard SOIP-8 package wiring can be u sed, and the pin end should</li> <li>Full solder coverage;</li> <li>If window wiring design is adopted, tempe rature drift can be reduced;</li> </ul>   | II          |
| ≥10A                  | <ul> <li>Adopt the wiring in the figure below, and ad opt the window design on the IP end surface;</li> <li>The surface of the window is covered with solder, and the pin end should be fully covere d with solder;</li> </ul>   |             |
| ≥30A                  | <ul> <li>Adopt the wiring in the figure below, and adopt the through-hole heat dissipation design with windows on the surface of the IP end;</li> <li>The window surface is covered with solder H&gt;0 .5mm, and the pin end should be fully covered with solder;</li> <li>Copper foil thickness is recommended to be ≥ 4 ounces or use a multi-layer over-current design</li> </ul> | -dI         |

Note: If the temperature of the current sensor rises above 165°C for more than 1 minute, th e current sensor package may be cracked or damaged due to internal heat shrinkage react ion. 165°C temperature standard reference packaging material properties



## Document modification record:

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