

N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (mA)			
60	2 at V _{GS} = 10 V	300			

FEATURES Halogen-free According to IEC 61249-2-21 Definition

- Low On-Resistance: 2 Ω
- Low Threshold: 2 V (typ.)
- Low Input Capacitance: 25 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- TrenchFET[®] Power MOSFET
- 1200V ESD Protection
- Compliant to RoHS Directive 2002/95/EC

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- · Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted									
Parameter		Symbol	Limit	Unit					
Drain-Source Voltage		V _{DS}	60	V					
Gate-Source Voltage		V _{GS}	± 20						
Continuous Durin Current (T. 150 °C)	T _A = 25 °C	- I _D	300	mA					
Continuous Drain Current (T _J = 150 °C) ^b	T _A = 100 °C		230						
Pulsed Drain Current ^a		I _{DM}	1200						
Deven Dissinguise	T _A = 25 °C	D_	0.35	10/					
Power Dissipation ^b	T _A = 100 °C	PD	0.14	W					
Maximum Junction-to-Ambient ^b		R _{thJA}	350	°C/W					
Operating Junction and Storage Temperature Range		T _{J,} T _{stg}	- 55 to 150	°C					

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 board.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

Top View

то-92



COMPLIANT HALOGEN

FREE





Parameter		Test Conditions	Limits			
	Symbol		Min.	Typ. ^a	Max.	Unit
Static				•	•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 10 \mu A$	60			- V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1		2.5	
Gate-Body Leakage		$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 10	μA
		$V_{DS} = 0 V, V_{GS} = \pm 15 V$			1	
	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 150	nA
		$V_{DS} = 0 V, V_{GS} = \pm 10 V, T_{J} = 85 °C$			± 1000	
		$V_{DS} = 0 V, V_{GS} = \pm 5 V$			± 100	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
	I _{DSS}	V_{DS} = 60 V, V_{GS} = 0 V , T_{J} = 125 °C			500	
On-State Drain Current ^a	I _{D(on)}	V_{GS} = 10 V, V_{DS} = 7.5 V	800			mA
		V_{GS} = 4.5 V, V_{DS} = 10 V	500			
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 400 mA		2		Ω
		V_{GS} = 4.5 V, I _D = 200 mA		4		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 200 mA	100			mS
Diode Forward Voltage	V _{SD}	I _S = 200 mA, V _{GS} = 0 V			1.3	V
Dynamic ^a						
Total Gate Charge	Qg	V_{DS} = 10 V, V_{GS} = 4.5 V I _D \cong 250 mA		0.4	0.6	nC
Input Capacitance	C _{iss}			30		pF
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}$		6		
Reverse Transfer Capacitance	C _{rss}	f = 1 MHz		2.5		
Switching ^{a, b, c}			<u> </u>			
Turn-On Time	t _{d(on)}	V_{DD} = 30 V, R _L = 150 Ω			25	ns
Turn-Off Time	t _{d(off)}	$I_D \cong 200 \text{ mA}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{G}} = 10 \Omega$		1	35	

Notes:

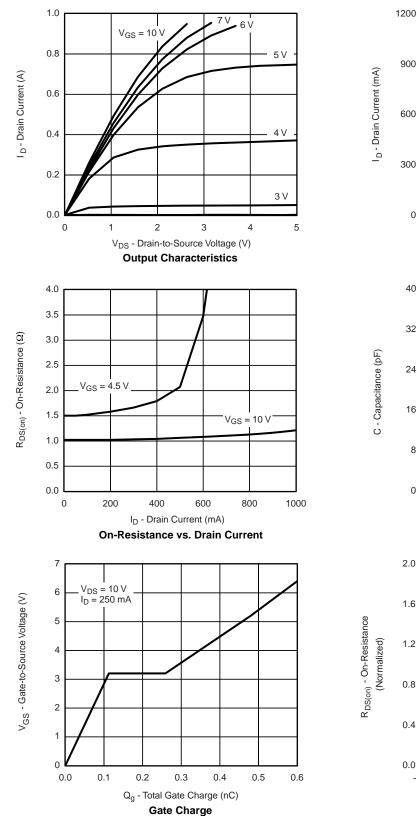
a. For DESIGN AID ONLY, not subject to production testing.

b. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.

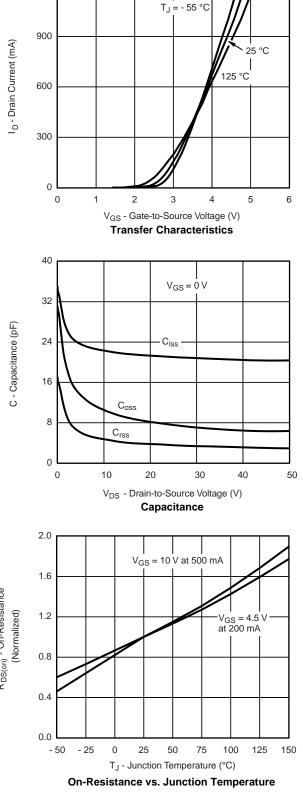
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





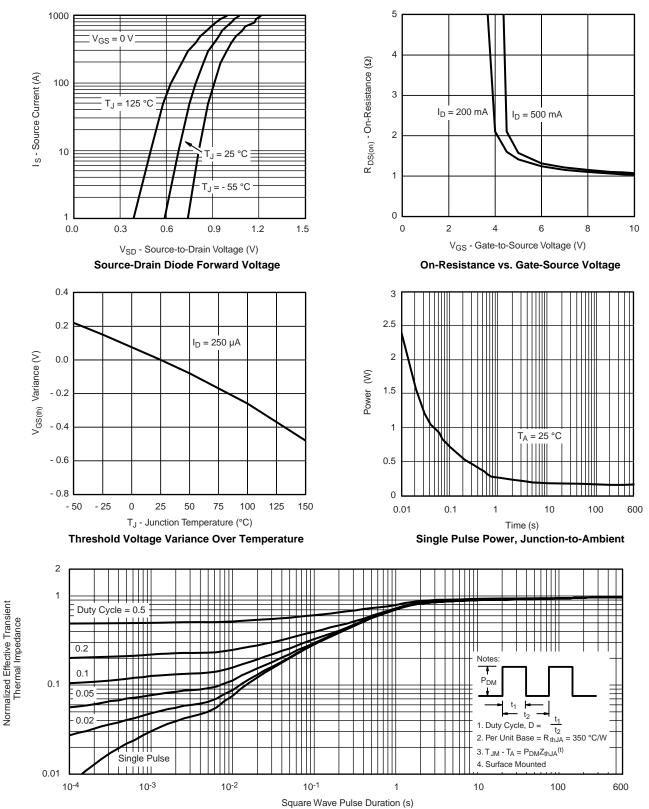
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



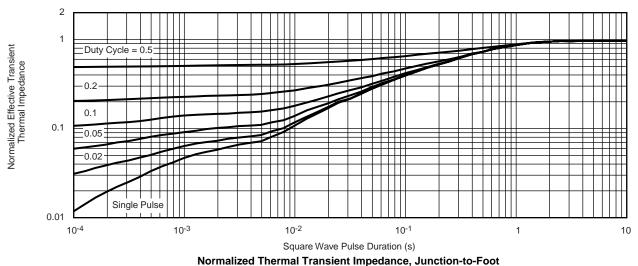
服务热线:400-655-8788



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



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