New Product

VBT6045CBP

Vishay General Semiconductor

Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.33$ V at $I_F = 10$ A

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C COMPLIANT
- Not recommended for PCB bottom side wave mounting
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT6045CBP	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	60	A	
	per diode		30		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	320	А	
Operating junction and storage temperature range		T _{OP} , T _{STG}	- 40 to + 150	°C	
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$		T _J ⁽²⁾	≤ 200	°C	

Notes

⁽¹⁾ With heatsink

⁽²⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

VBT6045CBP к PIN 1 O 0 HEATSINK

TMBS[®]

TO-263AB

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V _{RRM}	45 V			
I _{FSM}	320 A			
V_F at $I_F = 30$ A	0.47 V			
T _{OP} max.	150 °C			







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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 10 A	T _A = 25 °C	- V _F ⁽¹⁾	0.44	-	V	
	I _F = 15 A			0.47	-		
	I _F = 30 A			0.54	0.64		
	I _F = 10 A	T _A = 125 °C		0.33	-		
	I _F = 15 A			0.37	-		
	I _F = 30 A			0.47	0.56		
Reverse current per diode	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	3000	μA	
	v _R = 45 v	T _A = 125 °C		18	50	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	IBOL VBT6045CBP		
Typical thermal resistance	per diode	· R _{θJC}	1.5	°C/W	
Typical inernal resistance	per device		0.8		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT6045CBP-E3/4W	1.38	4W	50/tube	Tube	
TO-263AB	VBT6045CBP-E3/8W	1.38	8W	800/reel	Tape and reel	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

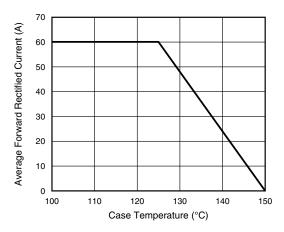


Fig. 1 - Maximum Forward Current Derating Curve

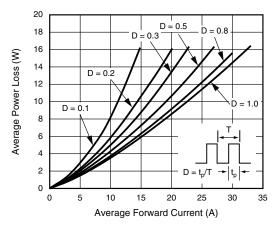


Fig. 2 - Forward Power Loss Characteristics Per Diode



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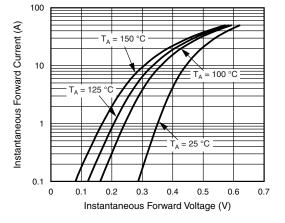


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

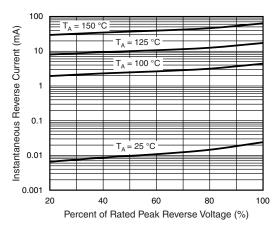


Fig. 4 - Typical Reverse Characteristics Per Diode

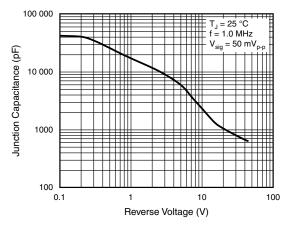


Fig. 5 - Typical Junction Capacitance Per Diode

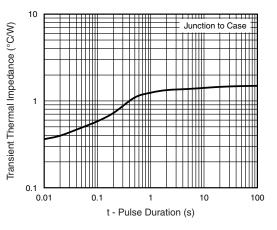
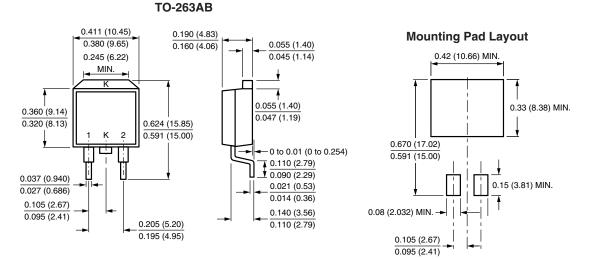


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com



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