

# P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)		
- 60	0.064 at V <sub>GS</sub> = - 10 V	- 30	12		
	0.077 at V <sub>GS</sub> = - 4.5 V	- 28	12		

#### **FEATURES**

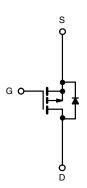
- TrenchFET® Power MOSFET
- 100 % UIS Tested

#### **APPLICATIONS**

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 2$	25 °C, unless othe	rwise noted		
Parameter	Symbol	Limit	Unit	
Gate-Source Voltage	V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	L	- 30	
Continuous Diain Curient (1) = 175 C)	T <sub>C</sub> = 100 °C	l <sub>D</sub>	- 20	
Pulsed Drain Current	I <sub>DM</sub>	- 90	Α	
Continuing Source Current (Diode Conduction)	I <sub>S</sub>	- 28		
Avalanche Current	I <sub>AS</sub>	- 31		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	7.2	mJ
Maximum Pawar Dissination	T <sub>C</sub> = 25 °C	P <sub>D</sub>	60 <sup>a</sup>	W
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	r D	6 <sup>b</sup>	] vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunding to Ambient	t ≤ 10 sec	R <sub>thJA</sub>	20	25	°C/W	
Junction-to-Ambient <sup>D</sup>	Steady State		62	75		
Junction-to-Case		R <sub>thJC</sub>	5	6		

#### Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.



<b>SPECIFICATIONS</b> $T_J = 25$	°C, unless	otherwise noted					
Parameter	Symbol	Test Conditions	Min	Typ <sup>a</sup>	Max	Unit	
Static							
Drain-Source Breakdown Voltage V <sub>(BR)D</sub>		$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 60			v	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0	ľ	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50		
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			- 150		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 10			Α	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A		0.064		Ω	
D : 0	r	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 125 °C		0.110			
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 175 °C		0.250			
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A		0.077			
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A		8		S	
Dynamic	•						
Input Capacitance	C <sub>iss</sub>			1000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		210			
Reverse Transfer Capacitance	C <sub>rss</sub>			110			
Total Gate Charge	$Q_g$			12.5	19		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3		nC	
Gate-Drain Charge	Q <sub>gd</sub>			3.2		7	
Gate Resistance	$R_{g}$	f = 1 MHz		8.0		Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			5	10		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 3.57 \Omega$		14	25		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong -8.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		15	25	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	] [		7	12	1	
Source-Drain Diode Ratings and Cha	racteristics	(T <sub>C</sub> = 25 °C) <sup>b</sup>		•		•	
Pulsed Current	I <sub>SM</sub>				- 30	Α	
Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>F</sub> = - 2 A, V <sub>GS</sub> = 0 V		- 0.9	- 1.3	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 8 A, di/dt = 100 A/μs		50	80	ns	
Reverse Recovery Time	Q <sub>rr</sub>	- 1F = - 6 A, αι/αι = 100 A/μS		80	120	nC	

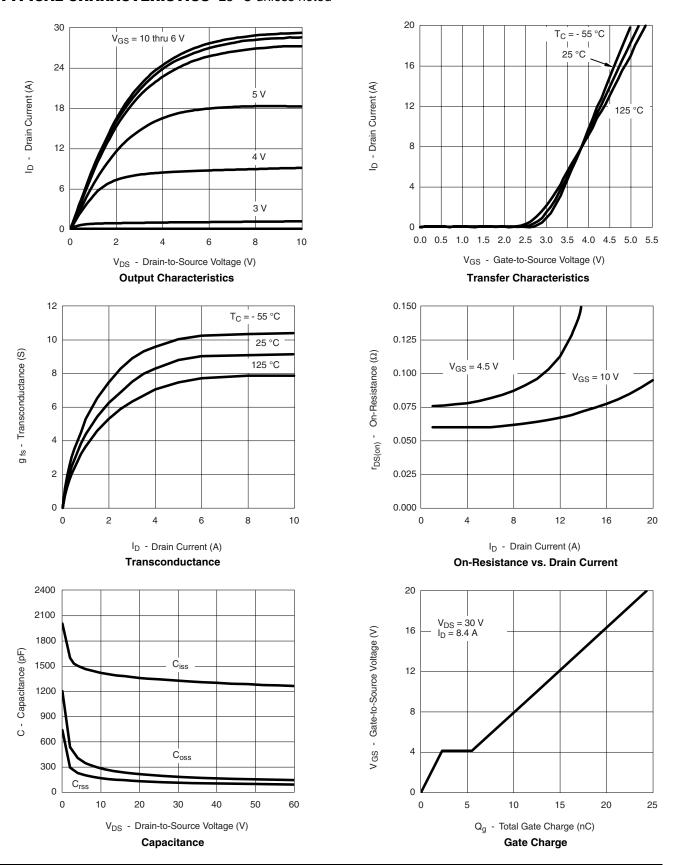
### Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

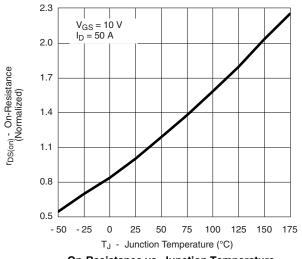


#### TYPICAL CHARACTERISTICS 25 °C unless noted

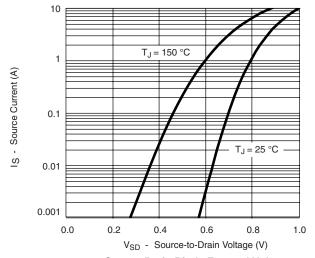




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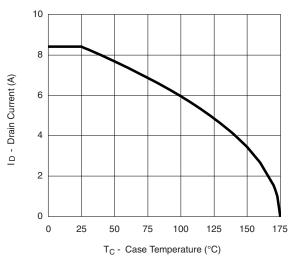




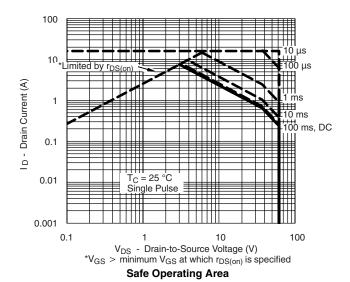


Source-Drain Diode Forward Voltage

#### THERMAL RATINGS

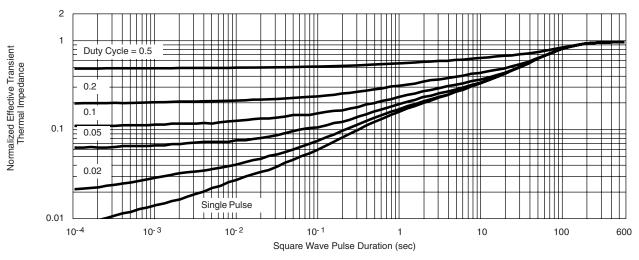


**Drain Current vs. Case Temperature** 

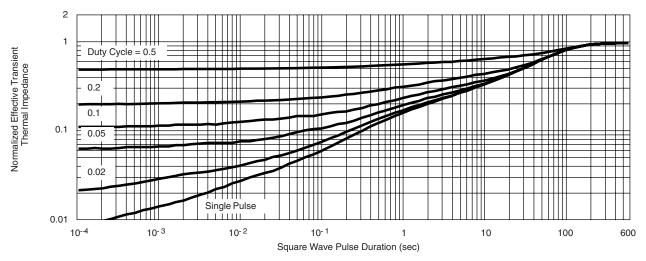




#### THERMAL RATINGS



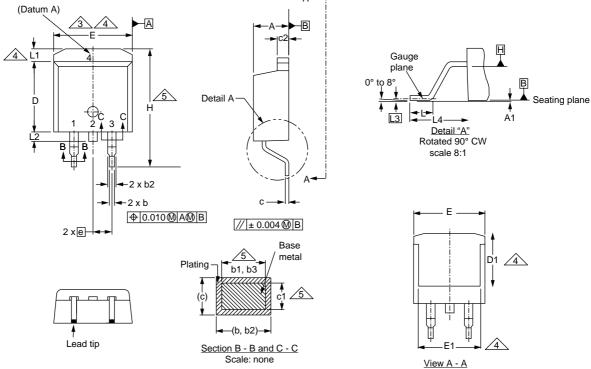
#### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



#### **TO-263AB**



	MILLIN	METERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.25	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	
c2	1.14	1.65	0.045	0.065	
D	8.38	9.65	0.330	0.380	
ECN: S-82110-Rev. A, 15-Sep-08					

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
D1	6.86	-	0.270	-
Е	9.65	10.67	0.380	0.420
E1	6.22	-	0.245	-
е	2.54 BSC		0.100 BSC	
Н	14.61	15.88	0.575	0.625
L	1.78	2.79	0.070	0.110
L1	-	1.65	-	0.066
L2	-	1.78	-	0.070
L3	0.25 BSC		0.010 BSC	
L4	4.78	5.28	0.188	0.208
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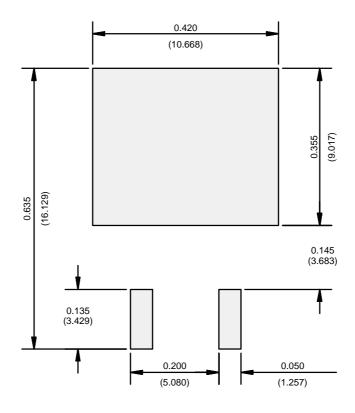
## Notes

DWG: 5970

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
- 4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
- 5. Dimension b1 and c1 apply to base metal only.
- 6. Datum A and B to be determined at datum plane H.
- 7. Outline conforms to JEDEC outline to TO-263AB.



## RECOMMENDED MINIMUM PADS FOR D2PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

服务热线:400-655-8788 7



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