

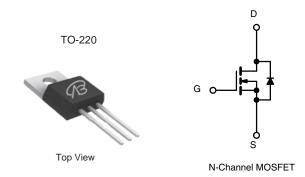
N-Channel 250 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A)	Q _g (TYP.)			
250	0.016at V _{GS} = 10 V	80	68nC			

FEATURES

- SGT technology Power MOSFET
- 100 % R_q and UIS tested
- Maximum 150°C junction temperature





APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V _{DS}	250	V			
Gate-Source Voltage	V _{GS} ± 20		V			
Ocalia de Buria Ocarda (T., 150.00)	T _C = 25 °C		80			
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C	─ I _D	64			
Pulsed Drain Current (t = 100 μs)	I _{DM}	240	A			
Avalanche Current	L = 0.5 mH	I _{AS}	80			
Single Avalanche Energy ^a	L = 0.5 IIII	E _{AS}	890	mJ		
Movimum Dower Dissination 8	T _C = 25 °C	В	300 ^b	w		
Maximum Power Dissipation ^a	T _C = 100 °C	P _D	150 ^b			
Operating Junction and Storage Temperature I	Range	T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)	R _{thJC}	0.5] C/W		

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	250	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_D=250\;\mu A$	2.5	-	4.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA	
		V _{DS} = 200 V, V _{GS} = 0 V	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 200 V, V_{GS} = 0 V, T_J = 125 °C	-	-	150	μA	
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 150°C	-	-	5	mA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	90	-	-	Α	
Duain Calumas On State Desistance 2		V _{GS} = 10 V, I _D = 30 A	-	0.013	-	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 30 A	-	0.018	-	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	-	75	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 230 V, f = 1 MHz	-	2300	-	pF	
Output Capacitance	C _{oss}		-	246	-		
Reverse Transfer Capacitance	C _{rss}		-	21	-		
Total Gate Charge ^c	Qg		-	73	96	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 230 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	-	16.7	-		
Gate-Drain Charge ^c	Q_{gd}		-	16.9	-		
Gate Resistance	R_g	f = 1 MHz	1.5	3	6	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	11	23		
Rise Time ^c	t _r	V_{DD} = 230 V, R_L = 1.66 Ω	-	15	35	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 60 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	35	40		
Fall Time ^c	t _f		-	20	30		
Drain-Source Body Diode Ratings at	nd Characteris	stics ^b (T _C = 25 °C)					
Pulsed Current (t = 100 μs)	I _{SM}		-	-	240	Α	
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V	-	0.8	1.2	٧	
Reverse Recovery Time	t _{rr}		-	35	-	ns	
Peak Reverse Recovery Charge	I _{RM(REC)}	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	11	20	Α	
Reverse Recovery Charge	Q _{rr}		-	0.9	1.8	μC	

Notes

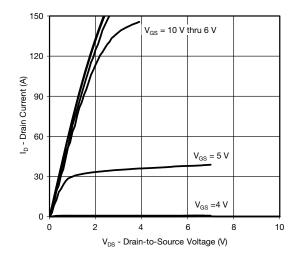
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- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

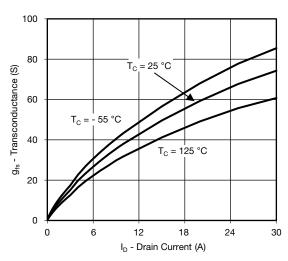
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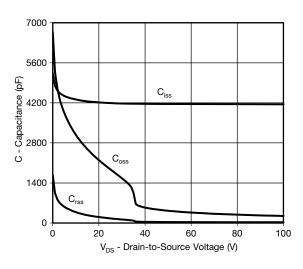
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



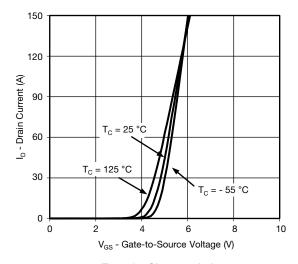
Output Characteristics



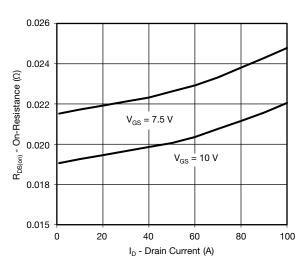
Transconductance



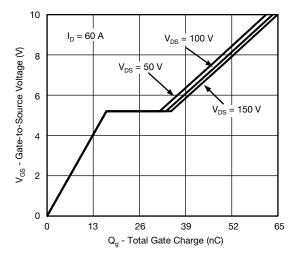
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current

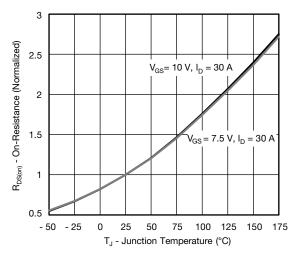


Gate Charge

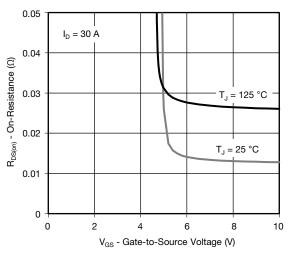
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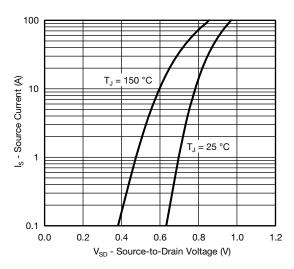
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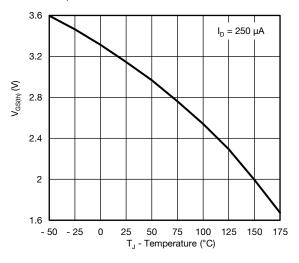
On-Resistance vs. Junction Temperature



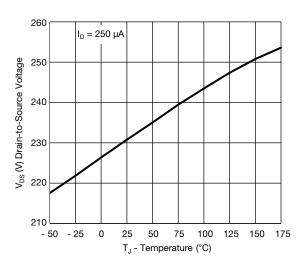
On-Resistance vs. Gate-to-Source Voltage



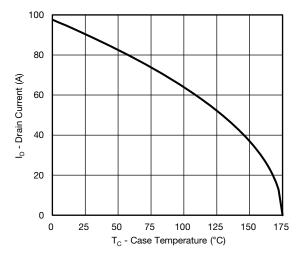
Source Drain Diode Forward Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

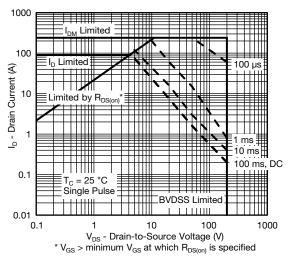


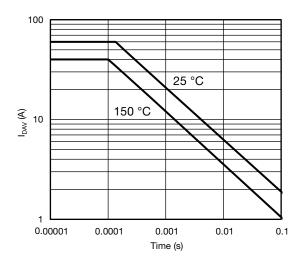
Current De-rating

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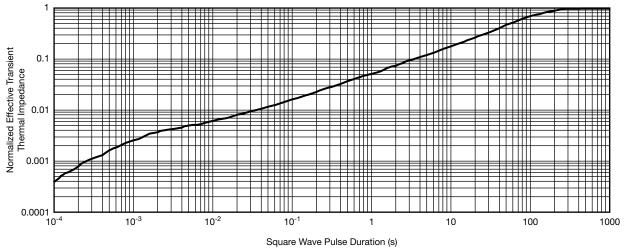
THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)





Safe Operating Area

Single Pulse Avalanche Current Capability vs. Time



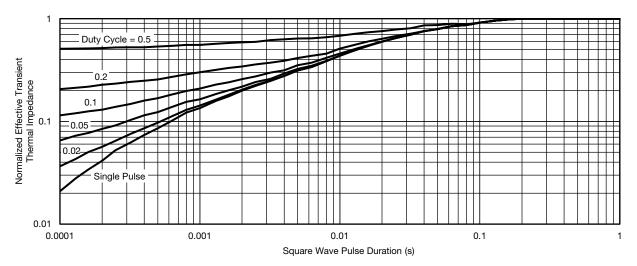
Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

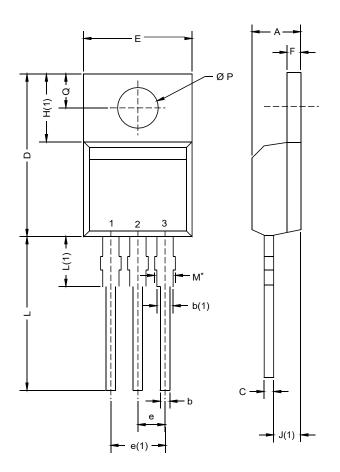
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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TO-220AB



	MILLIMETERS		INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, DWG: 5471					

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM

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