New Product



Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.43$ V at $I_F = 10$ A

TMBS® TO-3PW

PIN 1 O PIN 2 PIN 3 O CASE

PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 40 A				
V _{RRM}	100 V				
I _{FSM}	450 A				
E _{AS} at L = 180 mH	700 mJ				
V_F at $I_F = 40$ A	0.64 V				
T _J max.	150 °C				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

MECHANICAL DATA

Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free and RoHS compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	V80100PW	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	100	V		
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	80	A		
	per diode		40			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	450	А		
Non-repetitive avalanche energy at $T_J = 25$ °C, L = 180 m	E _{AS}	700	mJ			
Peak repetitive reverse current at $t_p = 2 \mu s$, 1 kHz, T _J = 38 °C ± 2 °C per diode				I _{RRM}	1.0	А
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	- 40 to + 150	°C		

RoHS COMPLIANT HALOGEN

V80100PW

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	l _R = 1.0 mA	T _A = 25 °C	V_{BR}	100 (minimum)	-	V	
Instantaneous forward voltage per diode	I _F = 10 A	T _A = 25 °C	V _F ⁽¹⁾	0.49	-	V	
	I _F = 20 A			0.59	-		
	I _F = 40 A			0.76	0.84		
	I _F = 10 A	T _J = 125 °C		0.43	-		
	I _F = 20 A			0.55	-		
	I _F = 40 A			0.64	0.76		
Reverse current per diode	V _R = 80 V	T _A = 25 °C	I _R ⁽²⁾	38	-	μA	
		T _A = 125 °C		17	-	mA	
	V _R = 100 V	T _A = 25 °C		85	1000	μA	
		T _A = 125 °C		33	76	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER		SYMBOL	V80100PW	UNIT			
Typical thermal resistance	per diode	- R _{θJC}	1.5	°C/W			
	per device		0.8	0/10			

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-3PW	V80100PW-M3/4W	4.5	4W	30/tube	Tube		

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

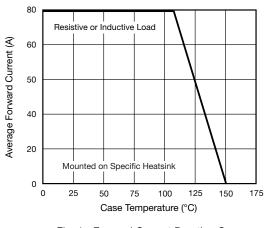
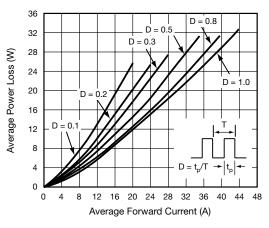
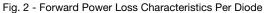


Fig. 1 - Forward Current Derating Curve





For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>



V80100PW

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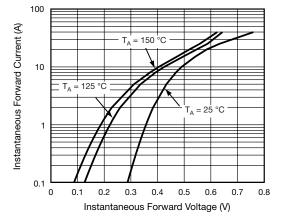


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

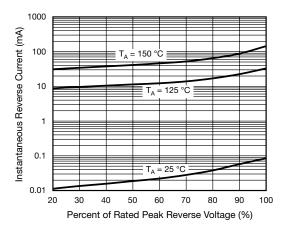
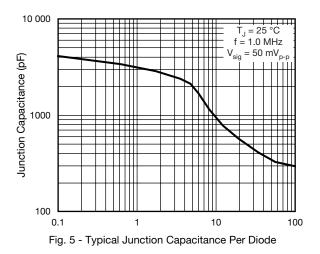


Fig. 4 - Typical Reverse Characteristics Per Diode



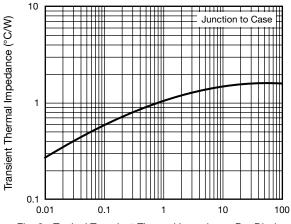
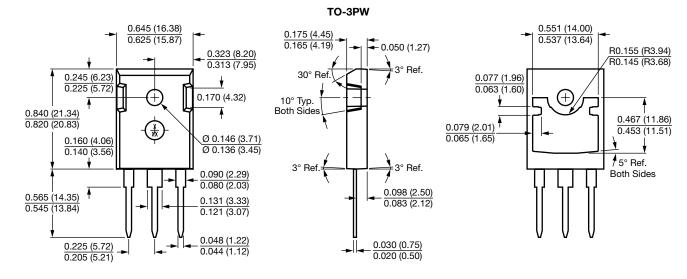


Fig. 6 - Typical Transient Thermal Impedance Per Diode



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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