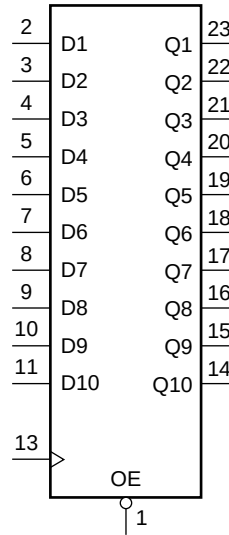
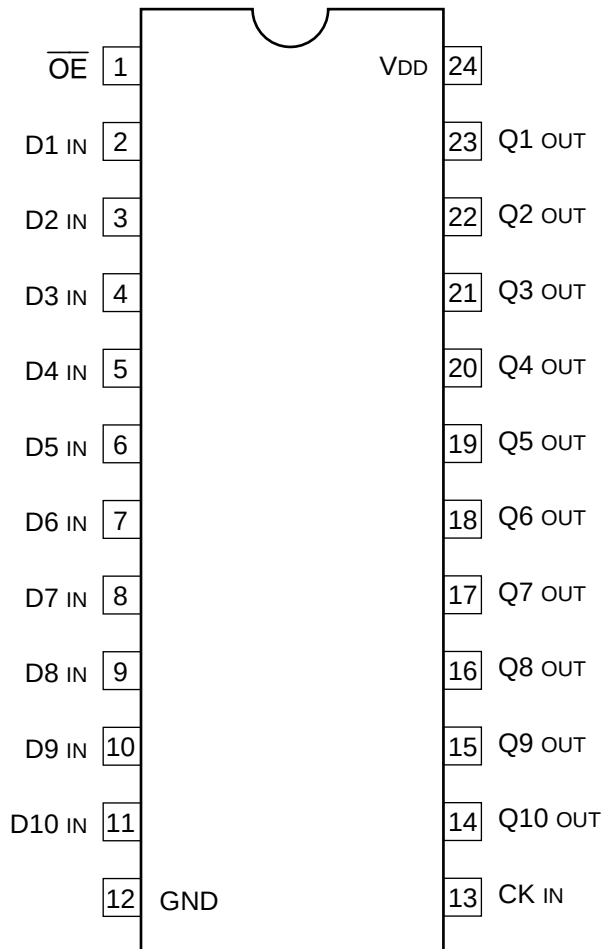


C-MOS 10-BIT BUS INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS —TOP VIEW—



FUNCTION TABLE (EACH FLIP-FLOP)

INPUTS			OUTPUTS
OE	CK	D	Q
0	$\uparrow$	1	1
0	$\uparrow$	0	0
0	0	x	Q ₀
1	x	x	HI-Z

0 : LOW LEVEL
 1 : HIGH LEVEL
 x : DON'T CARE
 HI-Z : HIGH IMPEDANCE

LOGIC DIAGRAM (POSITIVE LOGIC)

