

# **Ultra Low Cost 3-Pin Microprocessor Reset**

## **Description**

The V6340 monitors the supply voltage of any electronic system, and generates the appropriate Reset signal. The threshold must be chosen to the minimum allowed voltage which guarantees the good functionality of the system. As long as  $V_{\rm DD}$  stays upside this voltage level, the output stays inactive. If  $V_{\rm DD}$  drops below  $V_{\rm TH}$ , the output gets active. The threshold voltage may be obtained in different versions: 2.6V, 3.0V, 3.7V and 4.4V.

#### **Features**

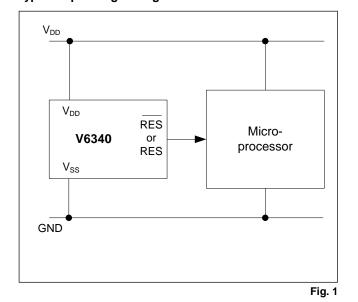
- SOT-23 package
- □ Reset output state guaranteed down to V<sub>DD</sub> = 1V @ 25°C
- □ Low supply current: stays stable during switching versions B, H, N: typ.  $19\mu$ A at  $V_{DD} = 5V$  other versions: typ.  $38\mu$ A at  $V_{DD} = 5V$
- ☐ High noise immunity
- No external components required
- Push-pull or Open drain output
- ☐ Pin compatible with MAX 809 in SOT-23, by appropriate layout on PCB
- TTL output compatibility

#### **Applications**

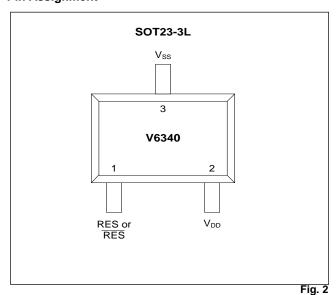
Applications needing a voltage detection:

- □ Computer electronics
- White / Brown goods
- Industrial electronics
- □ Telecom systems
- □ Hand-held systems

#### **Typical Operating Configuration**



#### Pin Assignment





#### **Absolute Maximum Ratings**

Parameter	Symbol	Conditions
Voltage at V <sub>DD</sub> to V <sub>SS</sub>	$V_{DD}$	-0.3V to +8V
Minimum voltage at RES or RES	V <sub>min</sub>	V <sub>SS</sub> – 0.3V
Maximum voltage at RES or RES	V <sub>max</sub>	V <sub>DD</sub> + 0.3V
Storage Temperature Range	T <sub>STO</sub>	-65°C to +150°C

Table 1

Stresses above these listed maximum ratings may cause permanent damages to the device. Exposure beyond specified operating conditions may affect device reliability or cause malfunction.

#### **Handling Procedures**

This device has built-in protection against high static voltages or electric fields; however, it is advised that normal precautions be taken as for any other CMOS component. Unless otherwise specified, proper operation can only occur when all terminal voltages are kept within the voltage range.

#### **Operating Conditions**

Parameter	Symbol	Min	Max	Unit
Operating Temperature 1)	TA	-40	+125	°C
Positive Supply Voltage 2)	$V_{DD}$	1	5.5	V

Table 2

#### **Electrical Characteristics**

 $T_A = +25$ °C, unless otherwise specified

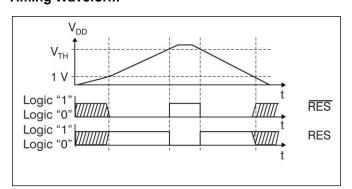
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Supply current	I <sub>DD</sub>	V <sub>DD</sub> = 5V, output open		38	50	μA
Threshold voltage	V <sub>TH</sub>	C, I, O	2.94	3.02	3.10	V
· ·	V <sub>TH</sub>	D, J, P	3.62	3.72	3.82	V
	$V_{TH}$	F, L, R	4.27	4.39	4.51	V
Threshold hysteresis	V <sub>HYS</sub>			5		mV
RES Output Low Level	V <sub>OL</sub>	$V_{DD} = 1.6V, I_{OL} = 1mA$		200	270	mV
•	Vol	$V_{DD} = 2.5V$ , $I_{OL} = 2mA$		195	250	mV
	$V_{OL}$	$V_{DD} = 3.5V, I_{OL} = 3mA$		198	250	mV
	Vol	$V_{DD} = 5V$ , $I_{OL} = 4mA$		185	250	mV
RES Output High Level	Vон	$V_{DD} = 1.6V, I_{OH} = -1mA$	1.25	1.36		V
	Vон	$V_{DD} = 2.5V$ , $I_{OH} = -1.5mA$	2.2	2.3		V
	V <sub>OH</sub>	$V_{DD} = 3.5V$ , $I_{OH} = -2.5mA$	3.15	3.27		V
	Vон	$V_{DD} = 5V$ , $I_{OH} = -3.5mA$	4.65	4.76		V
Output leakage current <sup>1)</sup>	II FAK	V <sub>DD</sub> = 5V		0.005	1	uА

Only for version B, H and N

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Supply current	I <sub>DD</sub>	V <sub>DD</sub> = 5V, output open		19	31	μA
Threshold voltage	$V_{TH}$	B, H, N	2.56	2.65	2.74	V
Threshold hysteresis	V <sub>HYS</sub>			32		mV

Table 3

# **Timing Waveform**



#### **Block Diagram**

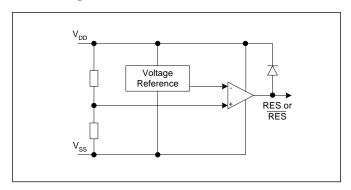


Fig.3 Fig.4

<sup>1)</sup>The maximum operating temperature is confirmed by sampling at initial device qualification. In production, all devices are tested at +25°C

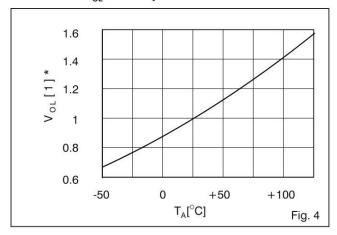
<sup>&</sup>lt;sup>2)</sup> V<sub>DD</sub> = 1V guaranteed at +25°C (see Fig. 14 for more information)

Only for Open drain versions

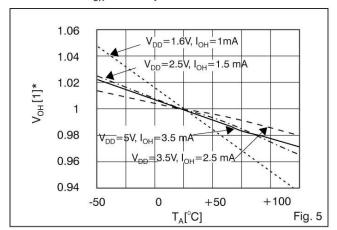


## **Typical Characteristics**

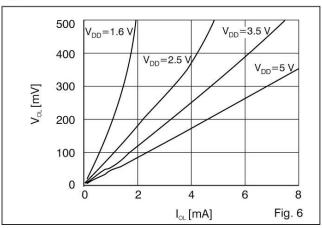
# Normalized $V_{\rm OL}$ vs. Temperature



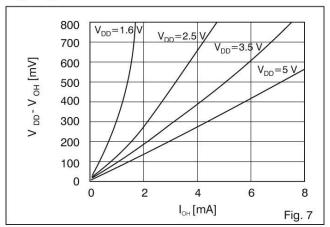
# Normalized V<sub>OH</sub> vs. Temperature



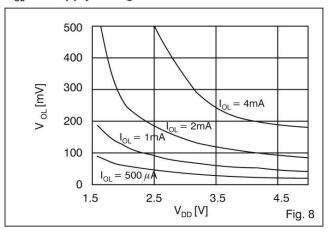
V<sub>OL</sub> vs. Output Current



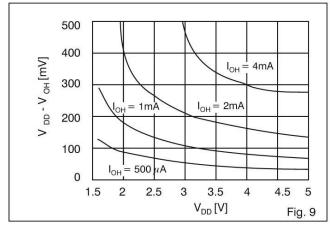
V<sub>DD</sub> - V<sub>OH</sub> vs. Output Current



V<sub>OL</sub> vs. Supply Voltage



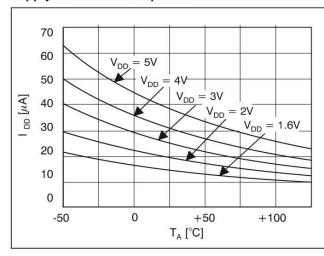
 ${
m V}_{
m DD}$  -  ${
m V}_{
m OH}$  vs. Supply Voltage

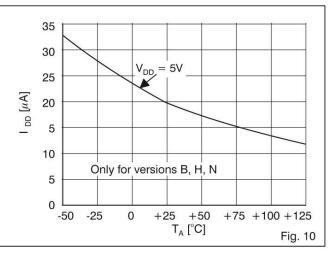


[1]\* Multiply value at +25°C by this factor to determine the value at temperature

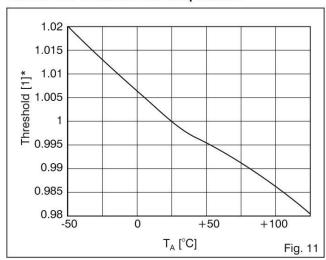


## Supply Current vs. Temperature

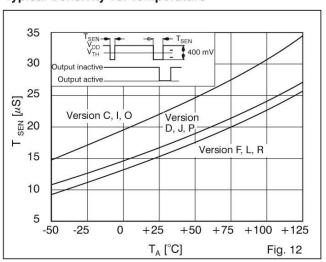




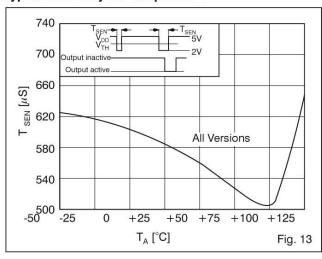
## Normalized Threshold vs. Temperature



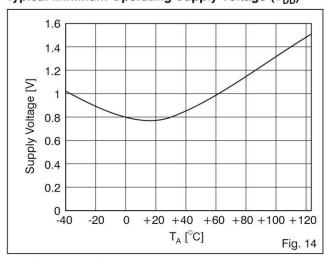
# Typical Sensivity vs. Temperature



## Typical Sensivity vs. Temperature



Typical Minimum Operating Supply Voltage (V<sub>DD</sub>)

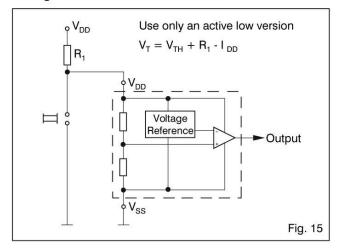


[1]\* Multiply value at  $+25^{\circ}$ C by this factor to determine the value at temperature

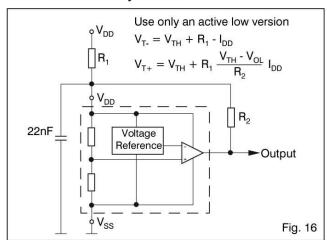


## **Typical Applications**

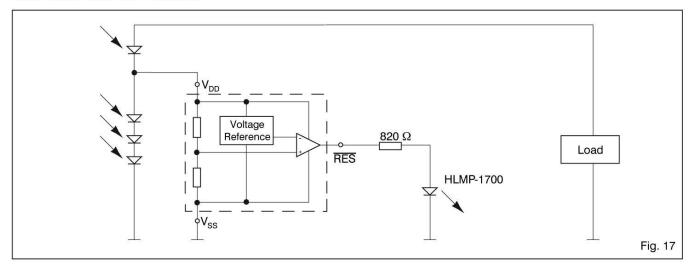
# **Voltage Monitor with Manual Reset**



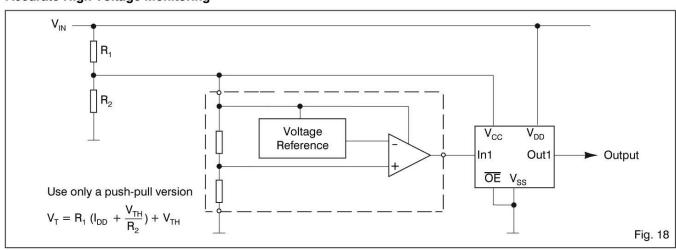
# **Reset Circuit with Hysteresis**



#### Solar Cell Power O.K. Indicator



# **Accurate High Voltage Monitoring**





# **Pin Description**

## SOT23-3L

Pin	Name	Function	
1	RES or RES	Reset output	
2	$V_{DD}$	Positive supply	
3	V <sub>SS</sub>	Supply ground	

Table 4

# **Packaging and Ordering Information**

# **Dimensions of SOT23-3L Package**

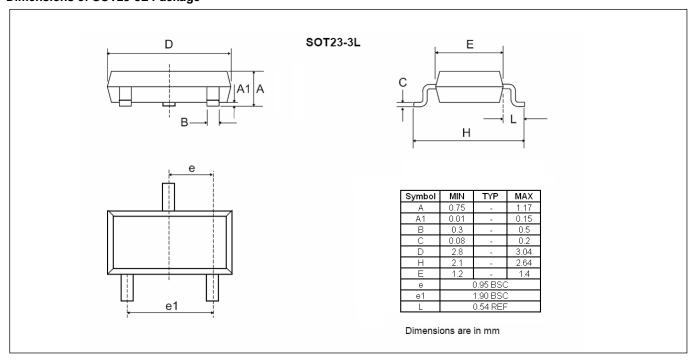
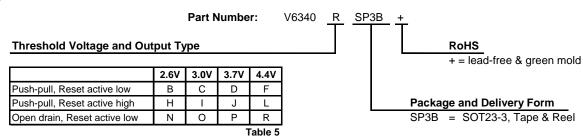


Fig. 5



#### **Ordering Information**



**Note:** Subject to availability (see standard versions list below). When ordering, please give complete Part Number without space between letters: eg. V6340RSP3B, etc.

#### Standard Versions (Top Marking)

#### Marking for SOT23-3 package

Part Number	Threshold Voltage	Output type	Package and Delivery Form	Top Marking <sup>1)</sup>
V6340BSP3B+	2.6V			E1##
V6340CSP3B+	3.0V			EC##
V6340DSP3B+	3.7V	Active low push-pull		BU##
V6340FSP3B+	4.4V		SOT23-3L, Tape & Reel	EA##
V6340LSP3B+	4.4V	Active high push-pull	3000 pcs	E8##
V6340OSP3B+	3.0V			EB##
V6340PSP3B+	3.7V	Active low open-drain		ED##
V6340RSP3B+	4.4V			E3##

<sup>1)</sup> Top marking is standard from 2006. No bottom marking exists. Where ## refers to the lot number (EM internal reference only)

#### Traceability for small packages

Due to the limited space on the package surface, the bottom marking contains a limited number of characters that provide only partial information for lot traceability. Full information for complete traceability is however provided on the packing labels of the product at delivery from EM: It is highly recommended that the customer insures full lot traceability of EM product in his final product.

#### **Standards Version (Samples)**

Part Number	
V6340BSP3B+	
V6340CSP3B+	
V6340DSP3B+	
V6340FSP3B+	

Part Number
V6340LSP3B+
V6340OSP3B+
V6340PSP3B+
V6340RSP3B+

Sample stock is generally held on **standard versions** only. Please contact factory for other versions not shown here and for availability of non standard versions.

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