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Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier



MicroSMP (DO-219AD)

____ Cathode Anode O

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DESIGN SUPPORT TOOLS



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 A			
V _{RRM}	45 V			
I _{FSM}	30 A			
V _F at I _F = 2 A (125 °C)	0.40 V			
T _J max.	150 °C			
Package	MicroSMP (DO-219AD)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 0.65 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop
- Low power loss, high efficiency
- Meets MSL level 1, J-STD-020, per LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications, in commercial, industrial, and automotive applications.

MECHANICAL DATA

Case: MicroSMP (DO-219AD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, and RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	YMBOL V2PL45		
Device marking code		2LE		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _{F(AV)}	2	А	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	30	А	
Operating junction and storage temperature range	T_{J} ⁽¹⁾ , T_{STG}	-40 to +150	°C	

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{0,JA}$





HALOGEN

FREE





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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST C	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 1.0 A	- T _A = 25 °C - T _A = 125 °C	- V _F ⁽¹⁾	0.43	-	V
	I _F = 2.0 A			0.48	0.56	
	I _F = 1.0 A			0.33	-	
	I _F = 2.0 A			0.40	0.48	
Reverse current per diode	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	0.35	mA
	v _R = 45 V	T _A = 125 °C		3	10	
Typical junction capacitance	4.0 V, 1 MHz		CJ	300	-	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER SYMBOL V2PL45			UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾⁽²⁾	130	°C/W	
	R _{0JM} ⁽³⁾	20		

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/P_{0JA}$

⁽²⁾ Free air, mounted on FR4 PCB, 2 oz. standard footprint, $R_{\theta JA}$ - junction to ambient

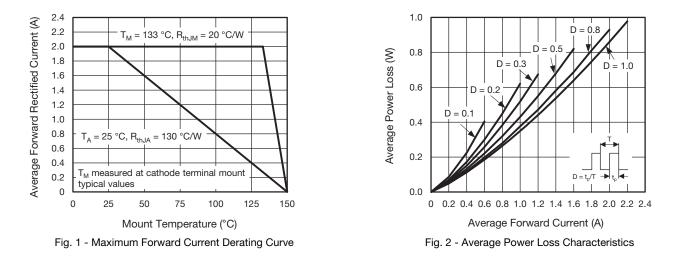
 $^{(3)}$ Mounted on PCB with 8.0 mm x 8.0 mm copper pad areas, $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTI		BASE QUANTITY	DELIVERY MODE		
V2PL45-M3/H	0.006	Н	4500	7" diameter plastic tape and reel	
V2PL45HM3/H ⁽¹⁾	0.006	Н	4500	7" diameter plastic tape and reel	

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



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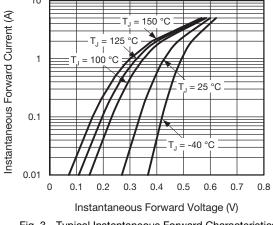
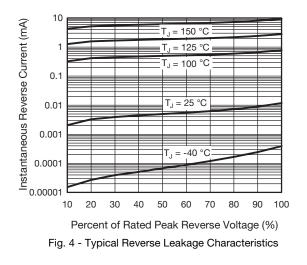


Fig. 3 - Typical Instantaneous Forward Characteristics



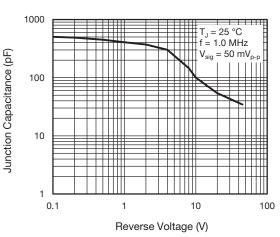


Fig. 5 - Typical Junction Capacitance

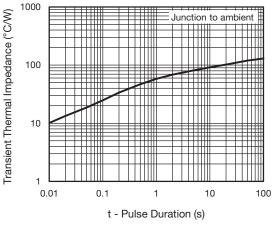
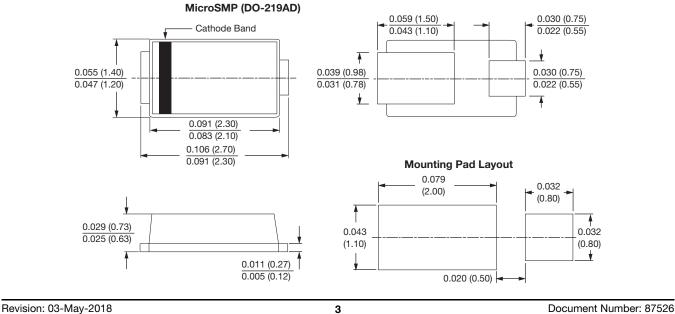


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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