



Approval

TFT LCD Approval Specification

MODEL NO.: V260B2 – P01

Customer:
approved by:
lote:

Approved By	TV Hea	d Division		
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REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver 2.0	Date Apr. 13,'09		All	Approval Specification was first issued.



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1. GENERAL DESCRIPTION

1.1 OVERVIEW

V260B2- P01 is a 26-inch TFT LCD cell with driver ICs and 1ch-LVDS interface. This module supports 1366 x 768 WXGA format and can display 16.7M colors (6-bit+Hi-FRC). The backlight unit is not built in

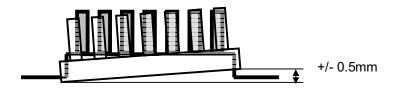
1.2 CHARACTERISTICS

CHARACTERISTICS ITEMS	SPECIFICATIONS
Screen Diagonal [in]	26.0
Pixels [lines]	1366×768
Active Area [mm]	575.769×323.712
Sub -Pixel Pitch [mm]	0.1405(H)×0.4215(V)
Pixel Arrangement	RGB vertical stripe
Weight [g]	TYP. 850
Physical Size [mm]	592(W) x 339.8(H) x 1.84(D) Typ.
Display Mode	TN, Normally White
Contrast Ratio	800:1 Typ.
	(Typical value measured at CMO's module)
Glass thickness (Array/CF) [mm]	0.7 / 0.7
Viewing Angle (CR>10)	+80/-80(H), +80/-70(V) Typ.
	(Typical value measured at CMO's module)
Color Chromaticity	R=0.653, 0.326
	G=0.272,0.587
	B=0.150,0.086
	W=0.311,0.320
	*Please refer to "color chromaticity" on p.16
Cell Transparency [%]	7.0%Typ.
	(Typical value measured at CMO's module)
Polarizer (CF side)	Anti-Glare coating,
	587.4(H) x 335.2(w). Hardness: 3H
Polarizer (TFT side)	587.4(H) x 335.2(w),
	Hardness: 3H

1.3 MECHANICAL SPECIFICATIONS

Item	Min.	Тур.	Max.	Unit	Note
Weight		850		g	
I/F connector mounting position	The mounting in	clination of the	connector makes		(1)
life confidenting position	the screen cente		(1)		

Note (1) Connector mounting position





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2. ABSOLUTE MAXIMUM RATINGS

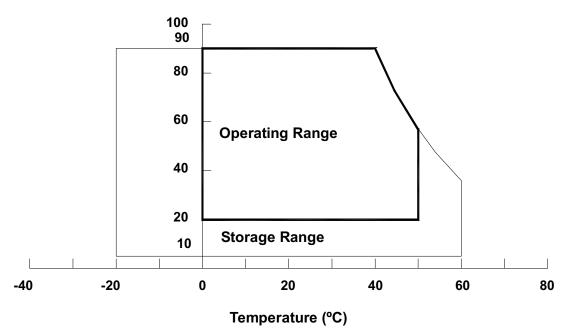
2.1 ABSOLUTE RATINGS OF ENVIRONMENT (BASED ON CMO MODULE V260B2-L01)

Item	Symbol	Va	lue	Unit	Note
item	Symbol	Min.	Max.	Offic	Note
Storage Temperature	T _{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T _{OP}	0	+50	°C	(1), (2)
Shock (Non-Operating)	S _{NOP}	_	50	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	_	1.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. (Ta \leq 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.
- Note (2) The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 65 °C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 65 °C. The range of operating temperature may degrade in case of improper thermal management in final product design.
- Note (3) 11 ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.
- Note (4) 10 ~ 500 Hz, 10 min, 1 time each X, Y, Z.
- Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.









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2.2 ABSOLUTE RATINGS OF ENVIRONMENT (OPEN CELL)

Storage Condition : With shipping package.

Storage temperature range : 25±5 $\,^{\circ}$ C Storage humidity range : 50±10%RH

Shelf life: a month

2.3 ELECTRICAL ABSOLUTE RATINGS

2.3.1 ELECTRICAL ABSOLUTE RATINGS (OPEN CELL)

Item	Svmbol	Value		Unit	Note	
item	Syllibol	Min.	Max.	Offic	Note	
Power Supply Voltage	Vcc	-0.3	13.0	V	(1)	
Input Signal Voltage	Vin	-0.3	3.6	V	(1)	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.



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3. ELECTRICAL CHARACTERISTICS

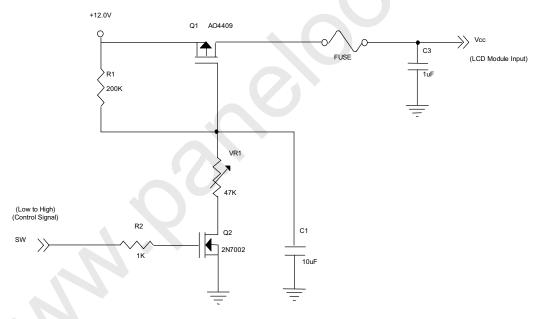
3.1 TFT LCD MODULE

Ta = 25 ± 2 °C

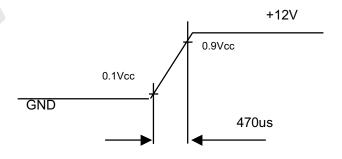
Parameter		Symbol	Value			Unit	Note	
		Symbol	Min.	Тур.	Max.	Offic	Note	
Power Su	pply Voltage		V_{CC}	11.4	12.0	12.6	V	(1)
Power Su	pply Ripple Vo	ltage	V_{RP}		_	300	mV	
Rush Cur	rent		I _{RUSH}	_	_	3.0	Α	(2)
		White		_	0.25	0.30	Α	
Power Su	pply Current	Black	Icc	_	0.35	0.40	Α	(3)
	Vertical Stripe			_	0.35	0.40	Α	
LVDS Differential Inpu Threshold Volta Differential Inpu Threshold Volta			V_{LVTH}	+100	_	_	mV	
			V_{LVTL}	_	_	-100	mV	>
	Common Input Voltage		V_{LVC}	1.125	1.25	1.375	>	
Terminating Re		esistor	R _T		100		ohm	
CMOS	CMOS Input High Threshold Voltage		V _{IH}	2.7	_	3.3	V	
interface	Input Low Thr	eshold Voltage	V _{IL}	0	_	0.7	V	

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



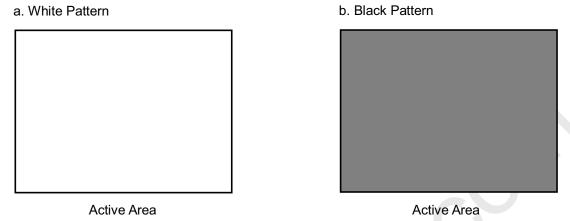
Vcc rising time is 470us

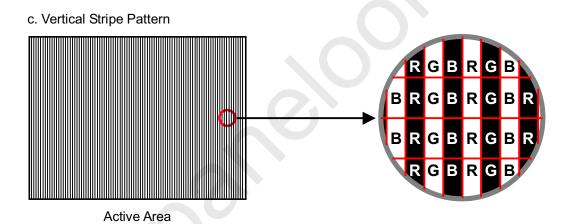




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Note (3) The specified power supply current is under the conditions at Vcc = 12 V, Ta = 25 \pm 2 °C, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.









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4. BLOCK DIAGRAM

4.1 TFT LCD MODULE

TFT LCD PANEL

(1366x3x768)

X BOARD



LVDS SIGNAL INPUT

Connector Part No.: FCI, 10041195-001 or compatible



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5. INTERFACE PIN CONNECTION

5.1 TFT LCD MODULE

CN1 Connector Pin Assignment

Pin No.	Symbol	Description	Note
1	VCC	Power supply: +12V	
2	VCC	Power supply: +12V	
3	VCC	Power supply: +12V	
4	VCC	Power supply: +12V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	SELLVDS	Select LVDS data format	(2)
10	NC	No connection	(3)
11	GND	Ground	
12	RX0-	Negative transmission data of pixel 0	
13	RX0+	Positive transmission data of pixel 0	
14	GND	Ground	
15	RX1-	Negative transmission data of pixel 1	
16	RX1+	Positive transmission data of pixel 1	
17	GND	Ground	
18	RX2-	Negative transmission data of pixel 2	
19	RX2+	Positive transmission data of pixel 2	
20	GND	Ground	
21	RXCLK-	Negative of clock	
22	RXCLK+	Positive of clock	
23	GND	Ground	
24	RX3-	Negative transmission data of pixel 3	
25	RX3+	Positive transmission data of pixel 3	
26	GND	Ground	
27	NC	No connection	(3)
28	NC	No connection	(3)
29	GND	Ground	
30	GND	Ground	

Note (1) Connector Part No.: FCI, 10041195-001 or compatible

Note (2) Ground or OPEN: Normal, High: JEIDA LVDS format

Please refer to 5.2 LVDS INTERFACE (Page 11)

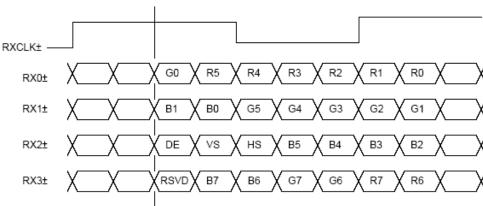
Note (3) Reserved for internal use. Please leave it open.



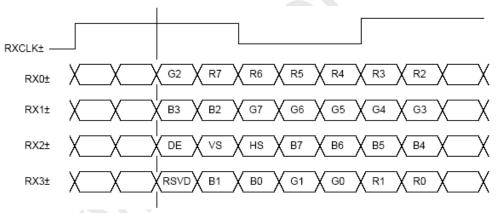
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5.2 LVDS INTERFACE

SELLVDS = L or Open (VESA)



SELLVDS = H (JEIDA)



R0~R7: Pixel R Data (7; MSB, 0; LSB) G0~G7: Pixel G Data (7; MSB, 0; LSB) B0~B7: Pixel B Data (7; MSB, 0; LSB)

DE: Data enable signal

Notes(1) RSVD(reserved)pins on the transmitter shall be "H" or "L".

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5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

						Data Signal																			
Color		Red				Green						Blue													
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	B4	ВЗ	B2	В1	В
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Scale	:	:	:	:	:	:	:	:	:	:	·	:	:	:	:	:	:	:	:	:	:	:	:	:	
Of Red	:	:	:	:	:	:	:	:	·	i.	:		:	:	:	:	:	:	:	:	:	:	:	:	
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
Scale	:	:		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
Of	:	<u>\:</u>	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
Green	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	
Green	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
Blue	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
JiuG	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note (1) 0: Low Level Voltage, 1: High Level Voltage



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6. INTERFACE TIMING

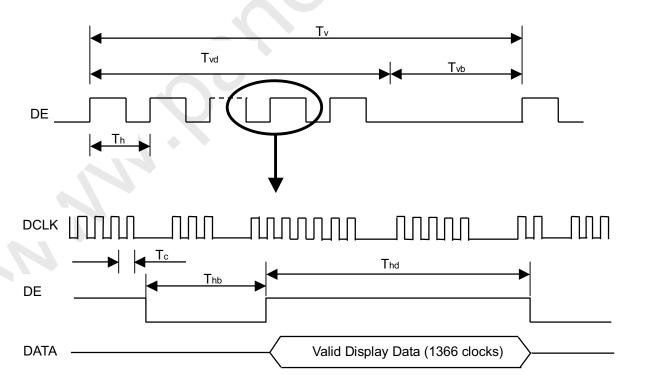
6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

			•		• •		
Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	1/Tc	60	76	82	MHz	
LVDS Receiver Clock	Input cycle to cycle jitter	Trcl	_	_	200	ps	
IVDS Bassiver Date	Setup Time	Tlvsu	600	_	_	ps	
LVDS Receiver Data	Hold Time	Tlvhd	600	_	_	ps	
	Frame Rate	Fr5	47	50	53	Hz	
	Taine Nate	Fr6	57	60	63	Hz	
Vertical Active Display Term	Total	Tv	778	806	888	Th	Tv=Tvd+Tvb
, .	Display	Tvd	768	768	768	Th	-
	Blank	Tvb	10	38	120	Th	-
	Total	Th	1442	1560	1936	Tc	Th=Thd+Thb
Horizontal Active Display Term	Display	Thd	1366	1366	1366	Tc	-
	Blank	Thb	76	194	570	Tc	_

Note (1) Since this module is operated in DE only mode, Hsync and Vsync input signals should be set to low logic level. Otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM

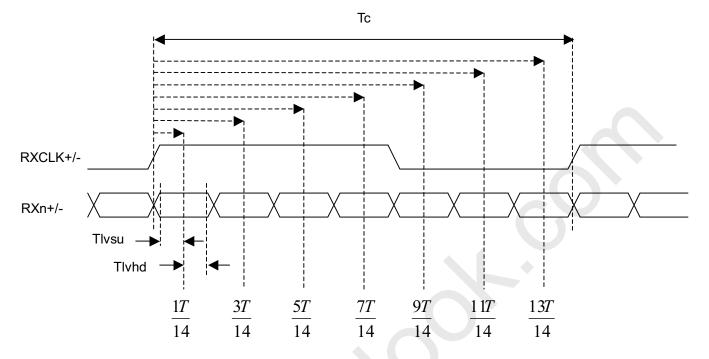






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LVDS RECEIVER INTERFACE TIMING DIAGRAM

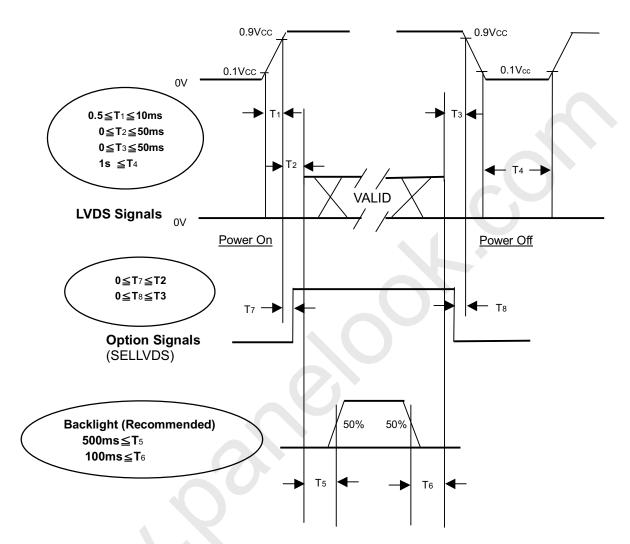




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6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

- Note (1) The supply voltage of the external system for the module input should follow the definition of Vcc.
- Note (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.
- Note (3) In case of Vcc is in off level, please keep the level of input signals on the low or high impedance. If T2<0,that maybe cause electrical overstress failure.
- Note (4) T4 should be measured after the module has been fully discharged between power off and on period.
- Note (5) Interface signal shall not be kept at high impedance when the power is on.





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7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit		
Ambient Temperature	Та	25±2	°C		
Ambient Humidity	На	50±10	%RH		
Supply Voltage	V_{CC}	12.0	V		
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTIC				
Lamp Current	Iμ	6.5 ± 0.5	mA		
Oscillating Frequency (Inverter)	F_W	58 ± 3	KHz		
Vertical Frame Rate	Fr	60	Hz		

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown as below. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (5).

Ite	Item Sy		Condition	Min.	Тур.	Max.	Unit	Note	
	Red	Rcx			0.653		1		
	Neu	Rcy			0.326		ı		
	Green	Gcx	θ _x =0°, θ _Y =0°		0.272		ı		
Color	Green	Gcy	Viewing Angle at Normal		0.587		-	(0),(5)	
Chromatici	ty Blue	Всх	Direction	-	0.150	_	ı	(0),(3)	
	Blue	Всу	Standard light source "C"		0.086		ı		
	White	Wcx			0.311		ı		
	vviile	Wcy			0.320		ı		
Center Transmittance		Т%	θ _x =0°, θ _Y =0°	-	7.0	-	%	(1),(7)	
Contrast Ratio CF		CR	with CMO module		800	-		(1),(3)	
Response Time		T _R	θ _x =0°, θ _Y =0°	-	1.3		ms	(4)	
		T _F	with CMO Module@60Hz	-	3.7		ms	(4)	
White Va	oriation	δW	$\theta_x=0^\circ, \ \theta_Y=0^\circ$			1.3	_	(1),(6)	
vviile v	anauon) O VV	with CMO module	-	1	1.5	1	(1),(0)	
	Horizontal	θ_{x} +			80				
Viewing		θ_{x} -	CR≥10		80		Deg.	(1),(2)	
Angle	Vertical	θ_{Y} +	With CMO module		80		Deg.	(1),(2)	
	vertical	θ _Y -			70				



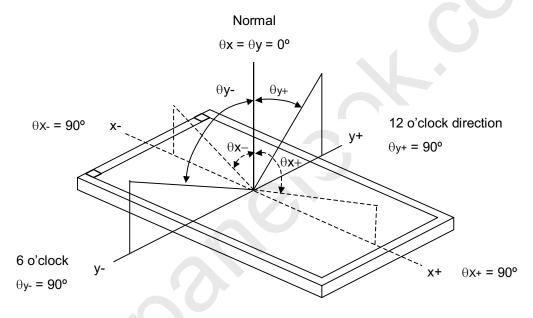
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- Note (0) Light source is the standard light source "C" which is defined by CIE and driving voltage are based on suitable gamma voltages. The calculating method is as following:
 - 1. Measure Module's and BLU's spectrum. White is without signal input and R,G,B are with signal input. BLU(for V260B2-L01) is supplied by CMO.
 - 2. Calculate cell's spectrum.
 - 3. Calculate cell's chromaticity by using the spectrum of standard light source "C".
- Note (1) Light source is the BLU which is supplied by CMO and driving voltage are based on suitable gamma voltages.
- Note (2) Definition of Viewing Angle (θx , θy):

Viewing angles are measured by EZ-Contrast 160R (Eldim)



Note (3) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

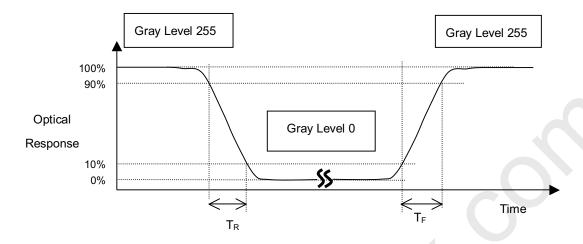
L 0: Luminance of gray level 0

CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (6).



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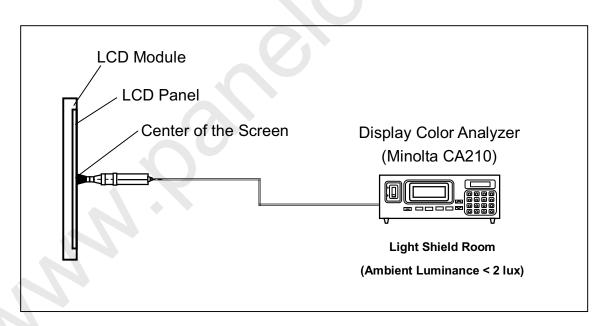
Note (4) Definition of Response Time (T_R, T_F) :



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Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 1 hour in a windless room.







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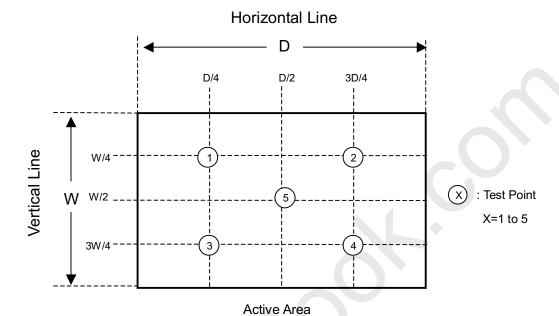
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Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 5 points

 $\delta W = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]$



Note (7) Definition of Transmittance (T%):

Module is without signal input.



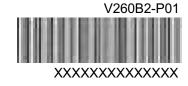


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8. DEFINITION OF LABELS

8.1 OPEN CELL LABEL

The barcode nameplate is pasted on each open cell as illustration for CMO internal control.





8.2 CARTON LABEL

The barcode nameplate is pasted on each box as illustration, and its definitions are as following explanation





(a) Model Name: V260B2– P01

(b) Carton ID: CMO internal control

(c) Quantities: 21



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9. PACKAGING

9.1 PACKING SPECIFICATIONS

(1) 21PCS LCD TV Panels / 1 Box

(2) Box dimensions: 812 (L) X 572 (W) X 277 (H)

(3) Weight: approximately 27.5 Kg

9.2 PACKING METHOD

Figures 9-1 and 9-2 are the packing method

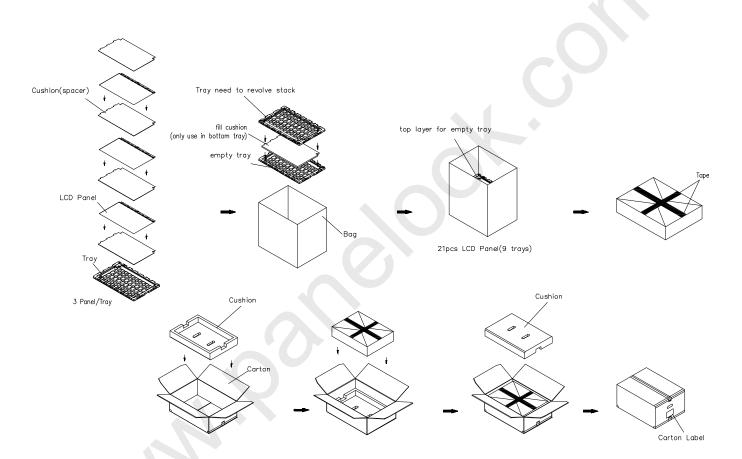
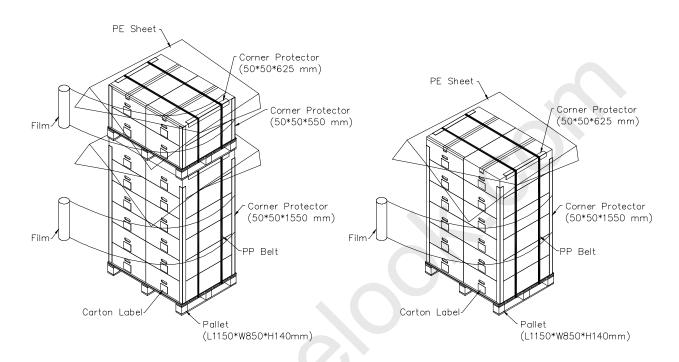


Figure.9-1 packing method



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Sea / Land Transportation (40ft HQ Container) Sea / Land Transportation (40ft Container)



Air Transportation

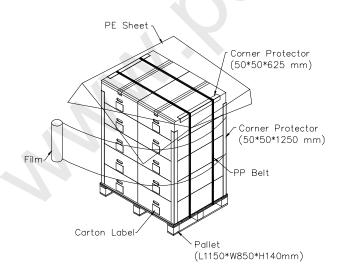


Figure.9-2 packing method

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10. PRECAUTIONS

10.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the product during assembly.
- (2) To assemble backlight or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel will be damaged.
- (4) Always follow the correct power sequence when the product is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (7) It is dangerous that moisture come into or contacted the product, because moisture may damage the product when it is operating.
- (8) High temperature or humidity may reduce the performance of module. Please store this product within the specified storage conditions.
- (9) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

10.2 SAFETY PRECAUTIONS

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the product's end of life, it is not harmful in case of normal operation and storage.





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11. REGULATORY STANDARDS

11.1 SAFETY

Regulatory	Item	Standard				
	UL	UL 60950-1: 2003				
Information Technology equipment	cUL	CAN/CSA C22.2 No.60950-1-03				
	СВ	IEC 60950-1:2001				
	UL	UL 60065: 2003				
Audio/Video Apparatus	cUL	CAN/CSA C22.2 No.60065-03				
	СВ	IEC 60065:2001				



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12. MECHANICAL CHARACTERISTICS

