Vishay General Semiconductor

# Dual High-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.52$  V at  $I_F = 5$  A



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### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	20 A			
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	150 A			
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.71 V			
T <sub>J</sub> max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Single			

### FEATURES

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test **Polarity:** as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V20DM120	UNIT		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	120	V		
Maximum DC forward rectified current (fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	20	A		
	I <sub>F(AV)</sub> <sup>(2)</sup>	5.5			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	150	А		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C		

#### Notes

<sup>(1)</sup> With infinite heatsink

<sup>(2)</sup> With recommended pad size, 2 oz FR4 PCB

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1

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RoHS

COMPLIANT

HALOGEN

V20DM120



V20DM120



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.62	-	V
	I <sub>F</sub> = 10 A			0.77	-	
	I <sub>F</sub> = 20 A			1.02	1.1	
	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.52	-	
	I <sub>F</sub> = 10 A			0.61	-	
	I <sub>F</sub> = 20 A			0.71	0.79	
Reverse current at rated V <sub>R</sub> per diode	V <sub>B</sub> = 90 V	$T_{A} = 25 \text{ °C}$	I <sub>R</sub> <sup>(2)</sup>	0.01	-	mA
	vR = 30 v	T <sub>A</sub> = 125 °C		3	-	
	$V_{\rm R} = 120 V$ —	T <sub>A</sub> = 25 °C		-	0.8	ШA
		T <sub>A</sub> = 125 °C		5	15	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1  $\,\%$  duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V20DM120	UNIT		
Typical thermal resistance	$R_{\theta JC}$	1.6	°C/W		
	R <sub>0JA</sub> (1)(2)	48			

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$  - junction-to-mount

<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N UNIT WEIGHT (g) PACKAGE CODE		BASE QUANTITY	DELIVERY MODE			
V20DM120-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20DM120HM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

Note

<sup>(1)</sup> AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

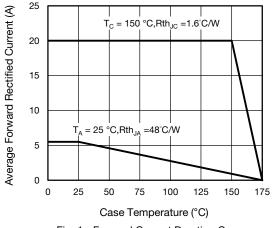


Fig. 1 - Forward Current Derating Curve

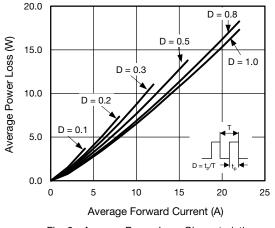


Fig. 2 - Average Power Loss Characteristics

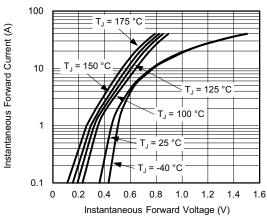
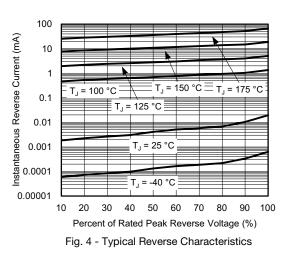
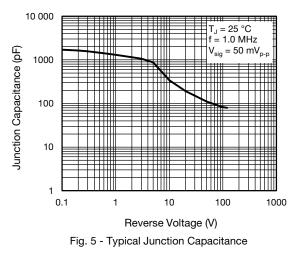


Fig. 3 - Typical Instantaneous Forward Characteristics





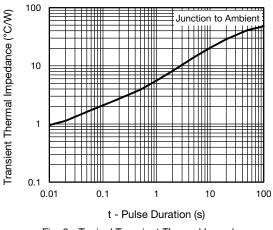


Fig. 6 - Typical Transient Thermal Impedance

Revision: 27-Mar-2020

3

Document Number: 87601

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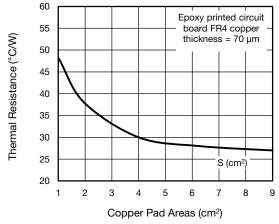
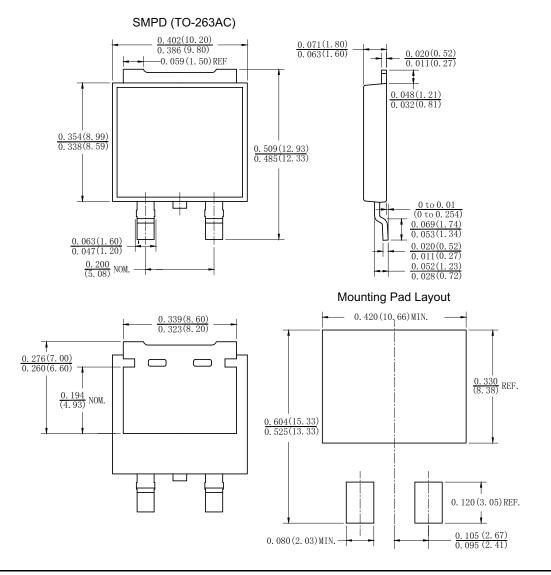


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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 Revision: 27-Mar-2020
 4
 Document Number: 87601

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