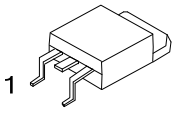
UTG7N65-S

Preliminary ***Insulated Gate Bipolar Transistor***

650V TRENCH GATE FIELD-STOP IGBT



TO-252

■ DESCRIPTION

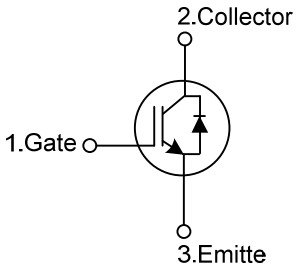
The UTC **UTG7N65-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG7N65-S** is suitable for the resonant or soft switching applications.

■ FEATURES

* High switching speed
* High avalanche ruggedness
* Low saturation voltage: $V_{CE(SAT).Typ.}$=1.5V @ I_C=7.0A, V_{GE}=15V (T_C =25°C)

■ SYMBOL



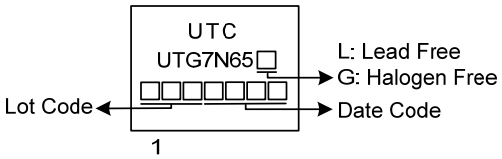
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG7N65L-TN3-R	UTG7N65G-TN3-R	TO-252	G	C	E	Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG7N65G-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free L: Lead Free

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	650	V
Gate-Emitter Voltage		V_{GES}	±20	V
Transient Gate-emitter voltage (t_P < 5 ms)			±25	V
Continuous Collector Current	T_C=25°C	I_C	14	A
	T_C=100°C		7	A
Collector Current Pulsed (Note 1)		I_{CM}	28	A
Diode Forward Current	T_C=25°C	I_F	14	A
	T_C=100°C		7	A
Short Circuit Withstand Time V_{GE} = 15V, V_{CC} ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥1.0s T_{VJ}= 25°C		t_{SC}	3	μs
Power Dissipation (T_C=25°C)		P_D	40	W
Operating Junction Temperature		T_J	-40 ~ +175	°C
Storage Temperature Range		T_{STG}	-55 ~ +175	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	3.125	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV_{CES}			650			V
Collector Cut-Off Current	I_{CES}	V_{CE}=650V, V_{GE}=0V				5	μA
G-E Leakage Current	I_{GES}	V_{CE}=0V, V_{GE}=±20V				±100	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I_C=250μA, V_{CE}=V_{GE}		4.0		6.5	V
Collector to Emitter Saturation Voltage	$V_{CE(SAT)}$	I_C=7.0A, V_{GE}=15V	T_C=25°C		1.5	2.1	V
			T_C=125°C		1.9		V
Dynamic Characteristics							
Input Capacitance	C_{IES}	V_{CE}=25V, V_{GE}=0V, f=1MHz			694		pF
Output Capacitance	C_{OES}				41.2		pF
Reverse Transfer Capacitance	C_{RES}				12.5		pF
Switching Characteristics							
Total Gate Charge	Q_G	V_{CE}=520V, I_C=7.0A, V_{GE}=15V			53.7		nC
Gate-Emitter Charge	Q_{GE}				15		nC
Gate-Collector Charge	Q_{GC}				23.9		nC
Turn-On Delay Time	$t_{DON)}$	V_{CC}=650V, I_C=7.0A, R_G=5Ω, V_{GE}=0~15V, L=1000μH			16.9		ns
Rise Time	t_R				27.5		ns
Turn-Off Delay Time	$t_{DOFF)}$				73.5		ns
Fall Time	t_F				209		ns
Turn-On Switching Loss	E_{ON}				0.254		mJ
Turn-Off Switching Loss	E_{OFF}				0.226		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Forward Voltage Drop	V_F	I_F=7.0A			1.67	3.0	V
Reverse Recovery Time	t_{rr}	I_F=7.0A, dI/dt=100A/μS, V_{CC}=400V			43.3		ns
Reverse Recovery Charge	Q_{rr}				10.6		nC

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